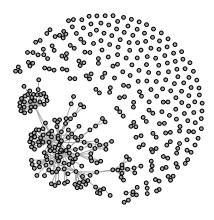
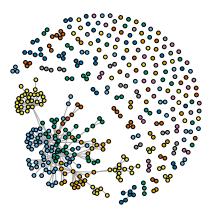
**Base Network** 

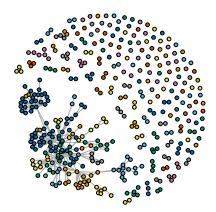
**Edge Betweenness** 

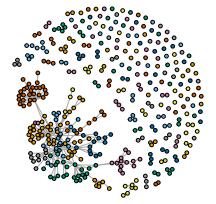




**Propogating Labels** 

**Greedy Optimization** of Modularity





Note: Using Fruchterman Reingold layout for consistent node placement.