EE671 Assignment4
16-Bit Multiply And Acummulator

G Kamalesh
20D070029

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1 Student Details

My roll number ends with 29. So, the value of nn is 29.

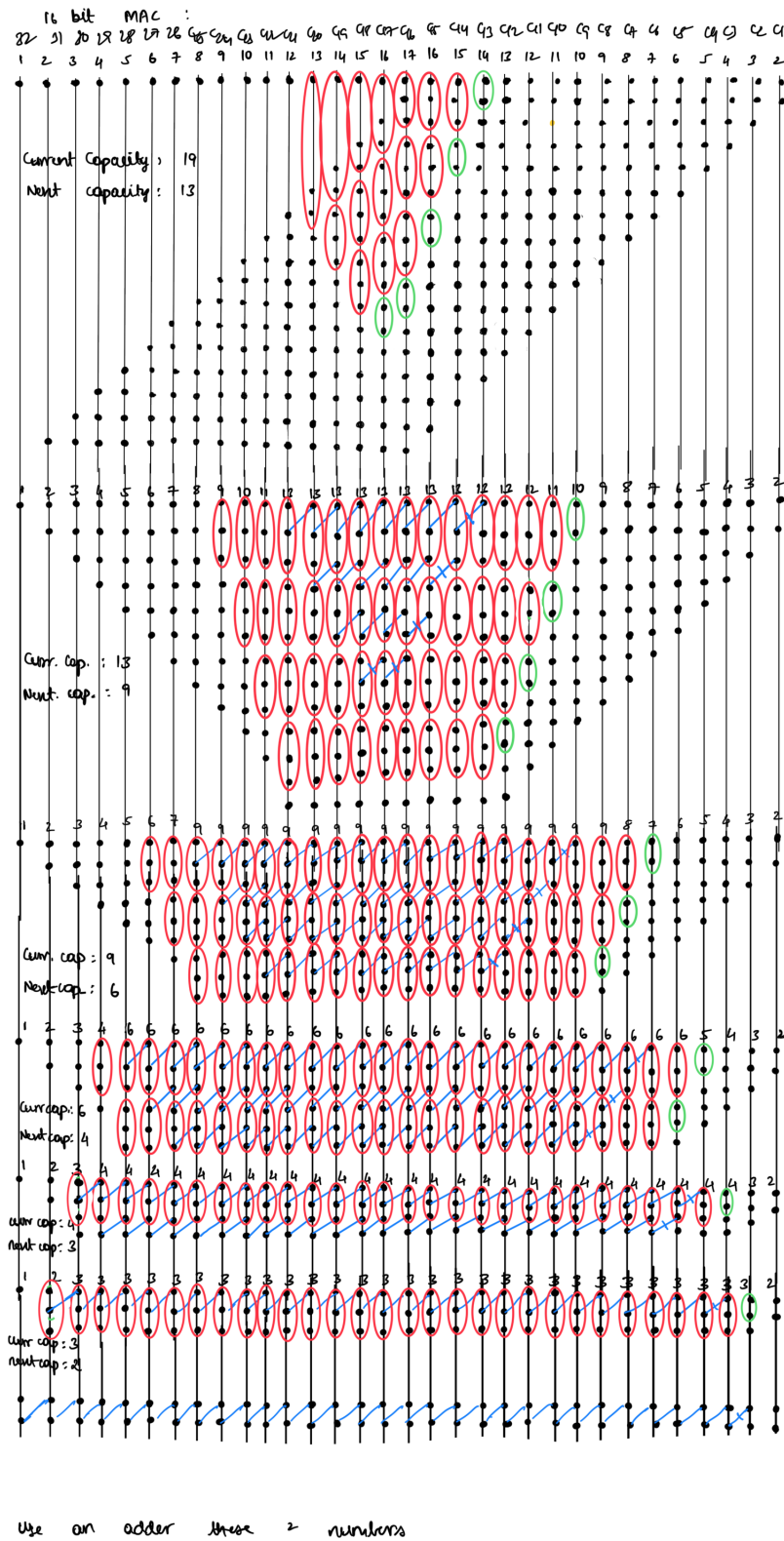| Function	Delay(in ps)
AND	329
A+B.C	429
XOR	658
A.B+C.(A+B)	658

Table 1:

2 Architecture

Figure 1: 32-bit MAC architecture

3 Netlist
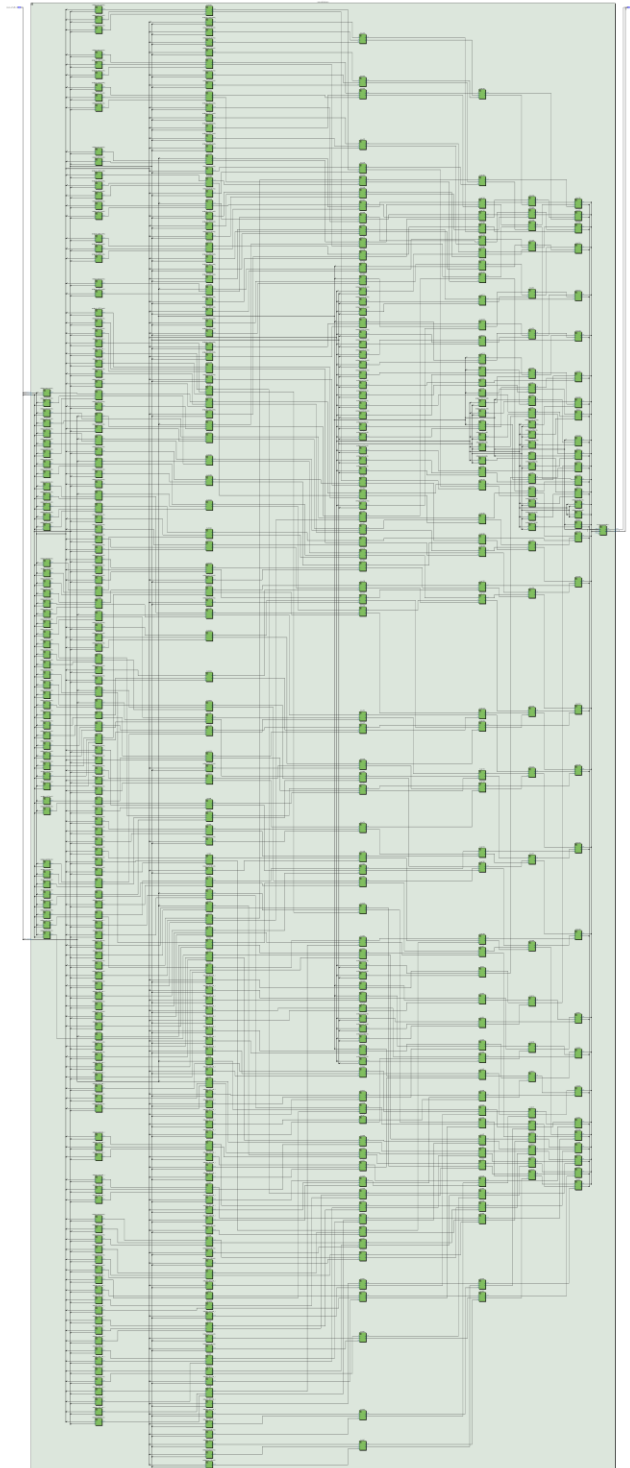

Figure 2: Generated Netlist

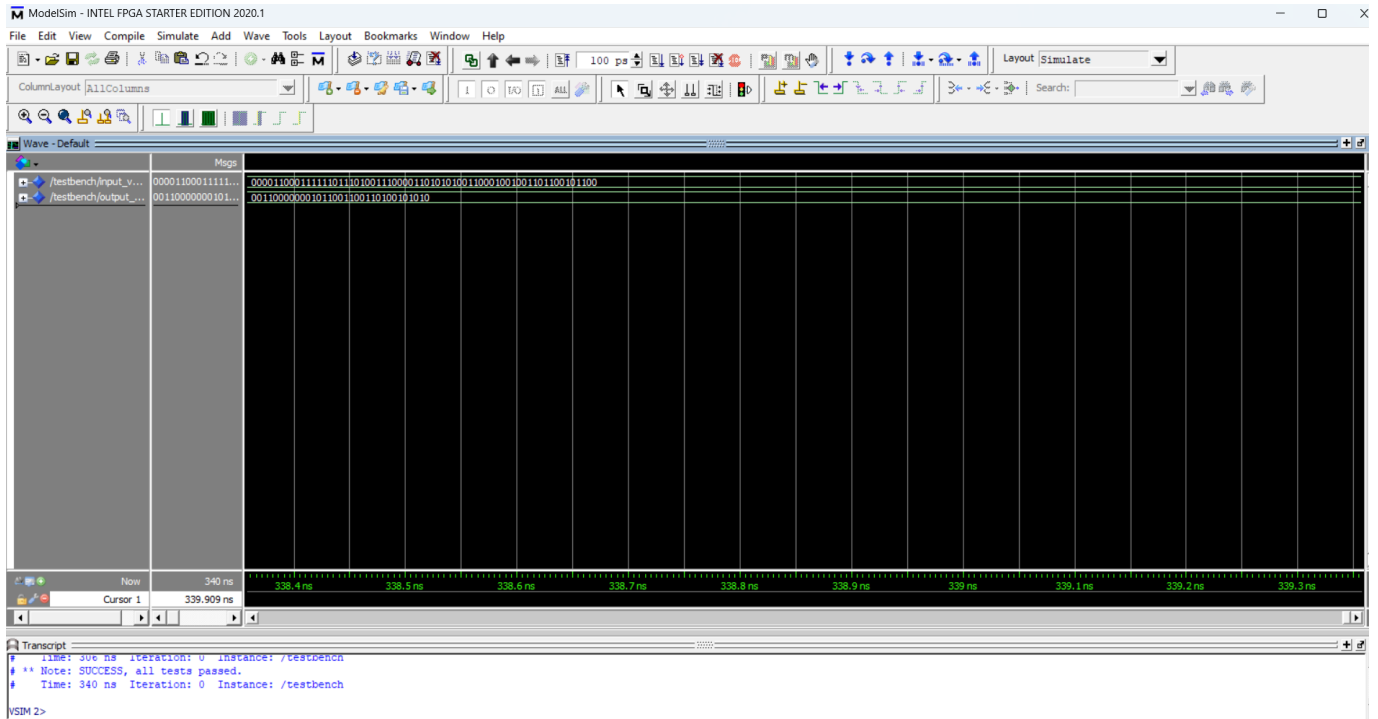# 4   Register Transfer Level Simulation
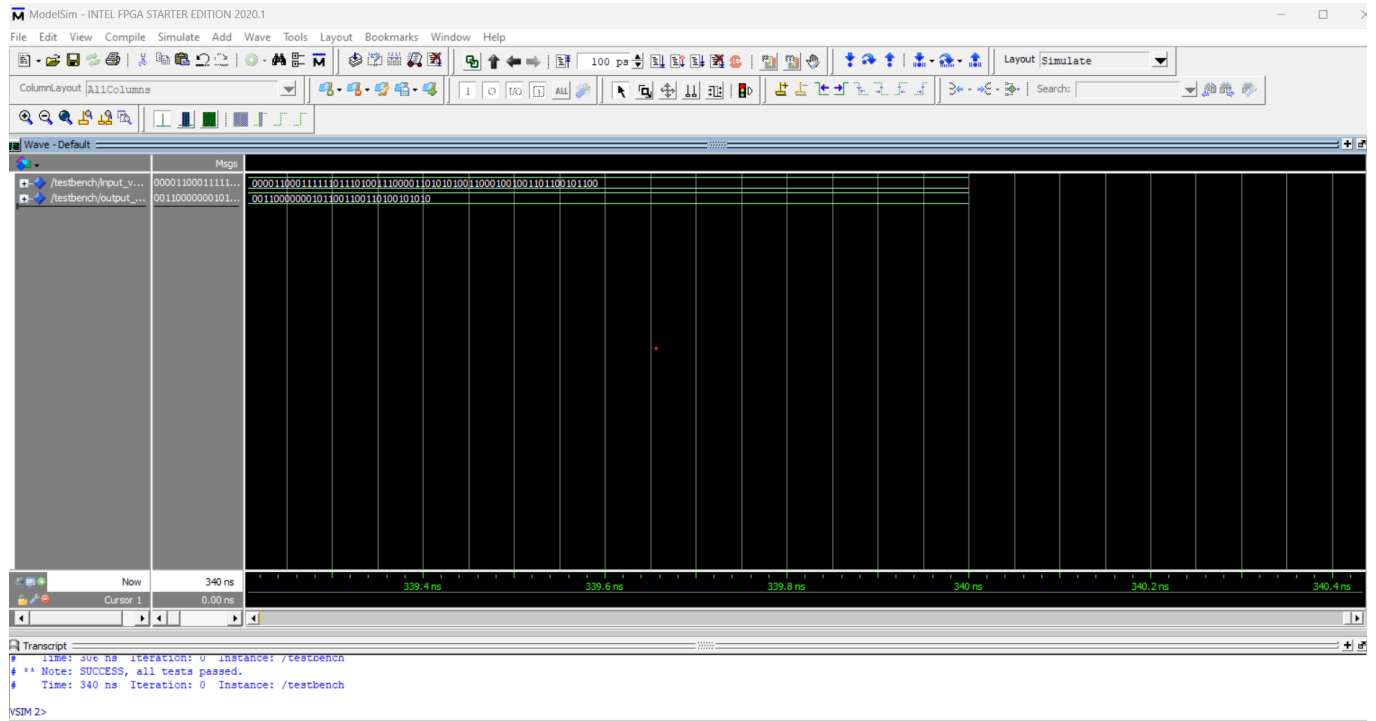

Figure 3: Register Transfer level view

5   Gate level Simulation

Figure 4: Gate Level Simulation