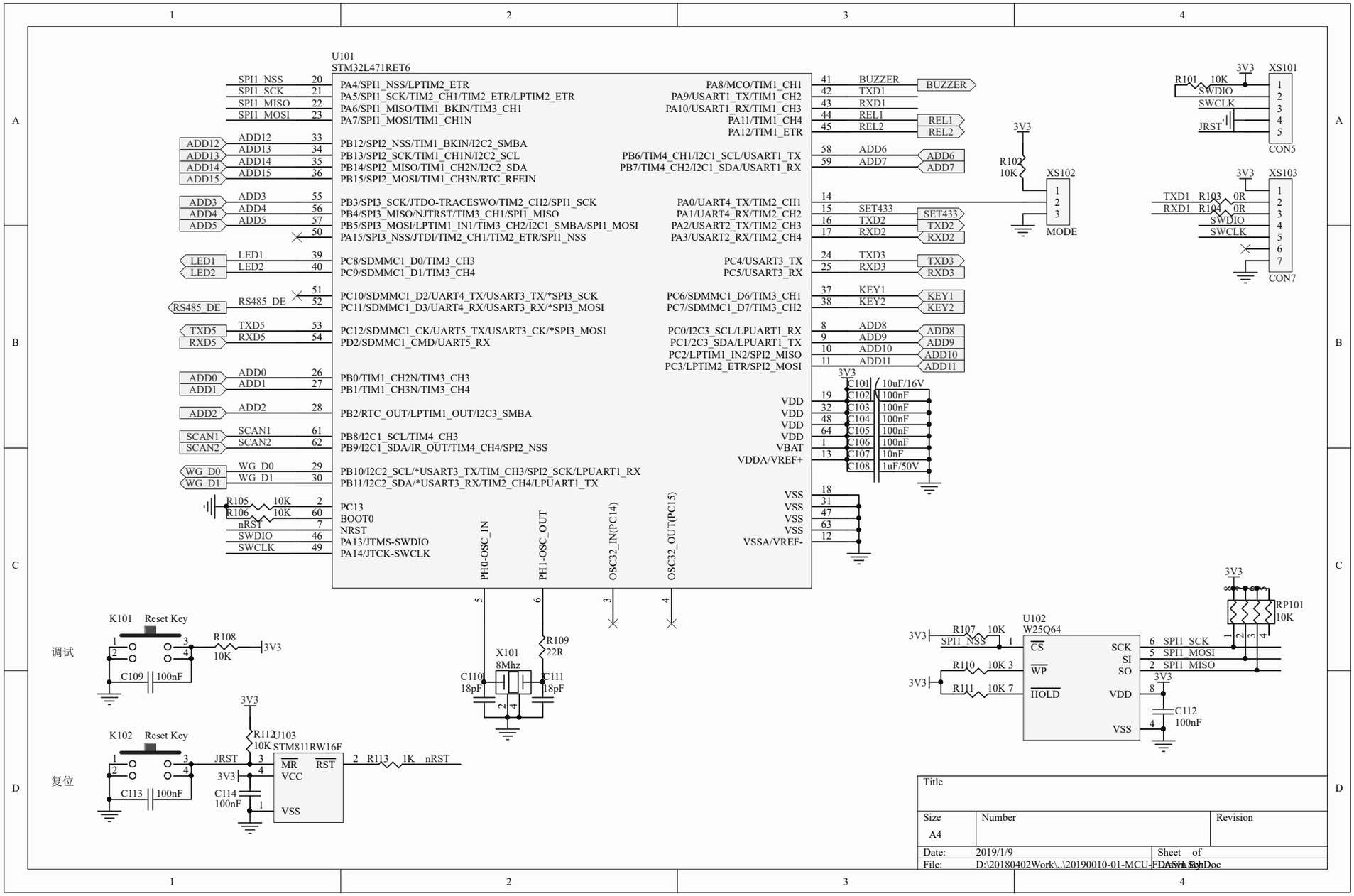
U101
STM32L471RET6
PA4/SPI1_NSS/LPTIM2_ETR
PA5/SPI1_SCK/TIM2_CH1/TIM2_ETR/LPTIM2_ETR
PA6/SPI1_MISO/TIM1_BKIN/TIM3_CH1
PA7/SPI1_MOSI/TIM1_CH1N
PB12/SPI2_NSS/TIM1_BKIN/I2C2_SMBA
PB13/SPI2_SCK/TIM1_CH1N/I2C2_SCL
PB14/SPI2_MISO/TIM1_CH2N/I2C2_SDA
PB15/SPI2_MOSI/TIM1_CH3N/RTC_REEIN
PB3/SPI3_SCK/JTDO-TRACESWO/TIM2_CH2/SPI1_SCK
PB4/SPI3_MISO/NJTRST/TIM3_CH1/SPI1_MISO
PB5/SPI3_MOSI/LPTIM1_IN1/TIM3_CH2/I2C1_SMBA/SPI1_MOSI
PA15/SPI3_NSS/JTDI/TIM2_CH1/TIM2_ETR/SPI1_NSS
PC8/SDMMC1_D0/TIM3_CH3
PC9/SDMMC1_D1/TIM3_CH4
PC10/SDMMC1_D2/UART4_TX/USART3_TX/*SPI3_SCK
PC11/SDMMC1_D3/UART4_RX/USART3_RX/*SPI3_MOSI
PC12/SDMMC1_CK/UART5_TX/USART3_CK/*SPI3_MOSI
PD2/SDMMC1_CMD/UART5_RX
PB0/TIM1_CH2N/TIM3_CH3
PB1/TIM1_CH3N/TIM3_CH4
PB2/RTC_OUT/LPTIM1_OUT/I2C3_SMBA
PB8/I2C1_SCL/TIM4_CH3
PB9/I2C1_SDA/IR_OUT/TIM4_CH4/SPI2_NSS
PB10/I2C2_SCL/*USART3_TX/TIM_CH3/SPI2_SCK/LPUART1_RX
PB11/I2C2_SDA/*USART3_RX/TIM2_CH4/LPUART1_TX
PC13
BOOT0
NRST
PA13/JTMS-SWDIO
PA14/JTCK-SWCLK
PA8/MCO/TIM1_CH1
PA9/USART1_TX/TIM1_CH2
PA10/USART1_RX/TIM1_CH3
PA11/TIM1_CH4
PA12/TIM1_ETR
PB6/TIM4_CH1/I2C1_SCL/USART1_TX
PB7/TIM4_CH2/I2C1_SDA/USART1_RX
PA0/UART4_TX/TIM2_CH1
PA1/UART4_RX/TIM2_CH2
PA2/USART2_TX/TIM2_CH3
PA3/USART2_RX/TIM2_CH4
PC4/USART3_TX
PC5/USART3_RX
PC6/SDMMC1_D6/TIM3_CH1
PC7/SDMMC1_D7/TIM3_CH2
PC0/I2C3_SCL/LPUART1_RX
PC1/2C3_SDA/LPUART1_TX
PC2/LPTIM1_IN2/SPI2_MISO
PC3/LPTIM2_ETR/SPI2_MOSI
VDD
VBAT
VDDA/VREF+
VSS
VSSA/VREF-
PH0-OSC_IN
PH1-OSC_OUT
OSC32_IN(PC14)
OSC32_OUT(PC15)
SPI1_NSS
SPI1_SCK
SPI1_MISO
SPI1_MOSI
ADD12
ADD13
ADD14
ADD15
ADD3
ADD4
ADD5
LED1
LED2
RS485_DE
TXD5
RXD5
ADD0
ADD1
ADD2
SCAN1
SCAN2
WG_D0
WG_D1
R105 10K
R106 10K
nRST
SWDIO
SWCLK
BUZZER
TXD1
RXD1
REL1
REL2
ADD6
ADD7
SET433
TXD2
RXD2
TXD3
RXD3
KEY1
KEY2
ADD8
ADD9
ADD10
ADD11
3V3
C101 10uF/16V
C102 100nF
C103 100nF
C104 100nF
C105 100nF
C106 100nF
C107 10nF
C108 1uF/50V
R102 10K
XS102
MODE
R101 10K
XS101
CON5
R103 0R
R104 0R
XS103
CON7
K101 Reset Key
调试
R108 10K
C109 100nF
K102 Reset Key
复位
C113 100nF
R112 10K
U103 STM811RW16F
MR
VCC
VSS
RST
JRST
C114 100nF
R113 1K
nRST
X101 8Mhz
C110 18pF
C111 18pF
R109 22R
U102 W25Q64
CS
WP
HOLD
SCK
SI
SO
VDD
VSS
R107 10K
R110 10K
R111 10K
SPI1_SCK
SPI1_MOSI
SPI1_MISO
RP101 10K
C112 100nF
Title
Size A4
Number
Revision
Date: 2019/1/9
Sheet of
File: D:\20180402Work\..\20190010-01-MCU-FDASH.SchDoc
Drawn By: