

An Extensible Architecture for Hyperdimensional Computing

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Index Terms—component, formatting, style, styling, insert

I. INTRODUCTION

Hyperdimensional computing is an emerging a

A. The Problem

While many previous works have implemented HDC accelerators [CITE THESE], they have all been restricted to a single encoding, mainly binary. The goal of this

II. PROPOSED ARCHITECTURE

A. Kernel-Level Architecture

B. System Architecture

C. AXI Interface