

Notes:

1. ANSI/IEEE 802.3 PHY configured for 100 Mbps full duplex operation with no negotiation.
2. Remove R18 to enable all modes with auto-negotiation.
3. U7 internal 1.2V regulator enabled.

Ethernet and RS-485 I/O

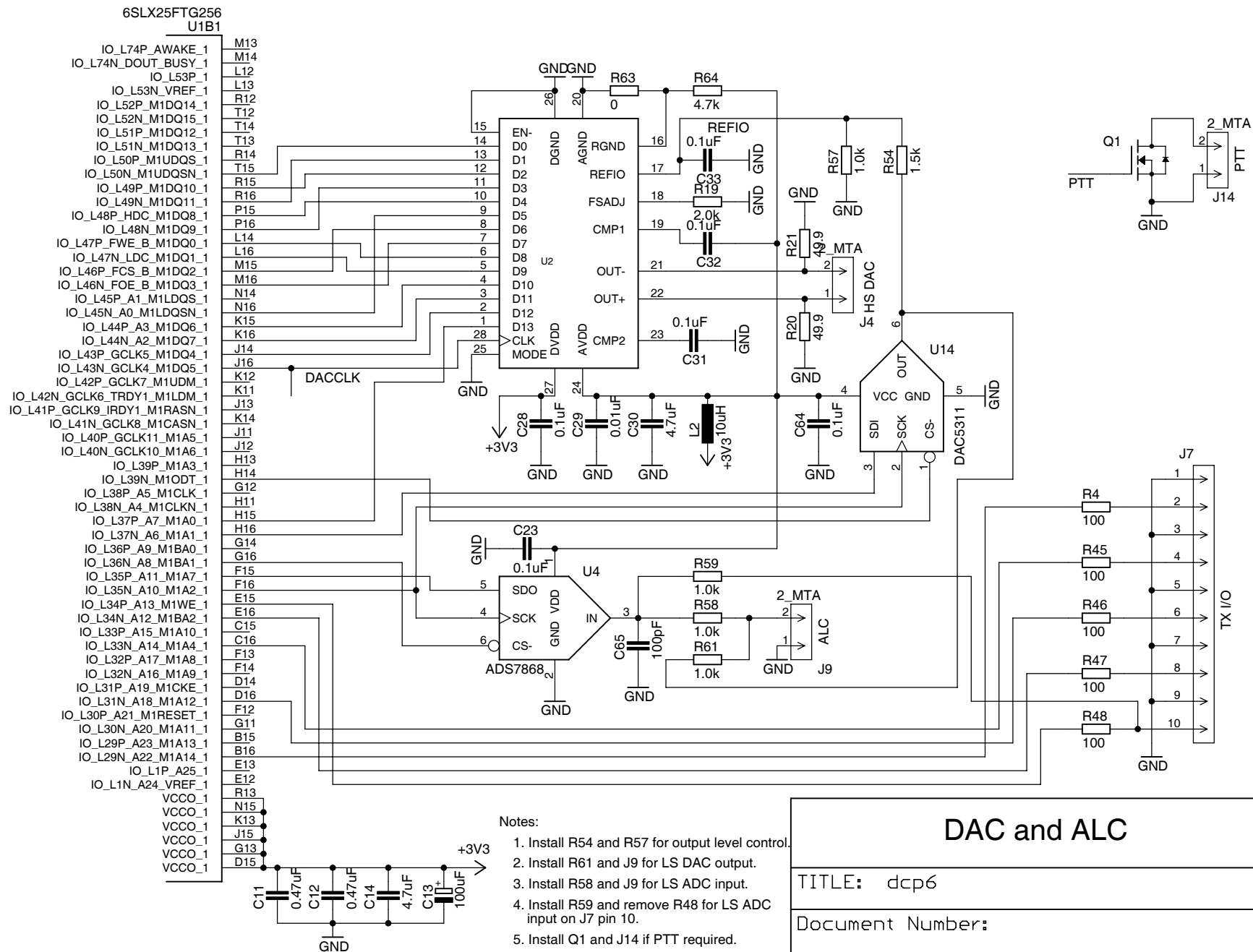
TITLE: dcp6

Document Number:

REV:

Date: 6/7/2012 18:59:55 UTC

Sheet: 1/6



DAC and ALC

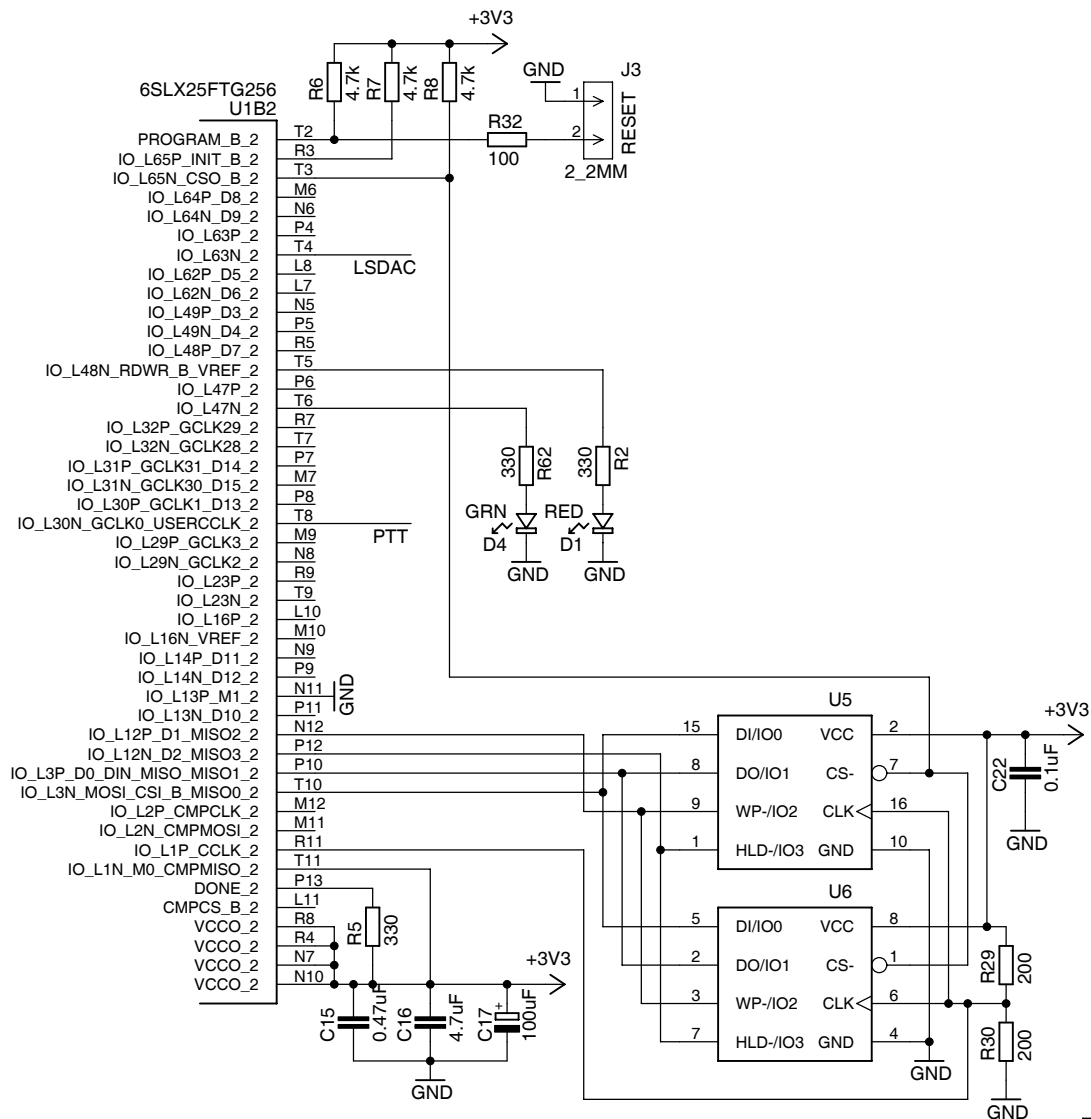
TITLE: dcp6

Document Number:

REV:

Date: 6/7/2012 18:59:55 UTC

Sheet: 2/6



Notes:

1. Mode bits set to 01 to select master SPI.
2. Atmel AT45DB (B, C and D); Numonyx M25P, M25PE, M45PE and N25Q; Spansion S25FL032P, S25FL064P and S25FL129P and Winbond W25Q (V and BV) supported.
3. Install 16-pin flash at U5 or 8-pin flash at U6.

Flash Memory

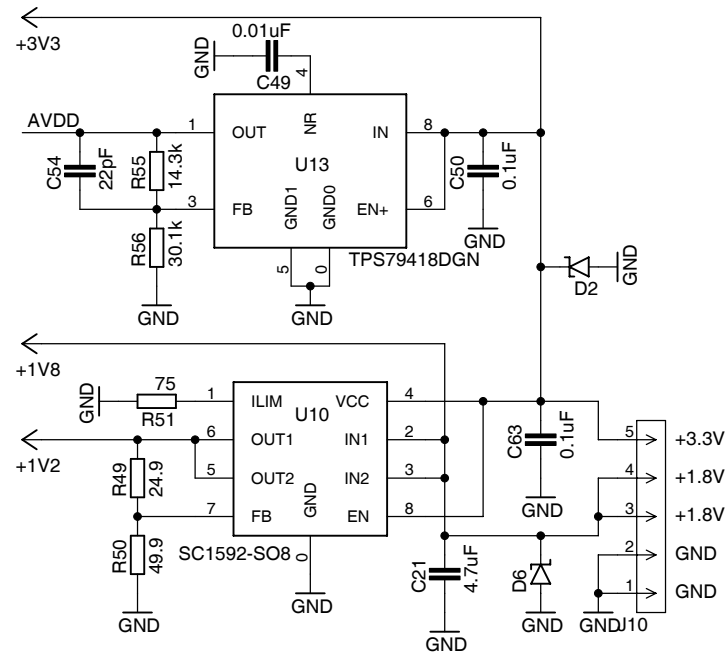
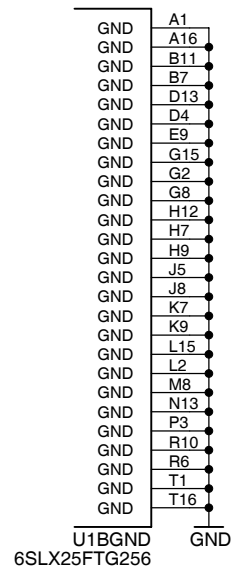
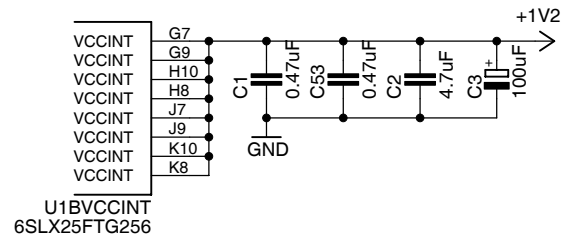
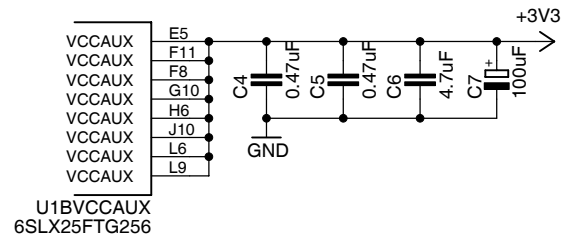
TITLE: dcp6

Document Number:

REV:

Date: 6/7/2012 18:59:55 UTC

Sheet: 3/6



Notes:

1. Install U13, C49, C50 and C54 if ADC power derived from 3.3V input.
2. Install R55 and R56 only if TPS79401 substituted for TPS79418.

Power

TITLE: dcp6

Document Number:

REV:

Date: 6/7/2012 18:59:55 UTC

Sheet: 5/6

