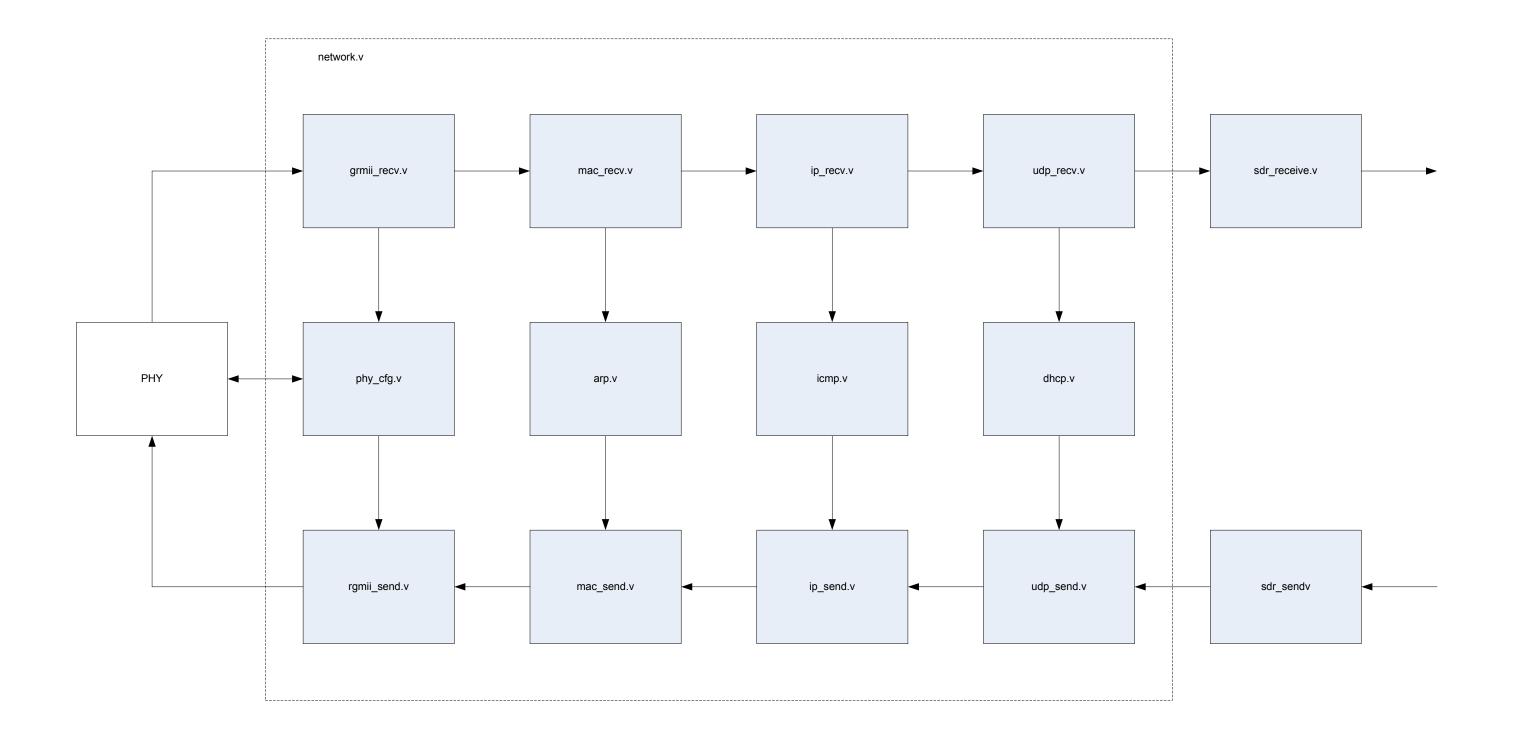
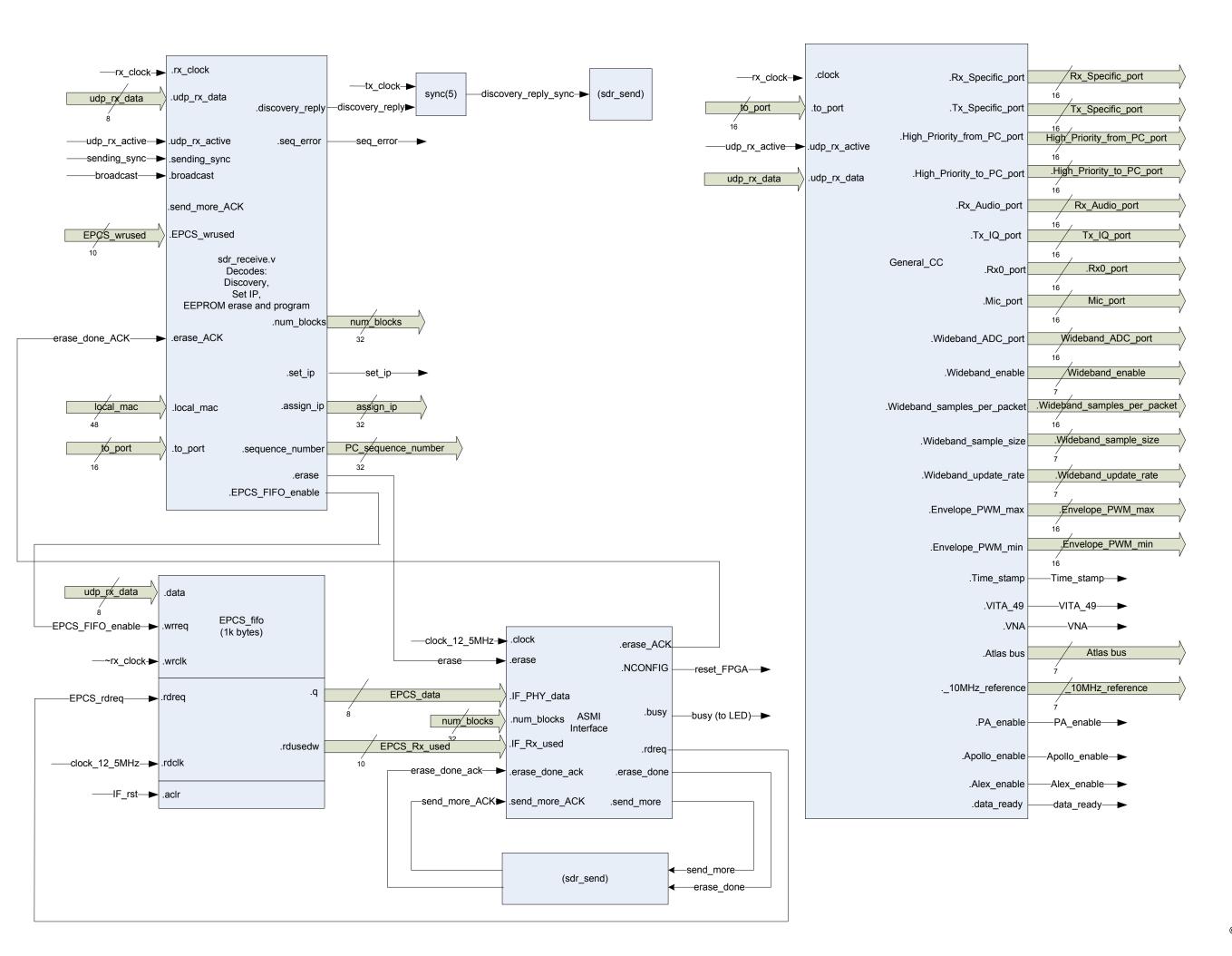
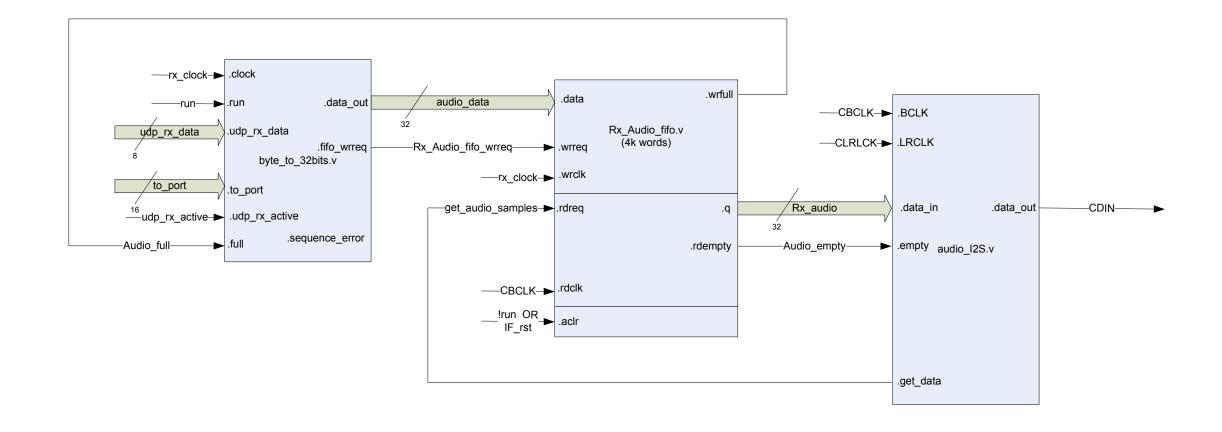
Architecture Overview



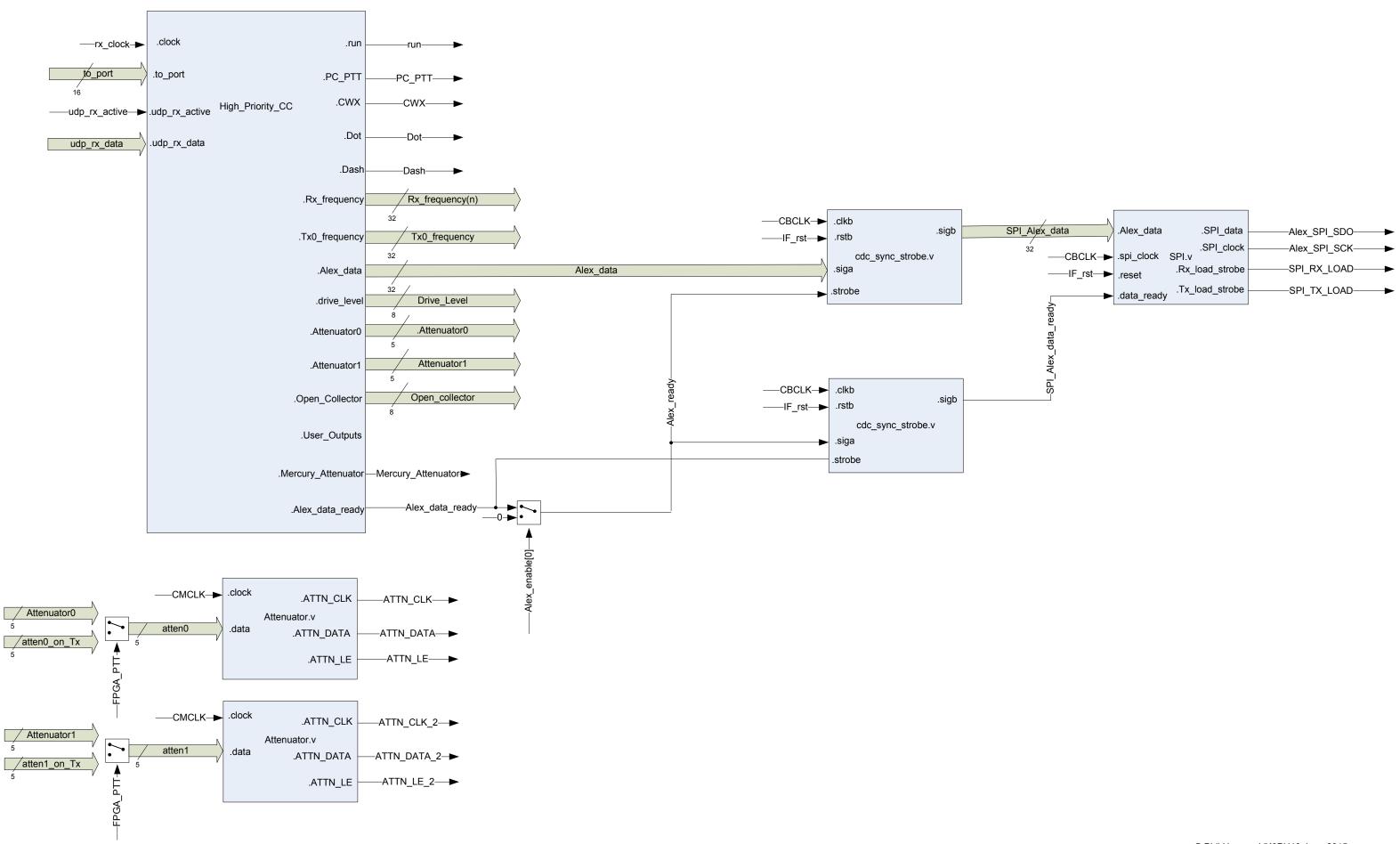
SDR_receive & General Command & Control



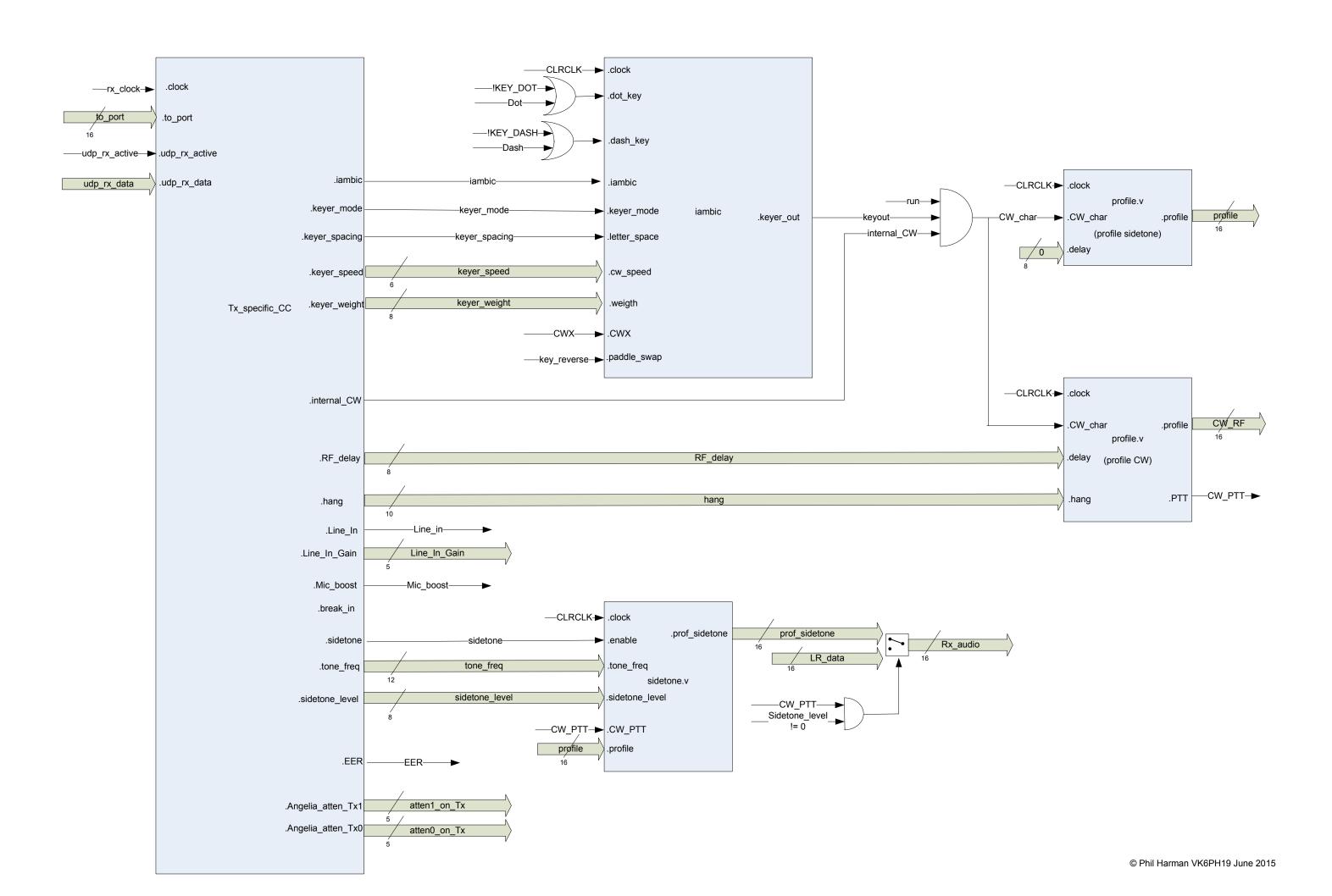
Receiver Audio



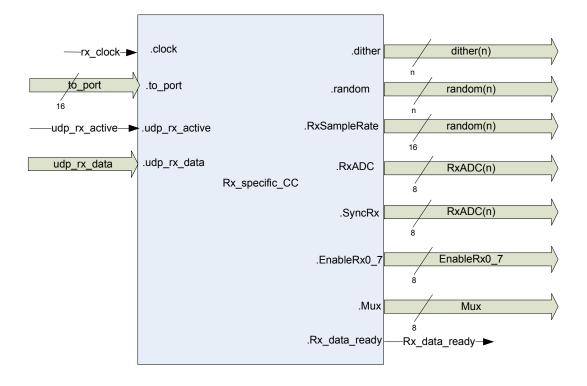
High Priority Command & Control (From PC)



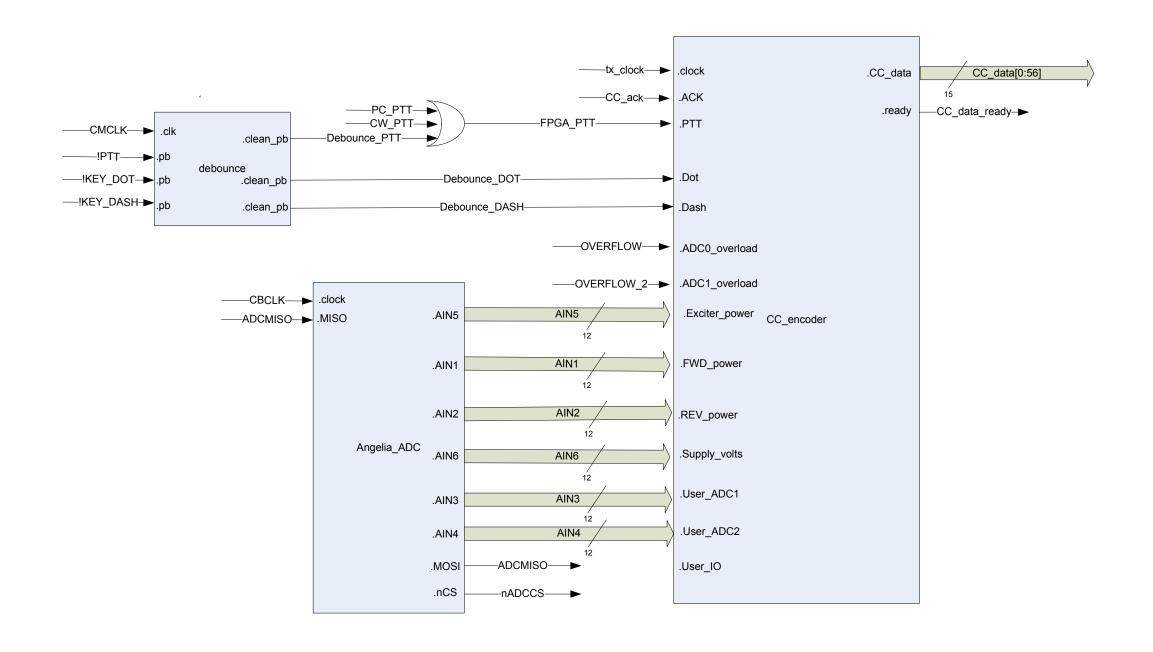
Tx Specific Command & Control - CW



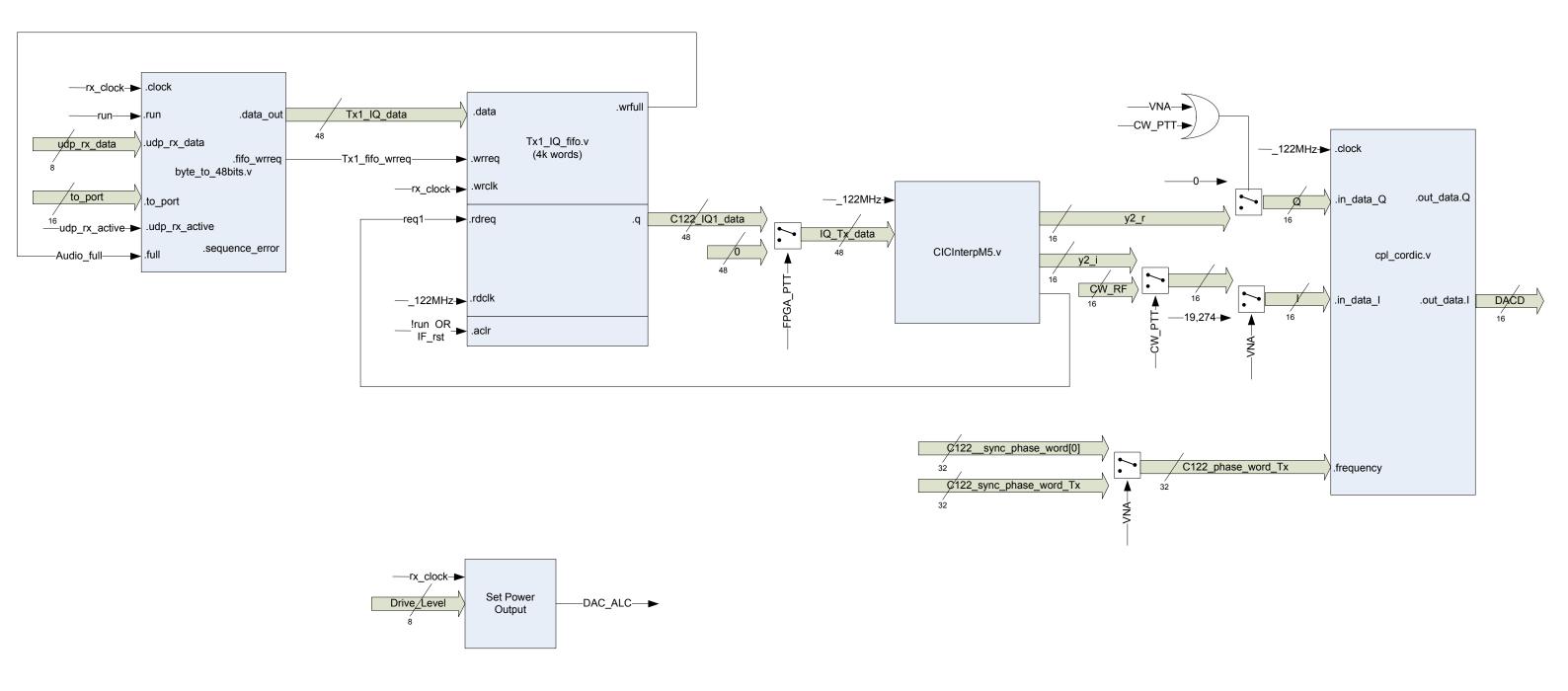
Receiver Specific Command & Control



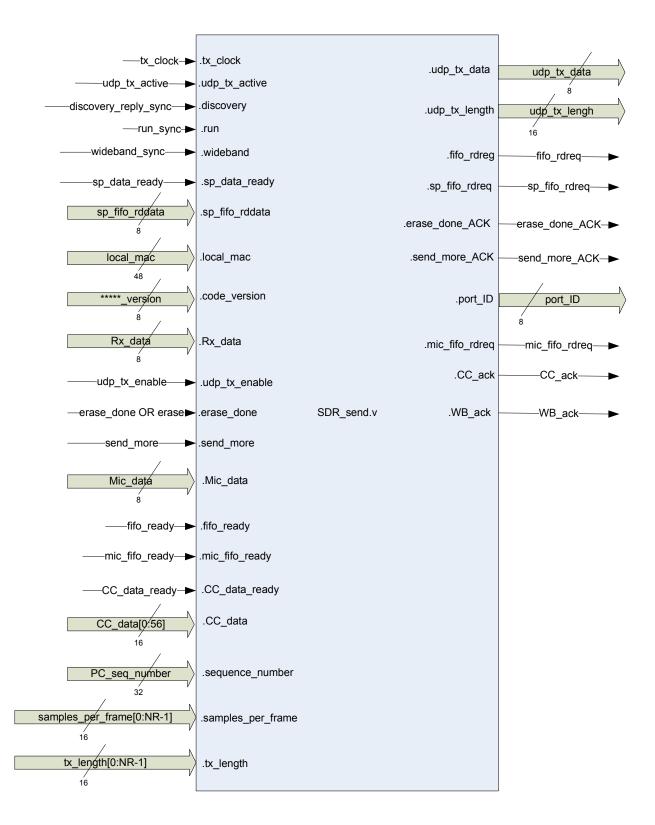
High Priority Command & Control (To PC)



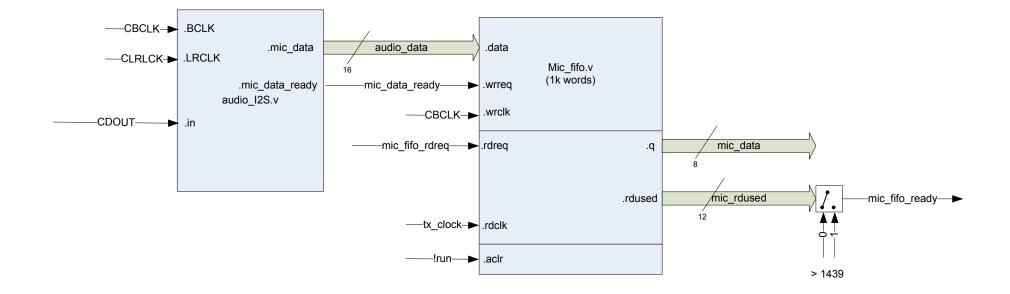
Transmitter



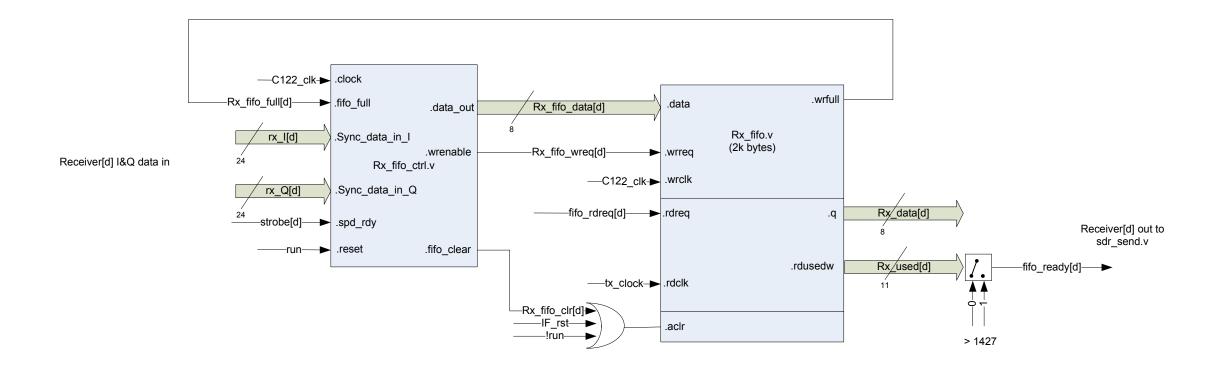
SDR_send.v



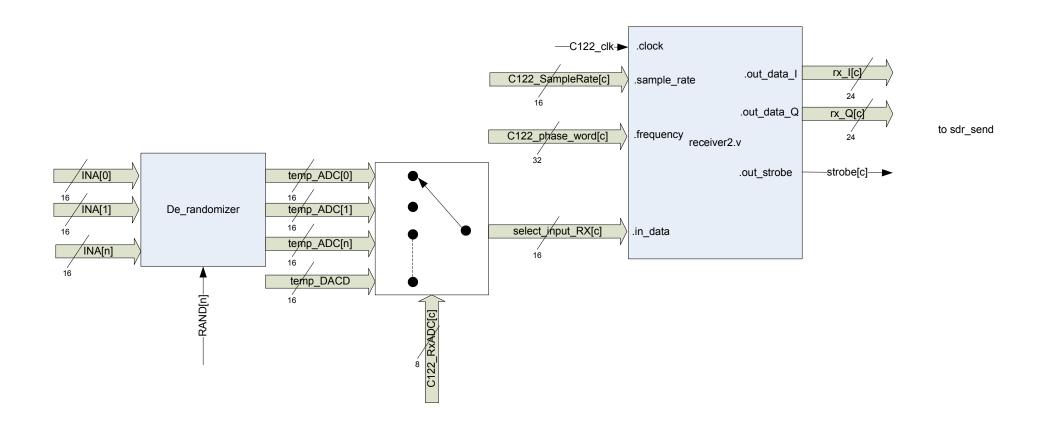
Microphone/Line in



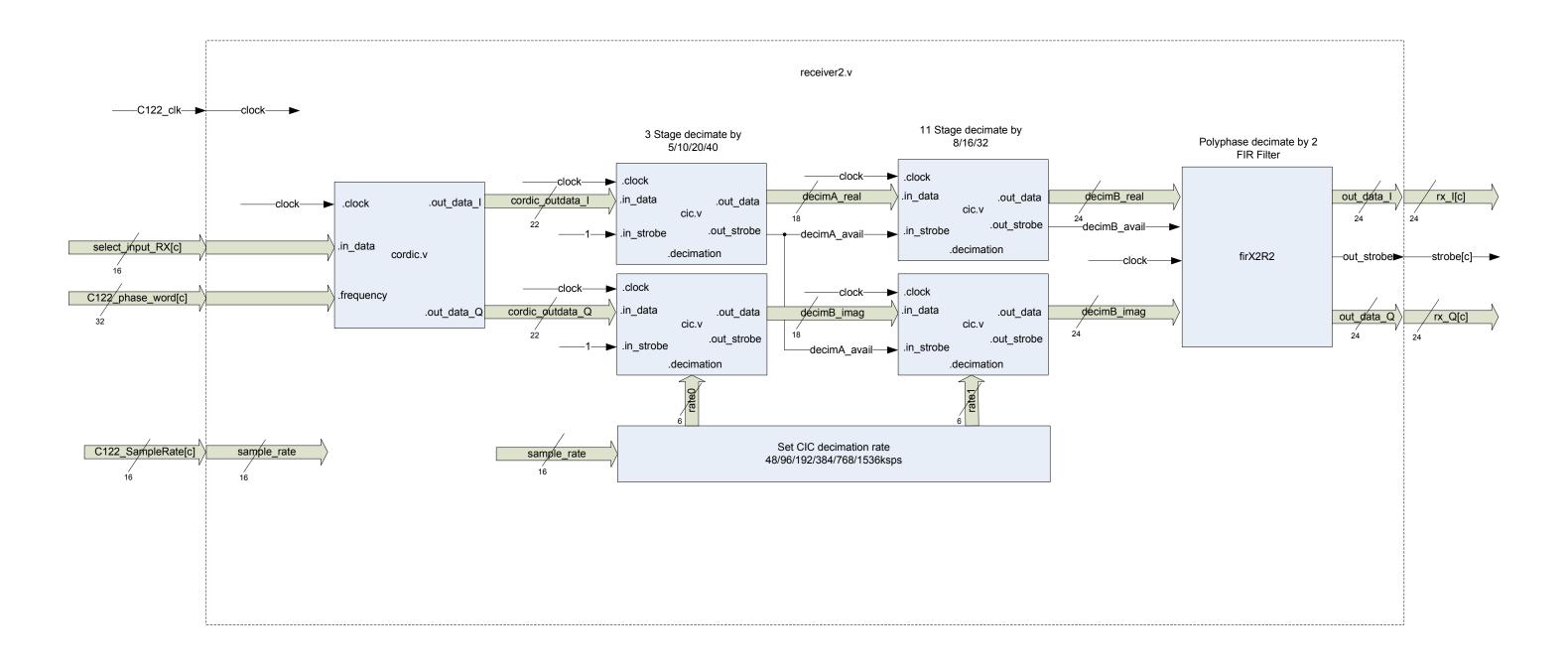
Receiver(1)



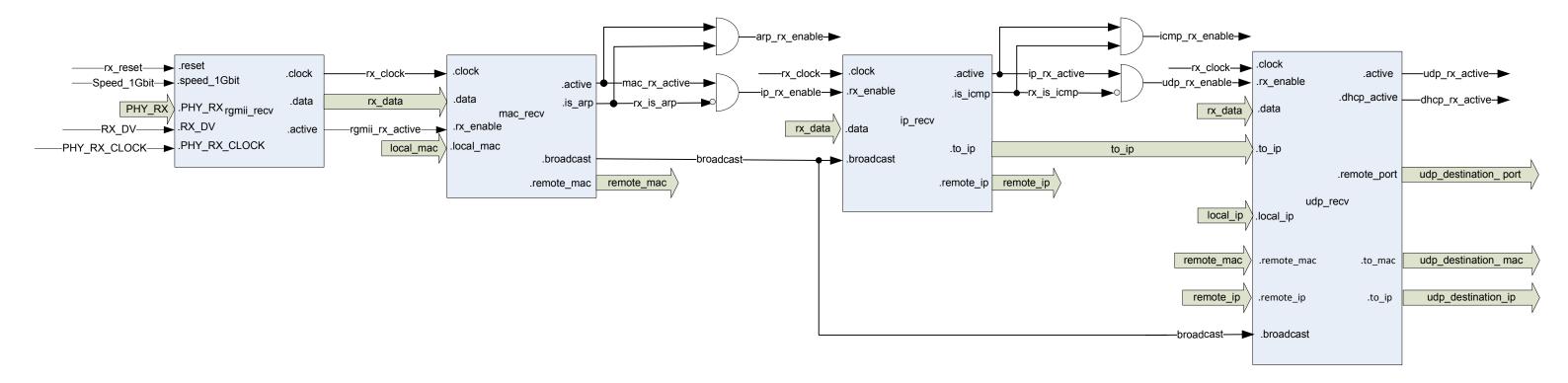
Receiver(2)

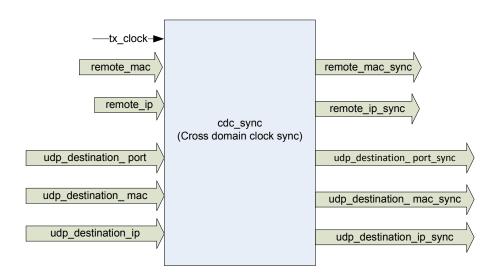


Receiver(3)



network.v (receive)

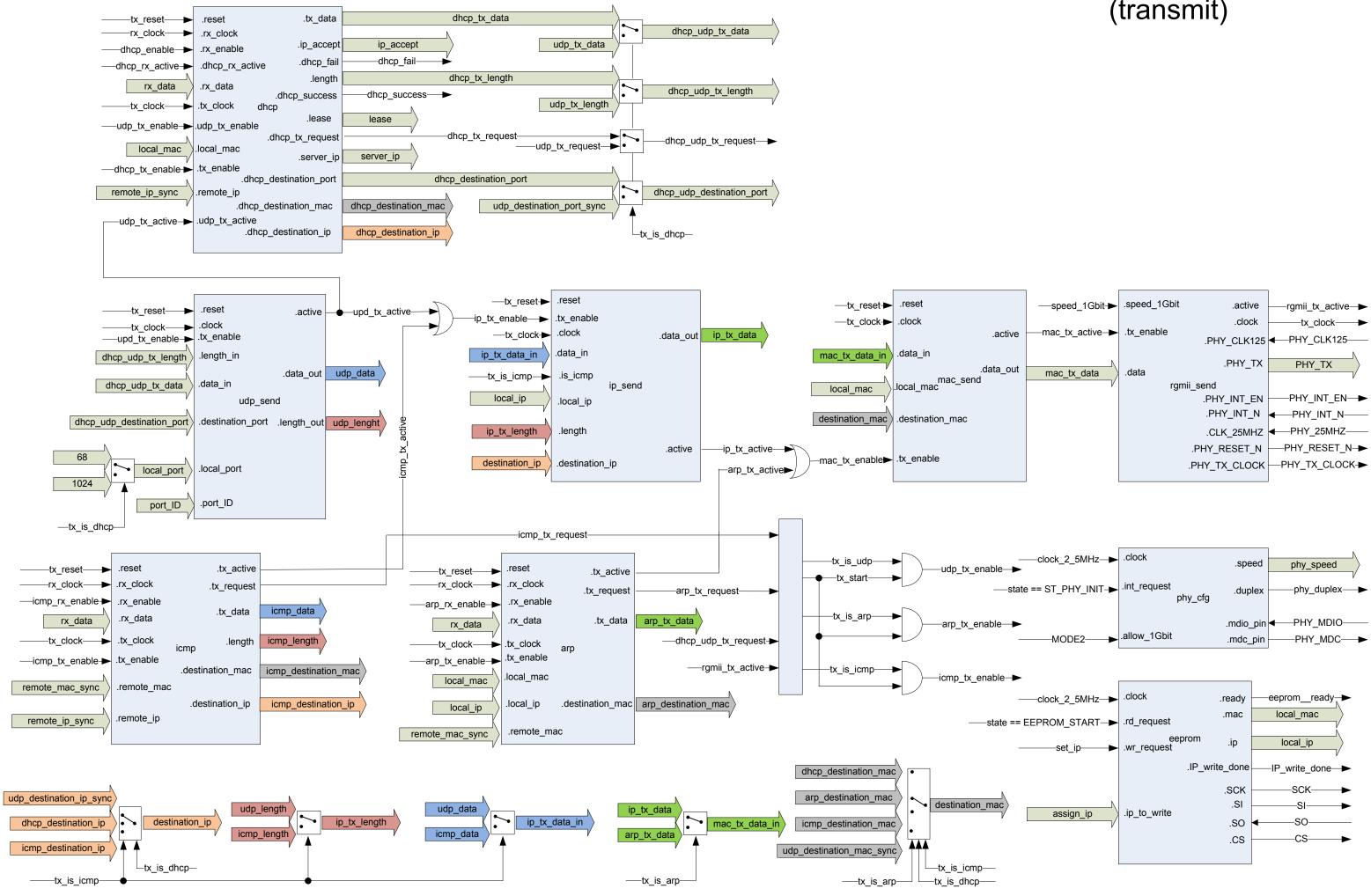




Move Rx data to Tx clock domain

rx_data udp_rx_data

network.v (transmit)



Wideband

