Pin Report (Sheet CYCLONE_II) for OZY_REVXA8.sch on 8/18/2006 2:30:15 PM

Name	Signal	Number	Function Name
U12.1	FPGA_ASDO	1	ASDO
U12.2	FPGA_NCSO	2	NCSO
U12.3	FX2_PC6	3	IO
U12.4	FX2_CTL3	4	IO
U12.5	FX2_CTL2/FLAGC	5	IO
U12.6	FX2_PA7/FLAGD/SLCS	6	IO
U12.7	+3.3VD	7	VCCIO1
U12.8	FX2_PA6/PKEND	8	IO
U12.9	GND	9	GND
U12.10	FX2_PA5/FIFOADR1	10	IO
U12.11	FX2_PA4/FIFOADR0	11	IO
U12.12	FX2_PA3/WU2	12	IO
U12.13	FX2_PA2/SLOE	13	IO
U12.14	FX2_PA1/INT1	14	IO
U12.15	FX2_PA0/INT0	15	IO
U12.16	FPGA_TDO	16	TDO
U12.17	FPGA_TMS	17	TMS
U12.18	FPGA_TCK	18	TCK
U12.19	FPGA_TDI	19	TDI
U12.20	FPGA_DATA0	20	DATA0
U12.21	FPGA_DCLK	21	DCLK
U12.22	FPGA_NCE	22	NCE
U12.23	FX2_CLK	23	CLK0
U12.24	FX2_IFCLK	24	CLK1
U12.25	GND	25	GND
U12.26	FPGA_NCONFIG	26	NCONFIG
U12.27	FPGA_CLK2IN	27	CLK2
U12.28	FPGA_CLK3IN	28	CLK3
U12.29	+3.3VD	29	VCCIO1
U12.30	FX2_RDY0/SLRD	30	IO
U12.31	FX2_RDY1/SLWR	31	IO
U12.32	FX2_RDY2	32	IO
U12.33	FX2_RDY3	33	IO
U12.34	FX2_RDY4	34	IO
U12.35	FX2_RDY5	35	IO
U12.36	FX2_PE0/T0OUT	36	IO
U12.37	FX2_PE1/T1OUT	37	IO
U12.38	GND	38	GND
U12.39	FX2_PE2/T2OUT	39	IO
U12.40	FX2_PE3/RXD0OUT	40	IO
U12.41	FX2_PE4/RXD1OUT	41	IO
U12.42	+3.3VD	42	VCCIO1
U12.43	FX2_PE5/INT6	43	IO
U12.44	FX2_PE6/T2EX	44	IO
U12.45	FX2_PE7/GPIFADR8	45	IO
U12.46	FPGA_CLKXOUT	46	IO
U12.47	FPGA_CLKYOUT	47	IO
U12.48	FX2_PC7	48	IO
U12.49	GND	49	GND
U12.50	GND	50	GND_PLL1
U12.51	+1.2VD	51	VCCD_PLL1
U12.52	GND	52	GND_PLL1

Name	Signal	Number	Function Name
U12.53	+1.2VA1	53	VCCA_PLL1
U12.54	GND	54	GNDA_PLL1
U12.55	GND	55	GND
U12.56	FX2_PB0/FD0	56	IO
U12.57	FX2_PB1/FD1	57	IO
U12.58	FX2_PB2/FD2	58	IO
U12.59	FX2_PB3/FD3	59	IO
U12.60	FX2_PB4/FD4	60	IO
U12.61	FX2_PB5/FD5	61	IO
U12.62	+3.3VD	62	VCCIO4
U12.63	FX2_PB6/FD6	63	IO
U12.64	FX2_PB7/FD7	64	IO
U12.65	GND	65	GND
U12.66	+1.2VD	66	VCCINT
U12.67	FPGA_GPIO1	67	IO
U12.68	FPGA_GPIO2	68	IO
U12.69	FPGA_GPIO3	69	IO
U12.70	FPGA_GPIO4	70	IO
U12.71	+3.3VD	71	VCCIO4
U12.72	FPGA_GPIO5	72	IO
U12.73	GND	73	GND
U12.74	FPGA_GPIO6	74	IO
U12.75	FPGA_GPIO7	75	IO
U12.76	FPGA_GPIO8	76	IO
U12.77	FPGA_GPIO9	77	IO
U12.78	GND	78	GND
U12.79	+1.2VD	79	VCCINT
U12.80	FPGA_GPIO10	80	IO
U12.81	FPGA_GPIO11	81	IO
U12.82	FPGA_GPIO12	82	IO
U12.83	+3.3VD	83	VCCIO4
U12.84	FPGA_GPIO13	84	IO
U12.85	GND	85	GND
U12.86	FPGA_GPIO14	86	IO
U12.87	FPGA_GPIO15	87	IO
U12.88	FPGA_GPIO16	88	IO
U12.89	FPGA_GPIO17	89	IO
U12.90	FPGA_GPIO18	90	IO
U12.91	+3.3VD	91	VCCIO4
U12.92	FPGA_GPIO19	92	IO
U12.93	GND	93	GND
U12.94	FPGA_GPIO20	94	IO
U12.95	FPGA_GPIO21	95	IO
U12.96	FPGA_GPIO22	96	IO
U12.97	FPGA_GPIO23	97	IO
U12.98	+3.3VD	98	VCCIO4
U12.99	FPGA_GPIO24	99	IO
U12.100	GND	100	GND
U12.101	FX2_INT4	101	IO
U12.102	FX2_T0	102	IO
U12.103	FPGA_RXD	103	IO
U12.104	FPGA_TXD	104	IO
U12.105	ATLAS_C31	105	IO
U12.106	ATLAS_C29	106	IO

Name	Signal	Number	Function Name
U12.107	FPGA_INIT_DONE	107	INIT_DONE
U12.108	FX2_T1	108	NCEO
U12.109	+3.3VD	109	VCCIO3
U12.110	ATLAS_C27	110	IO
U12.111	GND	111	GND
U12.112	ATLAS_C25	112	IO
U12.113	ATLAS_C24	113	IO
U12.114	ATLAS_C23	114	IO
U12.115	ATLAS_C22	115	IO
U12.116	ATLAS_C21	116	IO
U12.117	ATLAS_C20	117	IO
U12.118	ATLAS_C19	118	IO
U12.119	ATLAS_C18	119	IO
U12.120	ATLAS_C17	120	IO
U12.121	FPGA_NSTATUS	121	NSTATUS
U12.122	+3.3VD	122	VCCIO3
U12.123	FPGA_CONFIG_DONE	123	CONFIG_DONE
U12.124	GND	124	GND
U12.125	FPGA_MSEL1	125	MSEL1
U12.126	FPGA_MSEL0	126	MSEL0
U12.127	ATLAS_C16	127	IO
U12.128	ATLAS_C15	128	IO
U12.129	FPGA_CLK7IN	129	CLK7
U12.130	FPGA_CLK6IN	130	CLK6
U12.131	FPGA_CLK5IN	131	CLK5
U12.132	FPGA_CLK4IN	132	CLK4
U12.133	ATLAS_C14	133	IO
U12.134	ATLAS_C13	134	IO
U12.135	ATLAS_C12	135	IO
U12.136	+3.3VD	136	VCCIO3
U12.137	ATLAS_C11	137	IO
U12.138	ATLAS_C10	138	IO
U12.139	ATLAS_C9	139	IO
U12.140	GND	140	GND
U12.141	ATLAS_C8	141	IO
U12.142	ATLAS_C7	142	IO
U12.143	ATLAS_C6	143	IO
U12.144	ATLAS_C5	144	IO
U12.145	ATLAS_C4	145	IO
U12.146	ATLAS_C3	146	IO
U12.147	ATLAS_C2	147	IO
U12.148	+3.3VD	148	VCCIO3
U12.149	ATLAS_A2	149	IO
U12.150	ATLAS_A3	150	IO
U12.151	ATLAS_A4	151	IO
U12.152	ATLAS_A5	152	IO
U12.153	GND	153	GND
U12.154	GND	154	GND_PLL2
U12.155	+1.2VD	155	VCCD_PLL2
U12.156	GND	156	GND_PLL2
U12.157	+1.2VA2	157	VCCA_PLL2
U12.158	GND	158	GNDA_PLL2
U12.159	GND	159	GND
U12.160	ATLAS_A6	160	IO

Name	Signal	Number	Function Name
U12.161	ATLAS_A7	161	IO
U12.162	ATLAS_A8	162	IO
U12.163	ATLAS_A9	163	IO
U12.164	ATLAS_A10	164	IO
U12.165	ATLAS_A11	165	IO
U12.166	+3.3VD	166	VCCIO2
U12.167	GND	167	GND
U12.168	ATLAS_A12	168	IO
U12.169	ATLAS_A13	169	IO
U12.170	ATLAS_A14	170	IO
U12.171	ATLAS_A15	171	IO
U12.172	+3.3VD	172	VCCIO2
U12.173	ATLAS_A16	173	IO
U12.174	GND	174	GND
U12.175	ATLAS_A17	175	IO
U12.176	ATLAS_A18	176	IO
U12.177	GND	177	GND
U12.178	+1.2VD	178	VCCINT
U12.179	ATLAS_A19	179	IO
U12.180	ATLAS_A20	180	IO
U12.181	ATLAS_A21	181	IO
U12.182	ATLAS_A22	182	IO
U12.183	+3.3VD	183	VCCIO2
U12.184	GND	184	GND
U12.185	ATLAS_A23	185	IO
U12.186	GND	186	GND
U12.187	ATLAS_A24	187	IO
U12.188	ATLAS_A25	188	IO
U12.189	ATLAS_A27	189	IO
U12.190	+1.2VD	190	VCCINT
U12.191	ATLAS_A29	191	IO
U12.192	ATLAS_A31	192	IO
U12.193	FPGA_PIN193	193	IO
U12.194	+3.3VD	194	VCCIO2
U12.195	FPGA_PIN195	195	IO
U12.196	GND	196	GND
U12.197	FX2_CTL1/FLAGB	197	IO
U12.198	FX2_CTL0/FLAGA	198	IO
U12.199	FX2_PD7/FD15	199	IO
U12.200	FX2_PD6/FD14	200	IO
U12.201	FX2_PD5/FD13	201	IO
U12.202	+3.3VD	202	VCCIO2
U12.203	FX2_PD4/FD12	203	IO
U12.204	GND	204	GND
U12.205	FX2_PD3/FD11	205	IO
U12.206	FX2_PD2/FD10	206	IO
U12.207	FX2_PD1/FD9	207	IO
U12.208	FX2_PD0/FD8	208	IO