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REVISION RECORD

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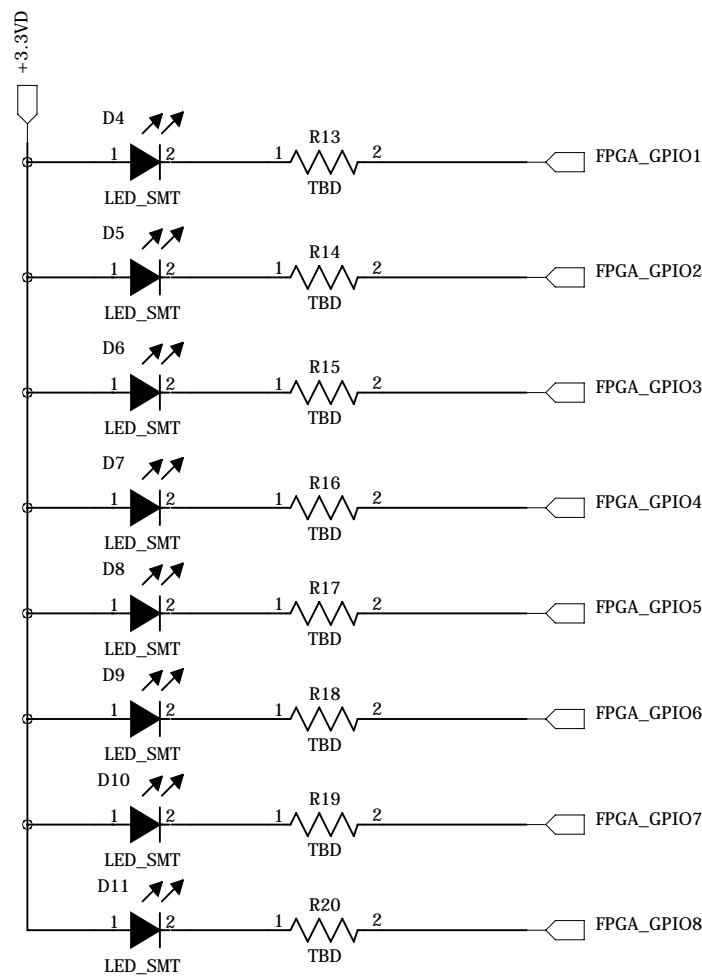
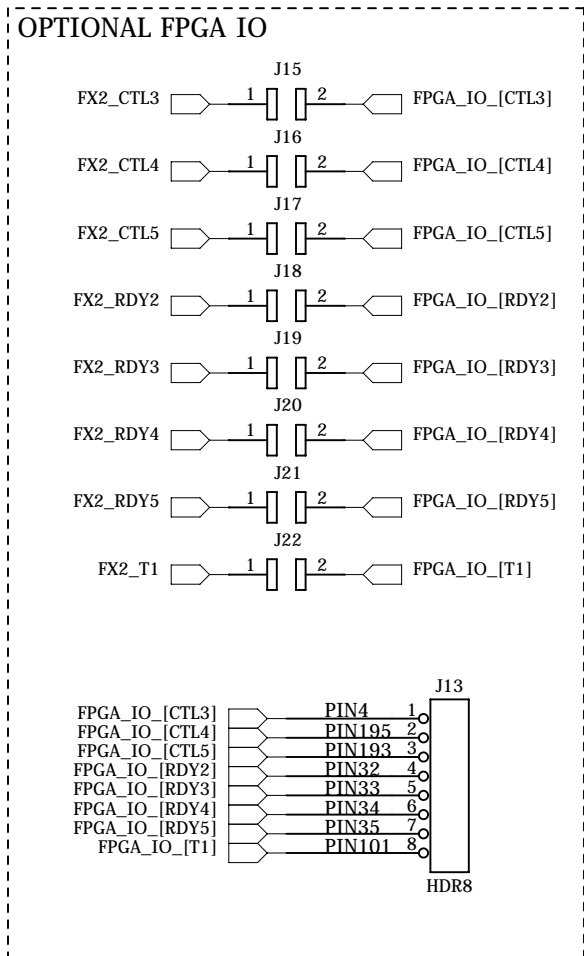
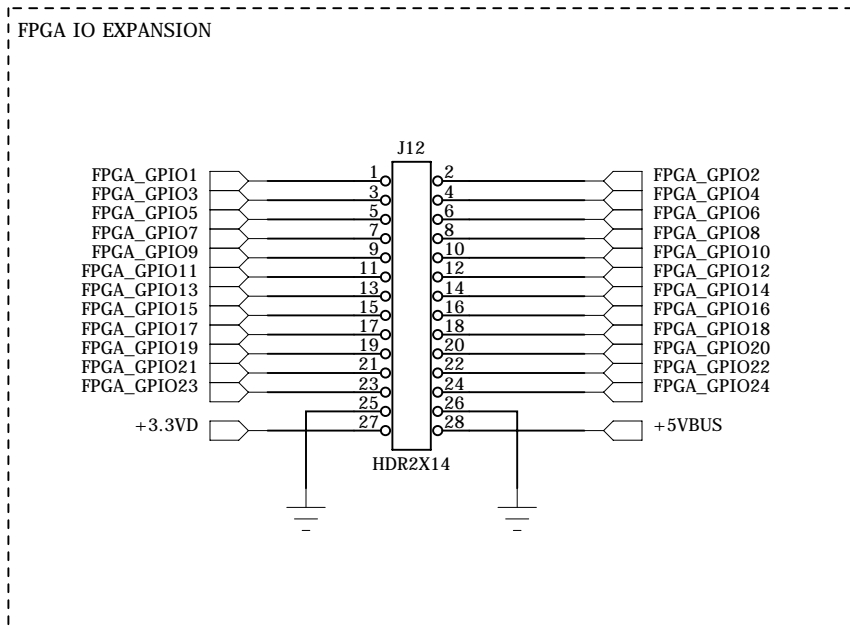
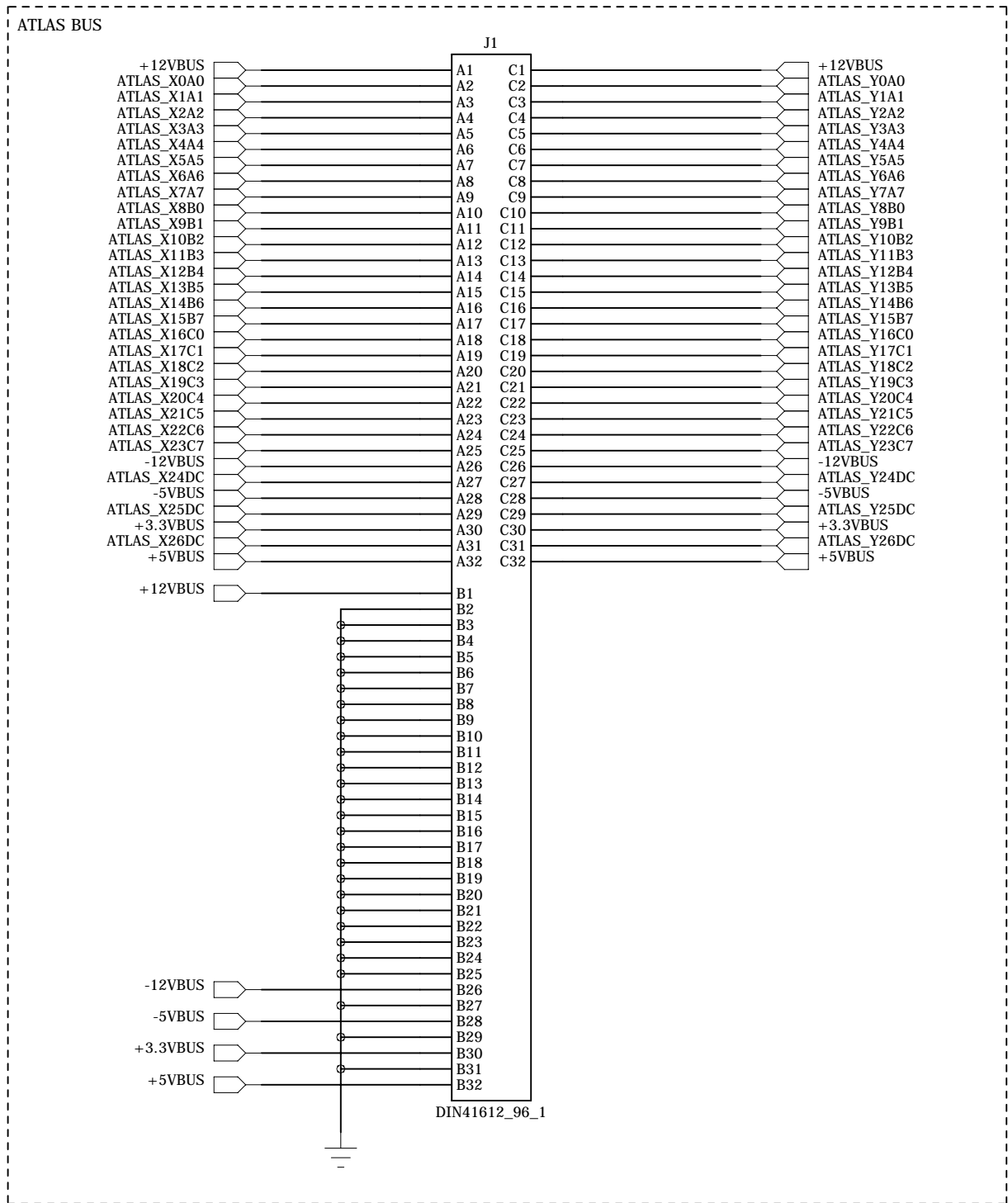
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DRAWN:
N8VB

DATED:
10 MAY 2006

CHECKED:

DATED:

COMPANY:

OPEN HIGH PERFORMANCE
SOFTWARE DEFINED RADIO
PROJECT

TITLE:

FPGA_USB Board

CODE:

SIZE:

C

DRAWING NO:

fpga_usb_nr.sch

REV:

NR

SCALE: NONE

SHEET: 3 OF 4

