ATLAS BUS SIGNALS							
DIN41612 96 PIN							
XBUS					YBUS		
PIN	<b>NAME</b>	<u>ALTERNATE</u>	PIN	NAME	PIN	NAME	<u>ALTERNATE</u>
A1	+12VDC		B1	+12VDC	C1	+12VDC	
A2	X0A0		B2	GND	C2	Y0A0	
A3	X1A1		В3	GND	C3	Y1A1	
A4	X2A2		B4	GND	C4	Y2A2	
A5	X3A3		B5	GND	C5	Y3A3	
A6	X4A4		В6	GND	C6	Y4A4	
A7	X5A5		B7	GND	C7	Y5A5	
A8	X6A6		В8	GND	C8	Y6A6	
A9	X7A7		В9	GND	C9	Y7A7	
A10	X8B0		B10	GND	C10	Y8B0	
A11	X9B1		B11	GND	C11	Y9B1	
A12	X10B2		B12	GND	C12	Y10B2	
A13	X11B3		B13	GND	C13	Y11B3	
A14	X12B4		B14	GND	C14	Y12B4	
A15	X13B5		B15	GND	C15	Y13B5	
A16	X14B6		B16	GND	C16	Y14B6	
A17	X15B7		B17	GND	C17	Y15B7	
A18	X16C0		B18	GND	C18	Y16C0	SPI - nCS4
A19	X17C1	nRST (1)	B19	GND	C19	Y17C1	SPI - nCS3
A20	X18C2	I2C - SCL	B20	GND	C20	Y18C2	SPI - nCS2
A21	X19C3	I2C - SDA	B21	GND	C21	Y19C3	SPI - nCS1
A22	X20C4	JTAG - TRST	B22	GND	C22	Y20C4	SPI - nCS0
A23	X21C5	JTAG - TMS	B23	GND	C23	Y21C5	SPI - SCK
A24	X22C6	JTAG - TCK	B24	GND	C24	Y22C6	SPI - MISO
A25	X23C7	JTAG - SDO ret	B25	GND	C25	Y23C7	SPI - MOSI
A26	-12VDC		B26	-12VDC	C26	-12VDC	
A27	X24DC	JTAG - SDO	B27	GND	C27	Y24DC	SPI - MOSI ovfl out
A28	-5VDC		B28	-5VDC	C28	-5VDC	
A29	X25DC	JTAG - SDI	B29	GND	C29	Y25DC	SPI - MOSI ovfl in
A30	+3.3VDC		B30	+3.3VDC	C30	+3.3VDC	
A31	X26DC		B31	GND	C31	Y26DC	
A32	+5VDC		B32	+5VDC	C32	+5VDC	

## Notes:

1 Reset line, Open collector pulled up to +3.3V