Opcode	Mnemonic	Control step	E _{MS}	E _{IR}	E _{PC}	E _{D0}	E _{ALUR}	C _{MAR}	CIR	C _{PC}	C _{D0}	CALUR	R	W	F ₁	F ₂
	(fetch step A)	1			1			1								
		2	1						1				1			
	(fetch step B)	3			1							1				1
		4					1			1						
000	CLEAR	1										1				
		2					1				1					
001	INC1	1				1						1				1
		2					1				1					
010	ADD# n	1		1								1			1	
		2					1				1					
011	DEC1	1				1						1			1	1
		2					1				1					
100	JMP loc	1		1						1						
101	BUZ loc	1	1 (if Z is not 0)						1 (if Z is not 0)							
110	LOAD loc	1		1				1								
		2	1								1		1			
111	STORE loc	1		1				1								
		2				1								1		