



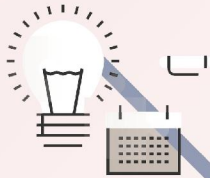
# PSG Institute of Technology and Applied Research

Neelambur, Coimbatore - 641062



## National Conference

on



## COMMUNICATION, COMPUTING AND SYSTEM DESIGN (NC<sup>3</sup>SD'19)

19<sup>th</sup>  
MARCH  
2019



Departments of  
Electrical & Electronics Engineering  
Electronics & Communication Engineering  
Computer Science and Engineering

Academic partners





## About the Institution

PSG Institute of Technology and Applied Research, established in the year 2014, is an AICTE approved Institution and affiliated to Anna University, Chennai. It is currently offering five different programmes in undergraduate level - CIVIL, CSE, ECE, EEE and MECH. The Institution is equipped with various Centres of Excellence, Innovation Centre, NI Graphical System Design Centre, Festo Lab, Keysight RF Circuit & System Design Centre, e-yantra. The Institution aims to achieve excellence in education and research, and nurture engineers with ethics, to face global challenges to serve industry and society.

## Electrical and Electronics Engineering

The field of Electrical and Electronics Engineering covers the design, manufacture, test and operation of electrical and electronic components, circuits and systems. The students who choose this programme are keen in acquiring knowledge and skills to take up challenges in a wide range of applications that include energy systems, digital systems, analog and digital electronics, embedded systems, IoT and robotics.

## Electronics and Communication Engineering

The Department of Electronics and Communication Engineering has been established to equip the students with necessary core competencies for the innovation of new and optimized technology to enhance the society. This will empower them to be proficient and skillful to excel in competitive examinations and in their career to create a difference from the acquired knowledge.

## Keynote Speakers



**Mr. Abilash ET Nair**

Senior Marketing  
Manager,  
ABB India Limited.



**Dr. Mahesh R Panicker**

Associate Professor,  
Electrical Engineering,  
IIT Palakad.



**Mr. G. S. Madhusudan**

CEO/Co-Founder,  
InCore Semiconductors,  
Chennai.



**Prof. Bobby George**

Associate Professor,  
Electrical Engineering,  
IIT Madras.

## Computer Science and Engineering

The Department of Computer Science and Engineering focuses on building a strong foundation in the basic concepts like logical thinking and programming skills and exposing recent developments in Computer Science and Engineering like SMAC technology (Social network analysis, Mobile computing, Analytics and Cloud computing) and Quantum computing. The students are trained in the latest hardware and software technologies to meet the latest global requirements. MoU has been signed with various research organizations and industries across the globe to promote technology development, training of students and development of state-of-art centers.

## About the Conference

The objective of NC<sup>3</sup>SD 2019 is to provide a platform for the academicians, researchers and industry personnel to gather and disseminate knowledge and results of original research, discussion of practical insights and the reporting of experience relevant to Electrical, Electronics, Communication, Computer Science and their applications. This Conference will explore the new horizon of innovations from distinguished researchers, scientists, and eminent authors in academia and industry, working for the advancements in engineering, science and technology from all over the country. The conference is envisaged to provide an opportunity for young students and researchers to find national linkage for future collaborations in their career path. This conference includes intellectual disclosures and keynote addresses by reputed academicians, industry experts and high quality presentations from the best of the researchers in India.



## Call for papers

High quality research contributions describing original and unpublished results of conceptual, constructive, empirical, experimental, or theoretical work in any one /combined areas of the themes of the conference are cordially invited.

The topics of interest for submission of papers include, but not limited to:

- 5G and Wireless Broadband Networks
- Visible Light Communication
- Massive MIMO
- Molecular & Biological Communications
- Network Coding
- Optical Communication
- Physical Layer Security
- RF/Microwave/Antennas
- Satellite Communication
- Software Defined & Cognitive Networks
- Adaptive/Array Signal Processing
- Adaptive Security Architecture
- Biomedical System Design
- Internet of Things (IoT)
- Semiconductor Technology
- Instrumentation & Feedback Control Systems
- Ad hoc & Sensor Networks
- Wireless Networks
- Android Applications
- Big Data Analytics
- Social Network Analysis
- Cluster, Cloud and Grid Computing
- Distributed Computing
- Pervasive Computing
- Green Computing
- High Performance Computing
- Quantum Computing
- Blockchains and Distributed Ledgers
- VLSI Architecture for High Speed and Low Power
- Formal Verification
- Power Electronics
- Energy Efficient Drives
- Renewable Power Conversion Technologies
- Automation
- Artificial Intelligence and its Application
- Virtual and Augmented Reality
- Robotics
- Power Line Communication
- Smart Grid
- Underwater Systems
- Device Modelling and Simulation
- MEMS/NEMS
- Embedded and Control Systems
- Logic/Computation
- CMOS Analog, Mixed Signal and Digital Circuits
- Image Processing
- Machine Learning
- High Voltage Engineering and Insulation Technology
- Electrical and Electronic Materials and Processes
- Power Quality

## Important Dates

Call for papers	:	31/01/2019
Abstract Submission	:	11/02/2019
Acceptance Notification	:	15/02/2019
Last Date of registration for conference	:	11/03/2019
Full Paper Submission	:	13/03/2019

## Registration Fees

Industry	:	Rs. 1500
Faculty	:	Rs. 1250
Scholar/Student	:	Rs. 1000

**Note :** Payment in the form of DD has to be drawn in favour of **PSG CNCE payable at Coimbatore.**

## Author Guidelines

Prospective authors are encouraged to submit full paper(s) highlighting the technical contributions and key results for review. Only original papers which have not been published or submitted elsewhere will be considered. Submissions should not exceed SIX A4 size pages in length, should use font size not smaller than 10 points, and have margins on all four sides as per the IEEE conference format.

Prospective authors may kindly submit their papers, as per the guidelines provided through email to : [nc3sd19@psgitech.ac.in](mailto:nc3sd19@psgitech.ac.in)

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