/CORDIC_FSM_v2_tb/clk														
/CORDIC_FSM_v2_tb/cordic_fsm_v2/state_reg 4'd0			4'd1	4'd2	4'd3	4'd5		4'd6	4'd7	4'd8		4'd0		
/CORDIC_FSM_v2_tb/cordic_fsm_v2/state_next 4'd0		, 4'd1	4'd2	4'd3	4'd5		4'd6	4'd7	4'd8	-	4'd0	4'd0		
/CORDIC_FSM_v2_tb/reset							-					<u> </u>		
/CORDIC_FSM_v2_tb/beg_FSM_CORDIC	-						-			-		+		
/CORDIC_FSM_v2_tb/ACK_FSM_CORDIC							-							
/CORDIC_FSM_v2_tb/operation							-			-		•		
/CORDIC_FSM_v2_tb/shift_region_flag(2'd1										-				
/CORDIC_FSM_v2_tb/cont_var 2'd2			1				-		-	-				
/CORDIC_FSM_v2_tb/ready_add_subt														
/CORDIC_FSM_v2_tb/max_tick_iter														
/CORDIC_FSM_v2_tb/min_tick_iter														
/CORDIC_FSM_v2_tb/max_tick_var														
/CORDIC_FSM_v2_tb/min_tick_var														
/CORDIC_FSM_v2_tb/ready_CORDIC														
/CORDIC_FSM_v2_tb/beg_add_subt														
/CORDIC_FSM_v2_tb/ack_add_subt														
/CORDIC_FSM_v2_tb/sel_mux_1														
/CORDIC_FSM_v2_tb/sel_mux_3														
/CORDIC_FSM_v2_tb/sel_mux_2 2'd2					2'd1	2'd2								
/CORDIC_FSM_v2_tb/mode														
/CORDIC_FSM_v2_tb/enab_cont_iter														
/CORDIC_FSM_v2_tb/load_cont_iter														
/CORDIC_FSM_v2_tb/enab_cont_var														
/CORDIC_FSM_v2_tb/load_cont_var														
/CORDIC_FSM_v2_tb/enab_RB1														
/CORDIC_FSM_v2_tb/enab_RB2												+		
/CORDIC_FSM_v2_tb/enab_d_ff_Xn												+		
/CORDIC_FSM_v2_tb/enab_d_ff_Yn												-		
/CORDIC_FSM_v2_tb/enab_d_ff_Zn														
/CORDIC_FSM_v2_tb/enab_dff5												+		
/CORDIC_FSM_v2_tb/enab_d_ff_out														
/CORDIC_FSM_v2_tb/enab_dff_shifted_x			+ +				+ .							
/CORDIC_FSM_v2_tb/enab_dff_shifted_y														
/CORDIC_FSM_v2_tb/enab_dff_LUT	+								+ -			+		
/CORDIC_FSM_v2_tb/enab_dff_sign														
11 1 1 1	110 ns	120 ns	130 ns	140 ns	150 ns	160 ns	170 ns	180 ns	190 ns	200 ns	210 ns	220 ns	230 ns	240 ns