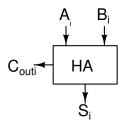
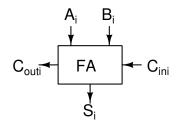
EEL 5105 - Laboratório 3

Meio somador (half adder):



Entradas		Saídas		Operação
A_i	B_i	C_{outi}	S_i	
0	0	0	0	0 + 0 = 0
0	1	0	1	0 + 1 = 1
1	0	0	1	1 + 0 = 1
1	1	1	0	1 + 1 = 2

Somador completo (full adder):



Entradas			Saídas		Operação
C_{ini}	A_i	B_i	C_{outi}	S_i	
0	0	0	0	0	0 + 0 + 0 = 0
0	0	1	0	1	0+0+1=1
0	1	0	0	1	0+1+0=1
0	1	1	1	0	0+1+1=2
1	0	0	0	1	1+0+0=1
1	0	1	1	0	1+0+1=2
1	1	0	1	0	1+1+0=2
1	1	1	1	1	1+1+1=3

Somador de 4 bits:

