

ANEXO

Universidade Federal de Santa Catarina - UFSC
Engenharia Eletrônica - EEL7303 - Circuitos Eletrônicos Analógicos
Exp00 - Amplificador Emissor-Comum
Gustavo Simas e André Mattos

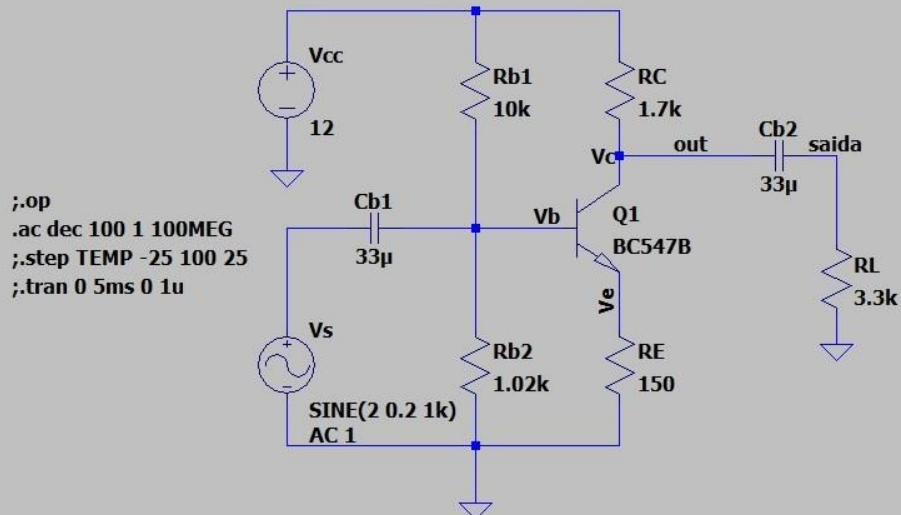


Figura 1 - Circuito Simulado em LTSpice

--- Operating Point ---

| | | |
|------------|---------------|----------------|
| V(out) : | 7.07034 | voltage |
| V(vb) : | 1.10195 | voltage |
| V(ve) : | 0.436389 | voltage |
| V(n001) : | 12 | voltage |
| V(n003) : | 2 | voltage |
| V(n002) : | 0.000233314 | voltage |
| V(nc_01) : | 0 | voltage |
| Ic(Q1) : | 0.0028998 | device_current |
| Ib(Q1) : | 9.46482e-006 | device_current |
| Ie(Q1) : | -0.00290926 | device_current |
| I(Cb2) : | -2.33314e-016 | device_current |
| I(Cb1) : | -2.96357e-017 | device_current |
| I(RL) : | 0 | device_current |
| I(Rb1) : | 0.00108981 | device_current |
| I(Rb2) : | 0.00108034 | device_current |
| I(Re) : | 0.00290926 | device_current |
| I(Rc) : | 0.0028998 | device_current |
| I(Vcc) : | -0.0039896 | device_current |
| I(Vs) : | -2.96357e-017 | device_current |

Figura 2 - Ponto Quiescente

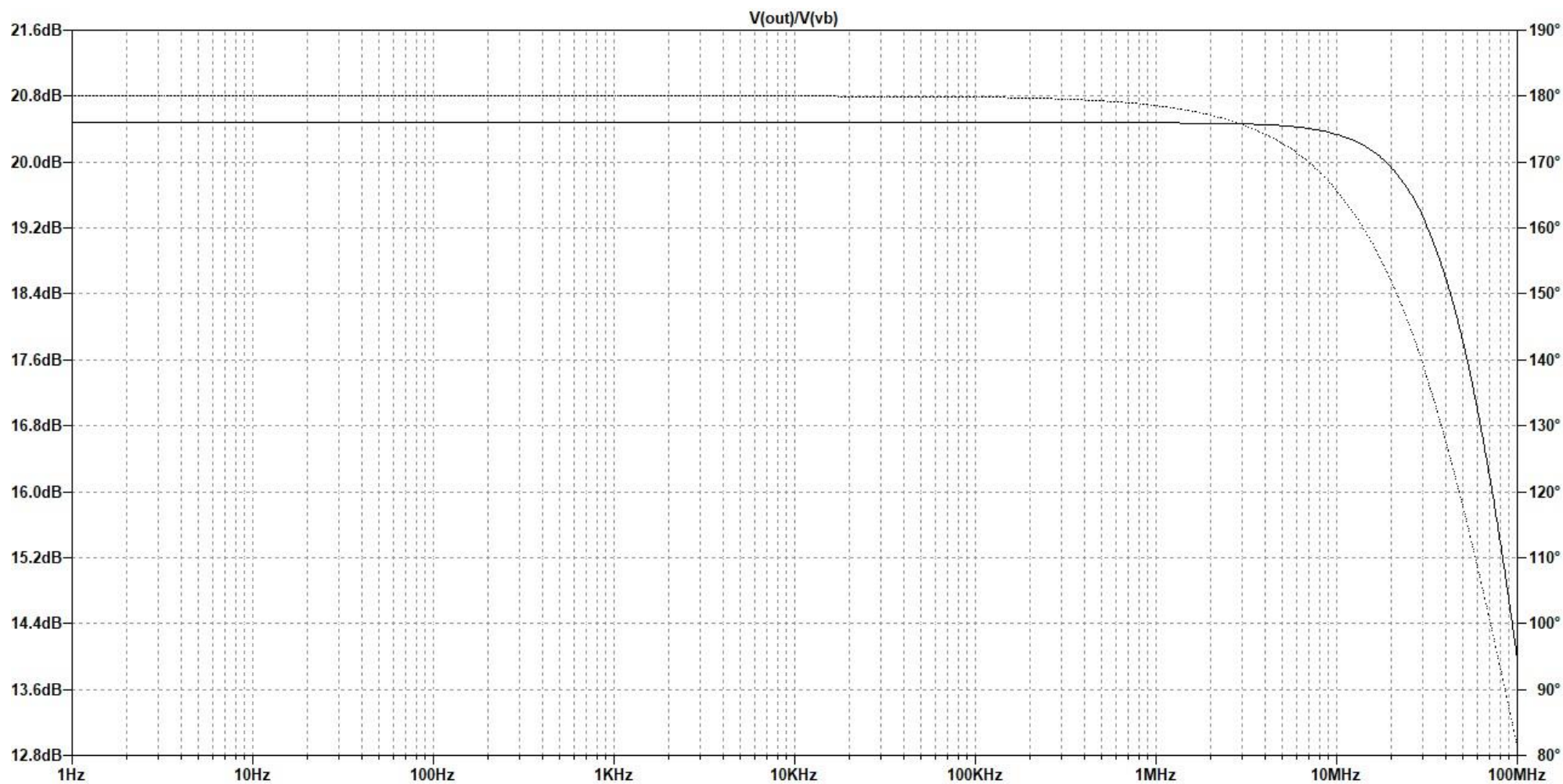


Figura 3 - Resposta em Frequência Sem Carga

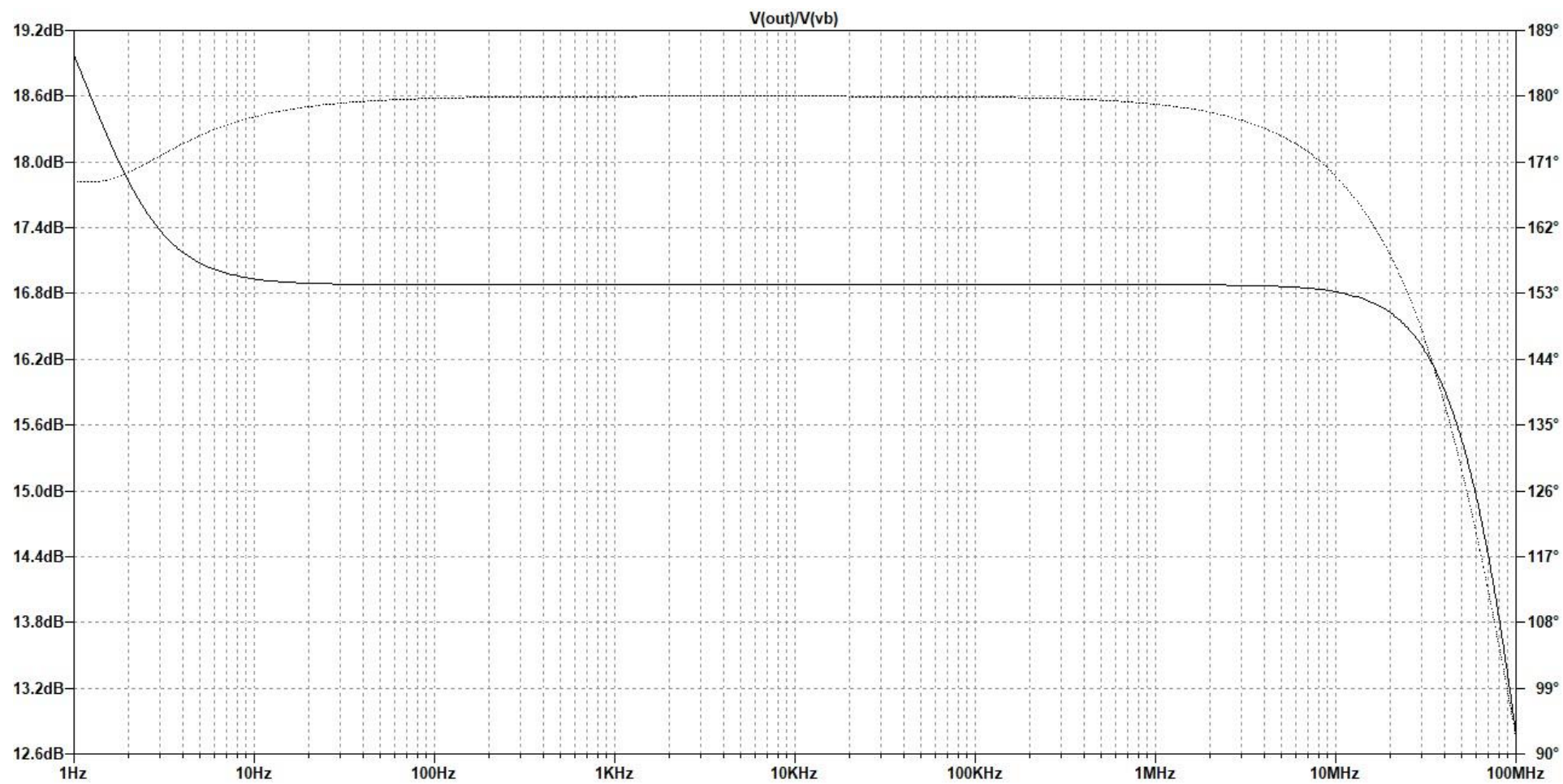


Figura 4 - Resposta em Frequência Com Carga

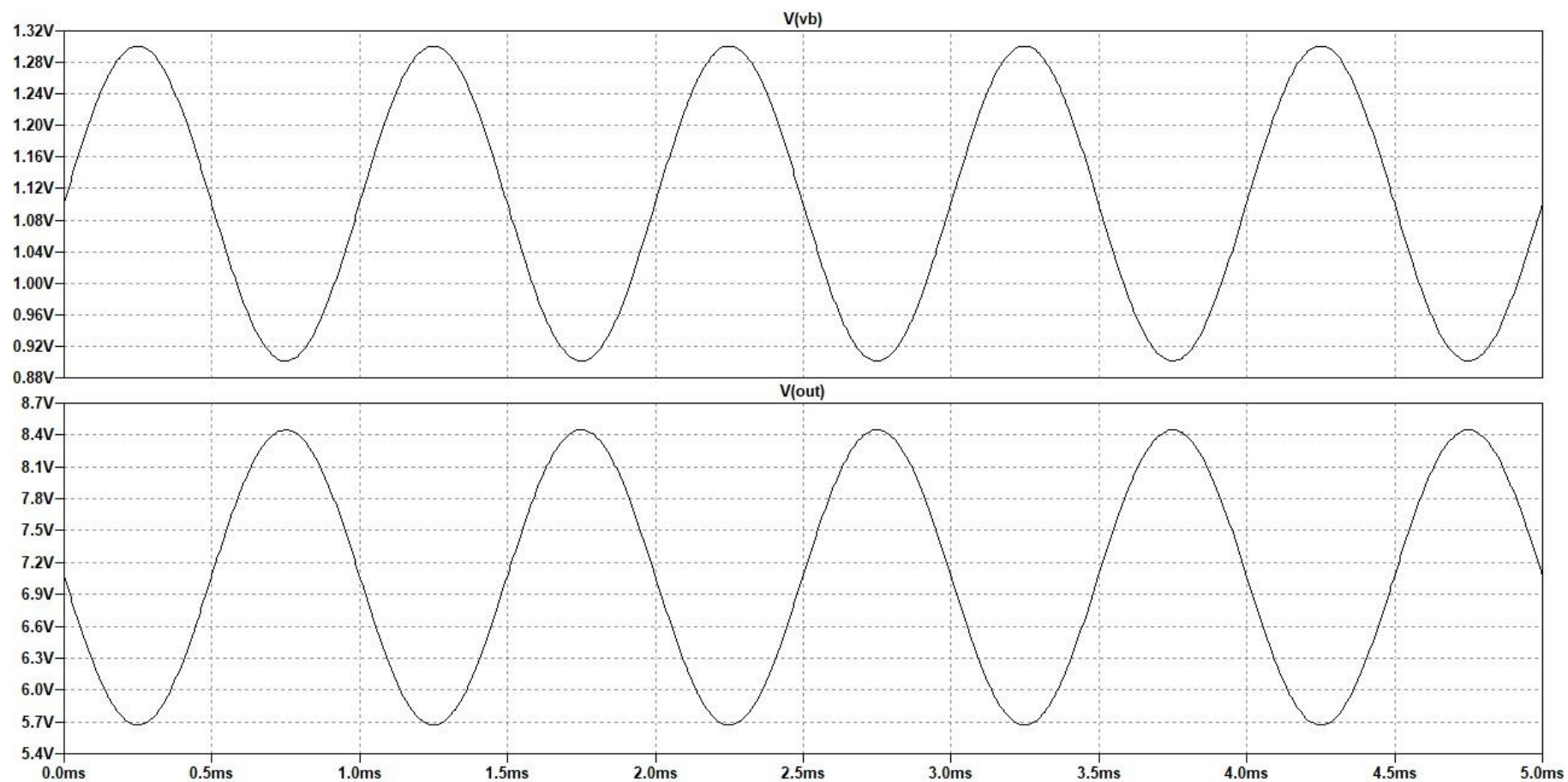


Figura 5 - Entrada e Saída em Análise Transiente com 200mV de Sinal de Entrada

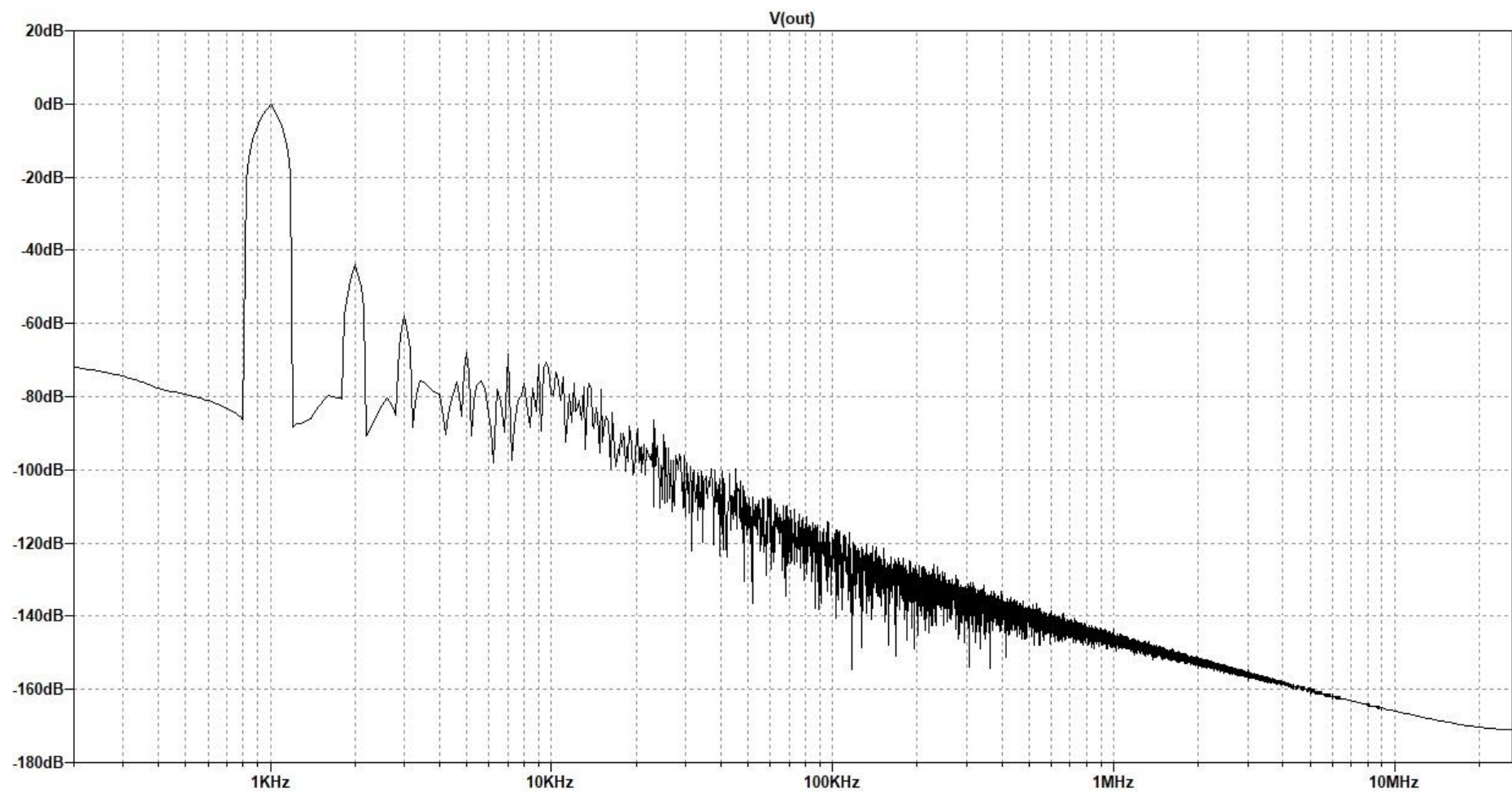


Figura 6 - Análise FFT com Entrada 200mV

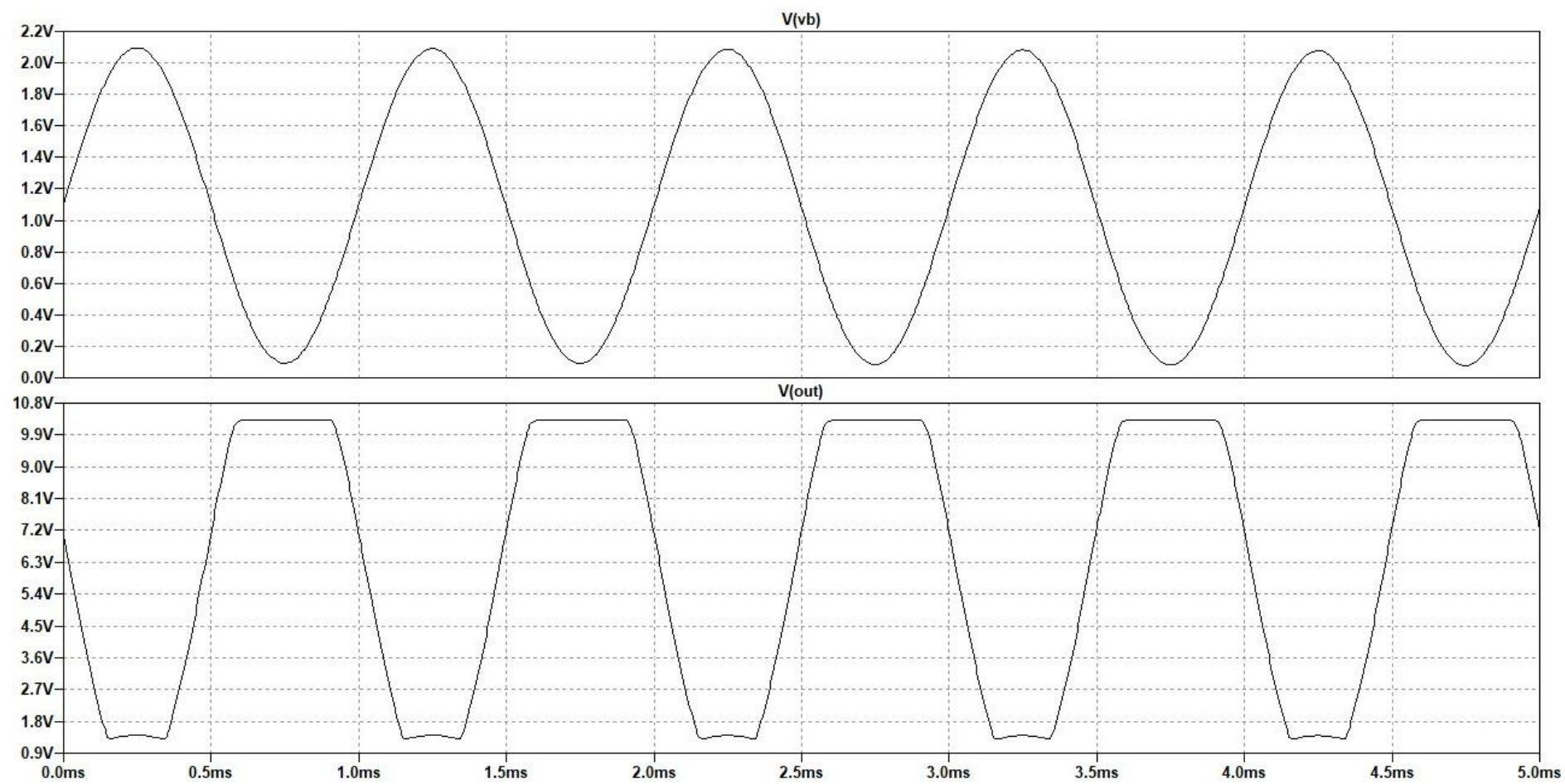


Figura 7 - - Entrada e Saída em Análise Transiente com 1V de Sinal de Entrada

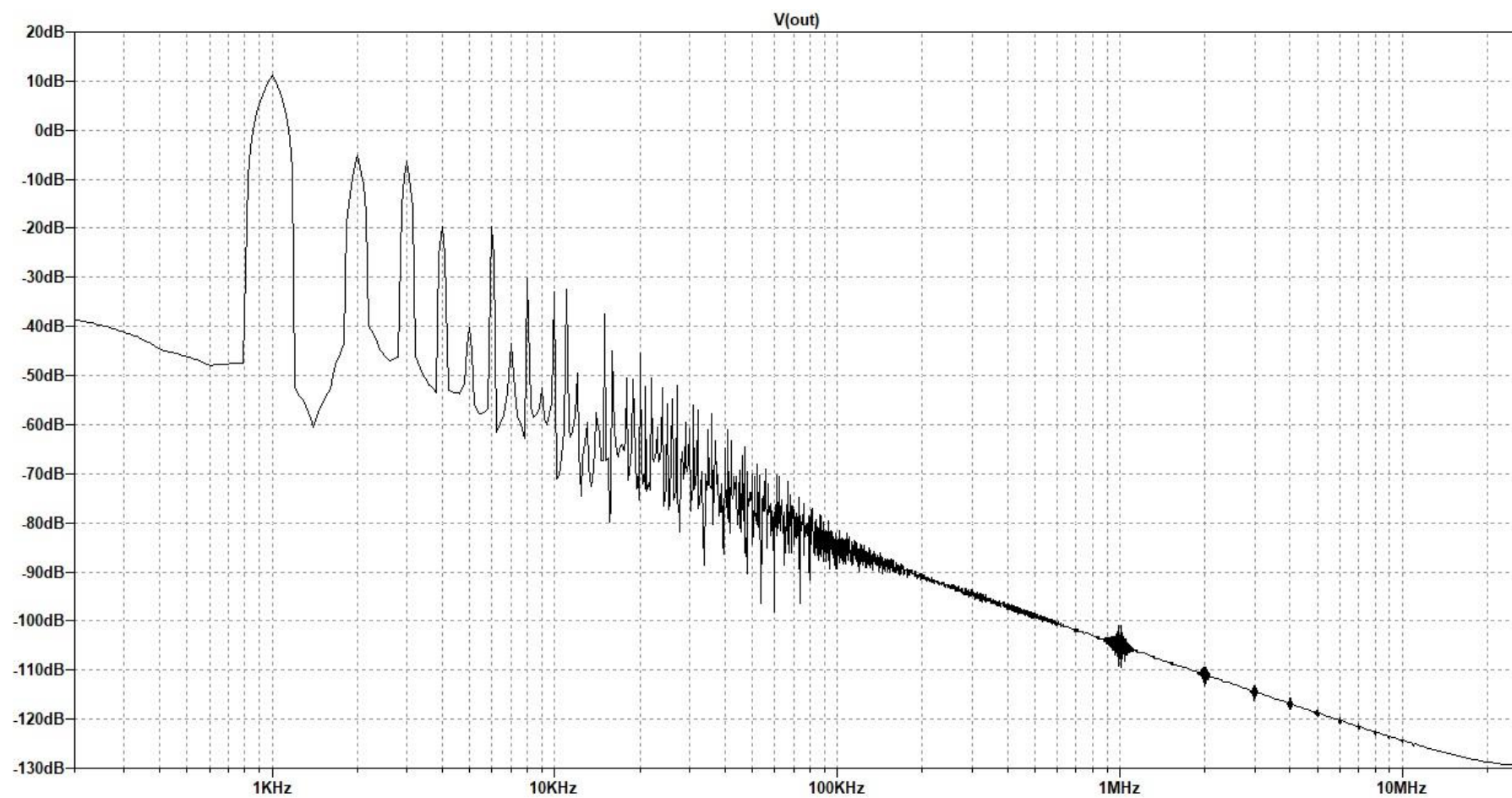


Figura 8 - Análise FFT com Entrada 200mV

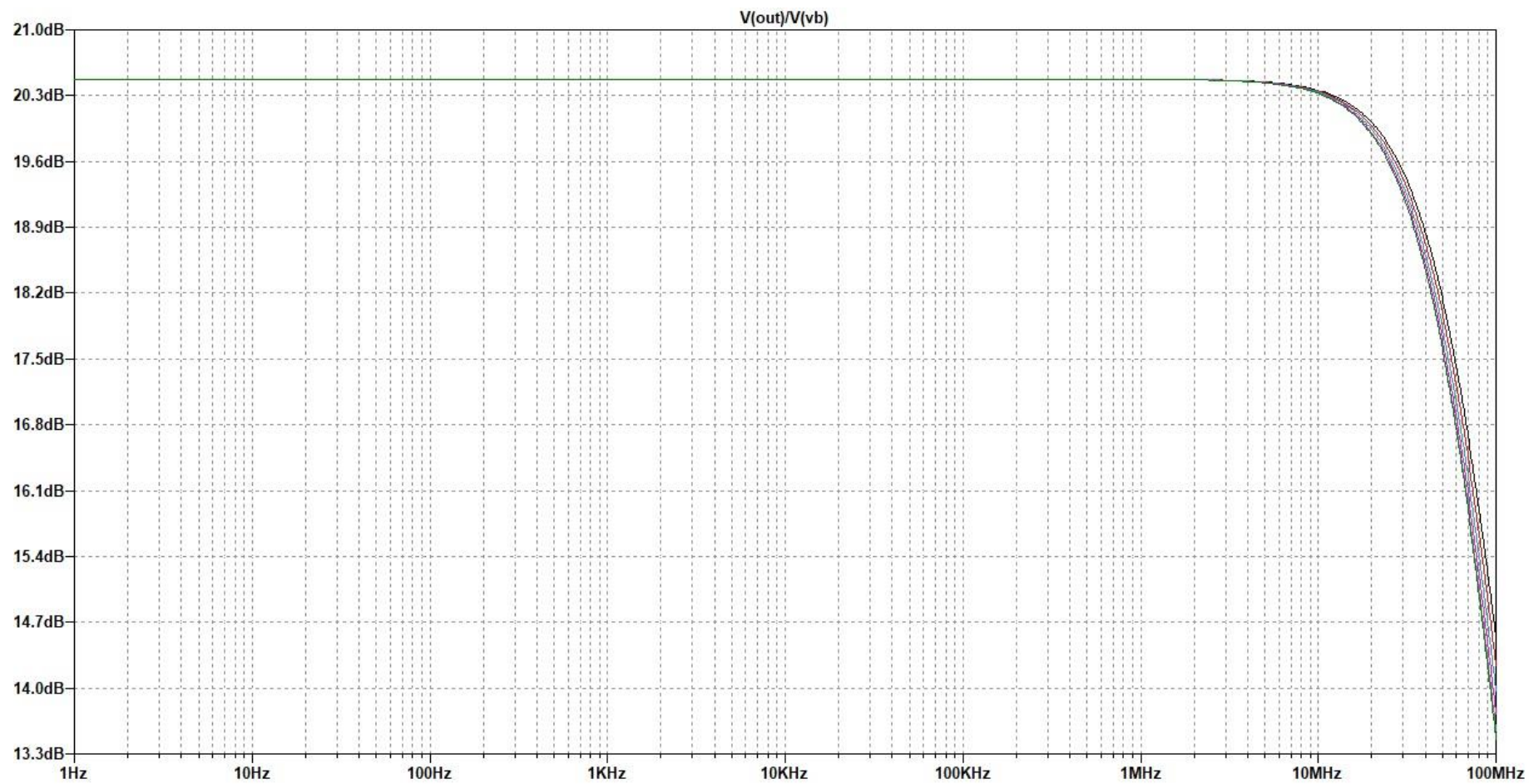


Figura 9 - Resposta em Frequência sem carga com variação de temperatura

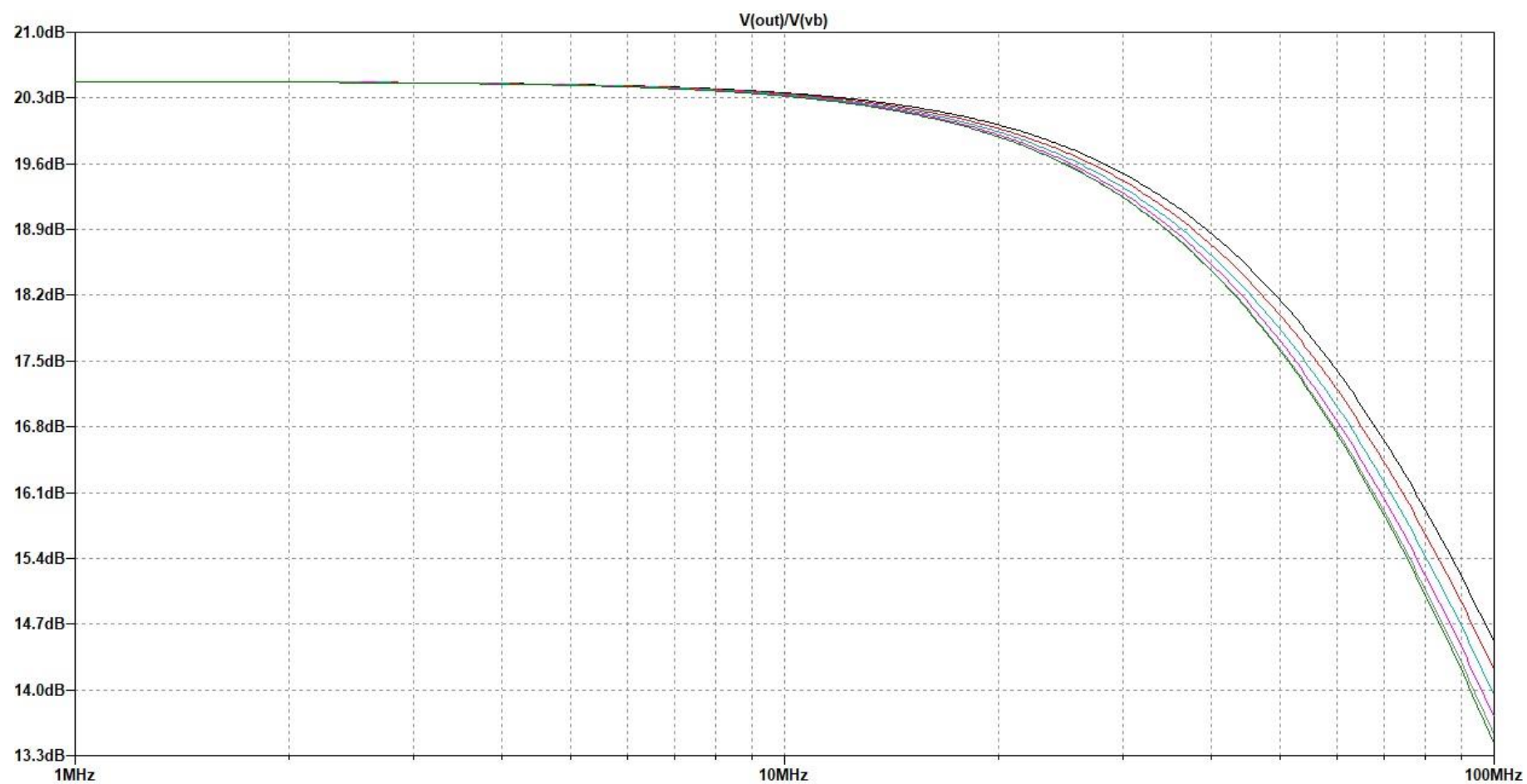


Figura 10 - Zoom da Resposta em Frequência sem carga com variação de temperatura

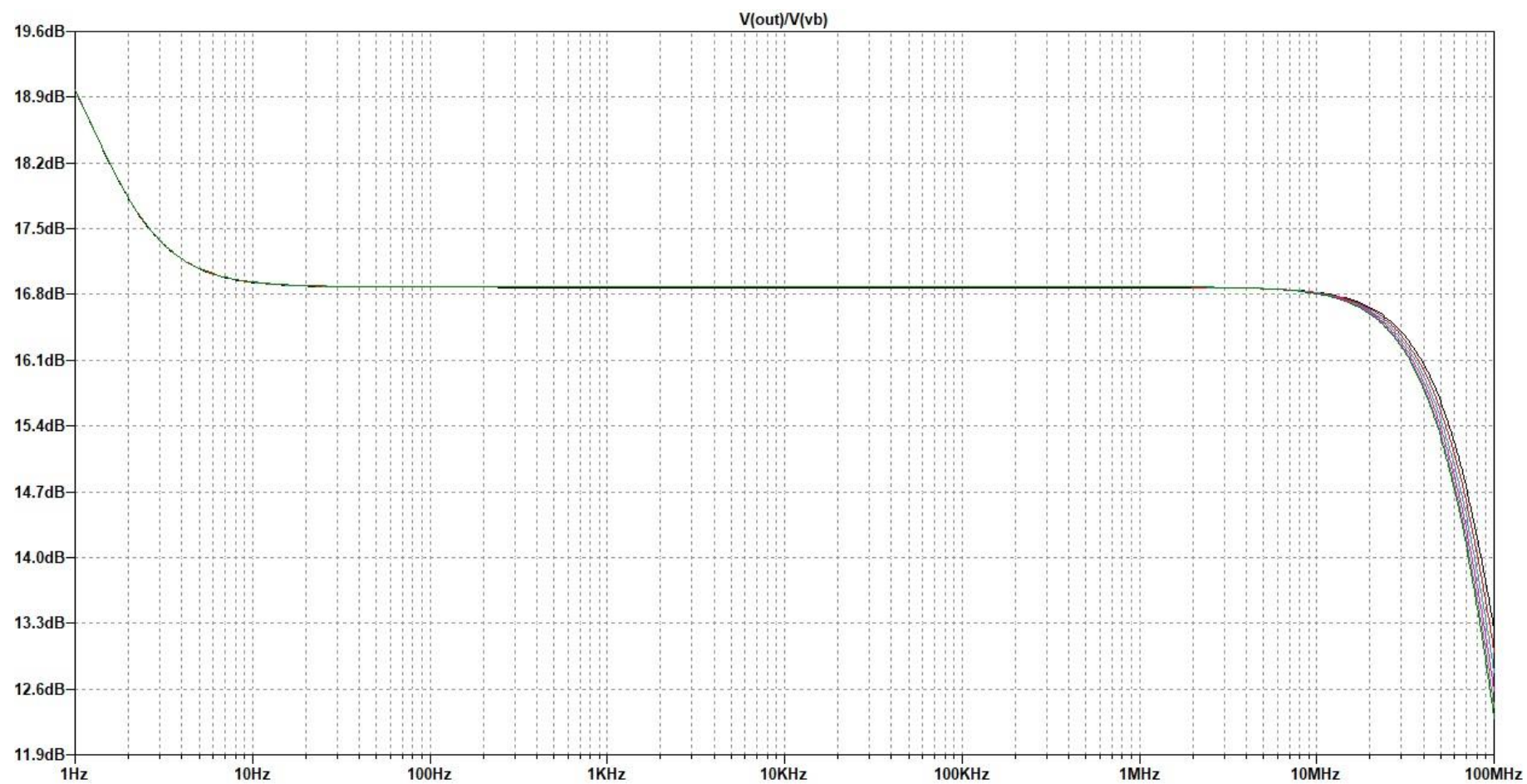


Figura 11 - Resposta em Frequência com carga com variação de temperatura

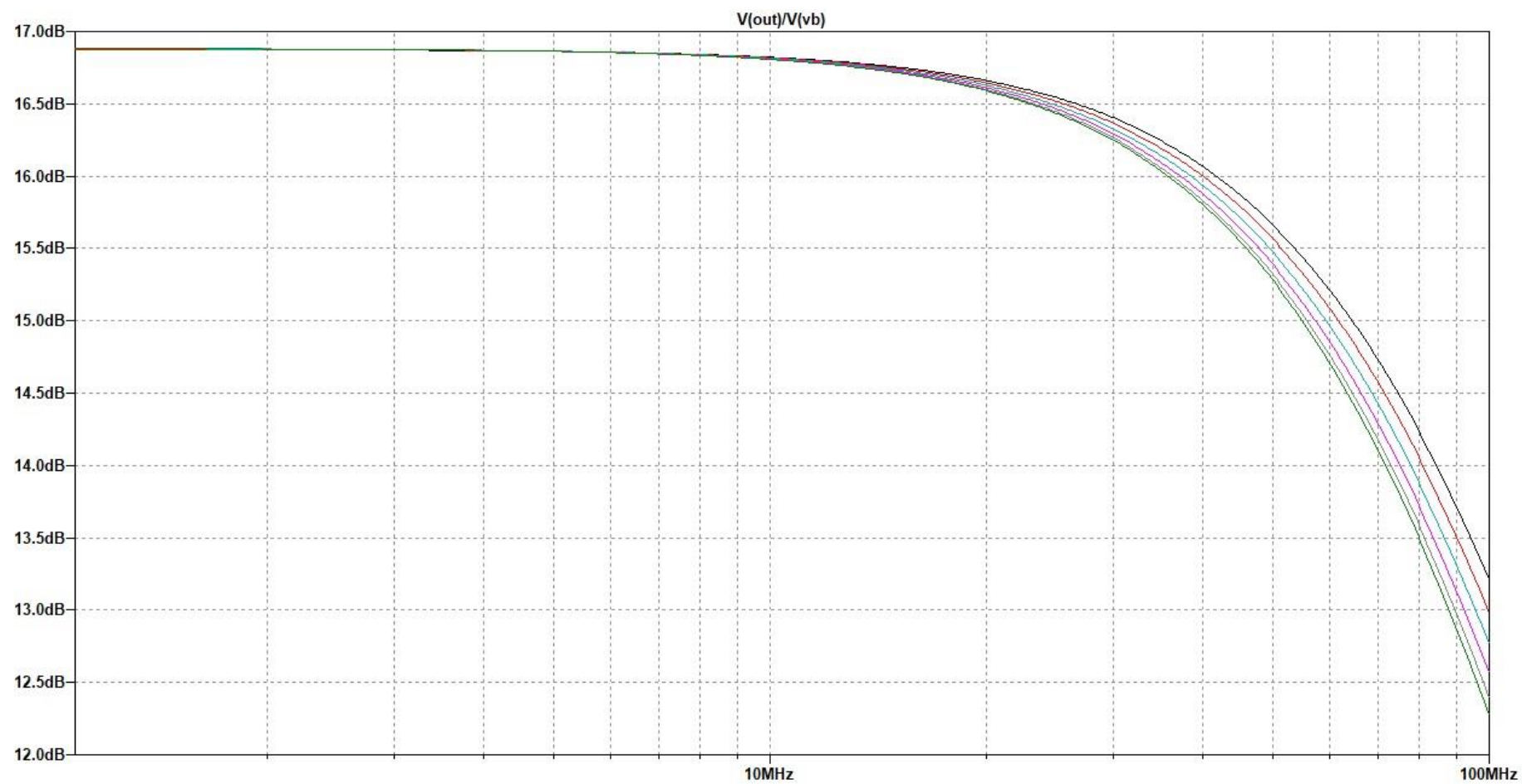


Figura 12 - Zoom da Resposta em Frequência com carga com variação de temperatura