• We might define the meaning for the recursive declaration

wp(**proc** 
$$p(\vec{x}, \vec{y}, \vec{z}); \mathcal{B}, \phi)$$

similarly to Eq. (23) for **do** roughly as follows:

- We start computing

$$wp(\mathcal{B}, \phi)$$

for its body  $\mathcal{B}$  as usual.

- When we encounter a recursive call, then we can calculate

$$\begin{aligned} &\operatorname{wp}(p(\vec{a}, \vec{b}, \vec{c}), \psi) \\ &= \operatorname{wp}((\vec{x}, \vec{y} := \vec{a}, \vec{b}; \mathcal{B}; \vec{b}, \vec{c} := \vec{y}, \vec{z}), \psi) \\ &= \operatorname{wp}((\vec{x}, \vec{y} := \vec{a}, \vec{b}), \underbrace{\operatorname{wp}((\mathcal{B}; \vec{b}, \vec{c} := \vec{y}, \vec{z}), \psi)}_{\text{The repeating part.}} \end{aligned}$$

but no further: re-entering  $\mathcal{B}$  would just lead us into this same situation again and again, and our formula would just get longer and longer.

- Let us now assume (analogously to induction) that we already knew the weakest precondition  $\mathcal{X}$  we are currently constructing.
- Although we do not know what this  $\mathcal{X}$  is actually like, we do know that it must be strong enough to validate this call.
- Hence we replace this repeating part with the implication

$$\mathcal{X} \Longrightarrow \operatorname{wp}((\vec{b}, \vec{c} := \vec{y}, \vec{z}), \psi).$$

- This way we do get a finite formula  $\varphi$ , but it has placeholders  $\mathcal{X}$  which must be filled.
- They are filled with a new atomic formula  $\mathcal{G}^p_{\phi}(k)$  meaning "calls to p restricted to recursion depth  $\leq k$ ":

$$\mathcal{G}^{p}_{\phi}(0) \Longleftrightarrow \operatorname{wp}(\mathcal{B}[p(\dots) \leftarrow \mathbf{abort}], \phi)$$
$$\mathcal{G}^{p}_{\phi}(k+1) \Longleftrightarrow \mathcal{G}^{p}_{\phi}(0) \vee \varphi[\mathcal{X} \leftarrow \mathcal{G}^{p}_{\phi}(k)]$$

Then the desired exact meaning is again

$$\exists k \in \mathbb{N} : \mathcal{G}^p_\phi(k)$$

but it is more involved than just stating and verifying the sufficient precondition we had in mind when designing p.

# 5.4 A Recursive Example: Quicksort

• Let us verify the well-known *quicksort* (Dromey, 1989, Chapter 9.5) sorting algorithm as another example of a recursive algorithm:

```
{ pre: A = a \land lower(a) \le l \land u \le upper(a)
post: perm(A, a) \land same(A, a, l, u) \land ordered(a[l . . . u])
bound: u - l }
proc quicksort(\mathbf{value}\ l\ ,\ u : \mathbb{Z};
```

```
\begin{array}{c} \textbf{value result} \ a \colon \ \textbf{array of} \ \tau);\\ \textbf{if} \ \ l < u \rightarrow\\ partition(l,u,a,m);\\ quicksort(l,m-1,a);\\ quicksort(m+1,u,a)\\ \boxed{\mid} \ \ l \geq u \rightarrow\\ \textbf{skip} \\ \textbf{fi} \end{array}
```

Recall the use of the ghost variable A to retain the original value of the input-output parameter a.

• Here the formula same(a, A, l, u) means "arrays a and A are the same outside the indices  $l \dots u$ ". This can be expressed as

```
lower(a) = lower(A) \land upper(a) = upper(A) \land\forall lower(a) \le i \le upper(a) : i < l \lor i > u \Longrightarrow a[i] = A[i].
```

• It relies on the nonrecursive procedure

```
{ pre: B = b \land lower(b) \le p \land p < q \land q \le upper(b) post: perm(B,b) \land same(B,b,p,q) \land p \le r \land r \le q \land b[p \dots r] \le b[r \dots q] } proc partition(value p, q: \mathbb{Z}; value result b: array of \tau; result r: \mathbb{Z});
```

The key to quicksort *efficiency* is to realize that it can be done with only  $\mathcal{O}(q-p)$  steps. But as far as its *correctness* goes, these pre- and postconditions suffice.

• Let us first verify the main call

to sort the given array x:

**Precondition** becomes

$$X = x \land \underbrace{\mathrm{lower}(x) \leq \mathrm{lower}(x)}_{\mathrm{TRUE}} \land \underbrace{\mathrm{upper}(x) \leq \mathrm{upper}(x)}_{\mathrm{TRUE}}.$$

Postcondition becomes

$$perm(X,x) \wedge \overbrace{same(X,x, lower(x), upper(x))}^{lower(X) = lower(X) + upper(X)} \wedge ordered(x).$$

Hence the **result**ing value of x is indeed an ordered permutation of its initial value X.

• Let us then verify the nonrecursive branch:

- The postcondition becomes

$$\operatorname{perm}(A,a) \wedge \underbrace{\operatorname{same}(A,a,l,u)}_{A=a \text{ and}} \wedge \underbrace{\operatorname{ordered}(a[l\dots u])}_{\text{at most one element}}$$

since  $l \geq u$  by this **if** guard.

- It is implied by the A = a already in the precondition.
- Hence this **skip** suffices.
- Verifying the recursive branch needs some way of talking about the current contents of array a at each step.

Let us introduce two new ghost variables  $A_1$  and  $A_2$  for this purpose, where each  $A_j$  is array a just before the jth recursive quicksort call in the source code.

- Then we take suitable fresh copies of the procedure specifications using these ghost variables for array a.
- The specification copy for the *partition* call is obtained by the following textual replacements:

 $B \leftarrow A$  since the incoming value of array b is the incoming value of array a.

 $b \leftarrow A_1$  since the **resulting** value of array b becomes the incoming **value** of array a for the 1st recursive call.

```
p \leftarrow l, \ q \leftarrow u \text{ and } r \leftarrow m \text{ from the call itself.}
```

We get:

```
{ pre: A = A_1 \wedge lower(A_1) \leq l \wedge l \leq u \wedge u \leq upper(A_1)

post: perm(A, A_1) \wedge same(A, A_1, l, u) \wedge

l \leq m \wedge m \leq u \wedge A_1[l \dots m] \leq A_1[m \dots u] }
```

- This precondition is ensured partly by the *quicksort* precondition and partly by the guard of this **if** branch.
- Hence this call is permitted, and so we can later assume its postcondition.
- Since we are now separating the different contents of array a from each other (via the ghost variables A,  $A_1$  and  $A_2$ ), we can add this postcondition into the already collected facts.
- That is, our verification of this branch has now proceeded as far as...

• Reassigning into the same variable (here array a) can falsify formulas that were true before the assignment. Separating its successive values via ghost variables (here A,  $A_1$  and  $A_2$ ) allows us to keep them true instead, as they speak of its past values.

• The next step is the 1st quicksort call. We take a copy of its specification with textual replacements  $A \leftarrow A_1$ ,  $a \leftarrow A_2$ ,  $l \leftarrow l$  (since l does not change in this call) and  $u \leftarrow m-1$ :

```
{ pre: A_1 = A_2 \land lower(A_2) \le l \land m - 1 \le upper(A_2)
post: perm(A_1, A_2) \land same(A_1, A_2, l, m - 1) \land
ordered(A_2[l \dots m - 1])
bound: (m - 1) - l }
```

- The *quicksort* bound must be greater than zero and decrease:

the if guard 
$$u - l > 0 \land (m - 1) - l < u - l \Leftrightarrow m - 1 < u$$
  $\Leftrightarrow m \le u$ 

which in turn appeared with the addition of the partition postcondition.

- The other claims in this precondition can be seen from this  $m \leq u$  and the *quicksort* precondition. Hence this call is permitted, and so we can add its postcondition too.
- The last step is the 2nd quicksort call. We take a copy of its specification with textual replacements  $A \leftarrow A_2$ ,  $a \leftarrow a$ ,  $l \leftarrow m+1$  and  $u \leftarrow u$ :

```
{ pre: A_2 = a \land lower(a) \le m + 1 \land u \le upper(a)
post: perm(A_2, a) \land same(A_2, a, m + 1, u) \land
ordered(a[m + 1 \dots u])
bound: u - (m + 1) }
```

The bound

the if guard 
$$u-l > 0 \land u - (m+1) < u-l \Leftrightarrow m+1 > l$$
  $\Leftrightarrow m \geq l$ 

and the other claims in this postcondition can be seen as in the preceding step. Hence this call is permitted too, and we can add its postcondition too.

• Let us now use these added conjuncts to verify the three conjuncts which form the *quicksort* postcondition:

```
perm(A, a) because we have added perm(A, A_1), perm(A_1, A_2) and perm(A_2, a). same(A, a, l, u) because we have added same(A, A_1, l, u), same(A_1, A_2, l, m-1) and same(A_2, a, m+1, u).
```

 $ordered(a[l \dots u])$  is a slightly more involved inference:

- We have ordered(a[l...m-1]) by  $ordered(A_2[l...m-1])$  and  $same(A_2, a, m+1, u)$ .
- Similarly we also have ordered(a[m+1...u]) by the the 2nd quicksort call postcondition.
- Next we note that perm(A, a) and same(A, a, l, u) together imply that  $perm(A[l \dots u], a[l \dots u])$ .

(We could also have stated this directly in the *quicksort* postcondition at the expense of making the *perm* inference above slightly trickier.)

- In the 1st quicksort call this note gives  $perm(A_1[l \dots m-1], A_2[l \dots m-1])$ , from which  $same(A_2, a, m+1, u)$  gives  $perm(A_1[l \dots m-1], a[l \dots m-1])$ .
- In the 2nd it gives  $perm(A_2[m+1 \dots u], a[m+1 \dots u])$  and  $same(A_1, A_2, l, m-1)$  gives  $perm(A_1[m+1 \dots u], a[m+1 \dots u])$  from it.
- We also have  $a[m] = A_1[m]$  by  $same(A_1, A_2, l, m-1)$  and  $same(A_2, a, m+1, u)$
- Hence we can appeal to the  $A_1[l ext{...} m] ext{ } ext{...} ext{ } ext{u}]$  in the partition post-condition, which takes care of the element  $A_1[m]$  gluing these two ordered parts together.

This completes our verification of this remaining quicksort recursive **if** branch, and hence of the entire algorithm.

## 5.4.1 Separating Value and Result Parameters

• We verified our *quicksort* algorithm as if it had separate **result** parameters:

```
{ pre: lower(a) \leq l \land u \leq \text{upper}(a) post: perm(\boldsymbol{c}, a) \land same(\boldsymbol{c}, a, l, u) \land ordered(\boldsymbol{c}[l \dots u]) bound: u - l }

proc quicksort2 (value l, u: \mathbb{Z};

value a: array of \tau;

result \boldsymbol{c}: array of \tau);

if l < u \rightarrow

partition2(l, u, a, m, \boldsymbol{a_1});

quicksort2(l, m - 1, a_1, \boldsymbol{a_2});

quicksort2(m + 1, u, a_2, \boldsymbol{c})

\parallel l \geq u \rightarrow

skip

fi
```

Then these local code variables  $a_1$  and  $a_2$  concretize the ghost variables  $A_1$  and  $A_2$ .

• The practical reason why we kept reassigning into array a instead of introducing these local arrays  $a_1$  and  $a_2$  was execution time and space optimization – eliminating unnecessary copying of unmodified array elements.

(Except that the call-by-value return parameter passing convention of GCL copies anyway...)

- This original quicksort is an in-place sorting algorithm, and an efficient one at that.
- On the other hand, there was a design time cost:
  - The correctness proof introduced  $A_1$  and  $A_2$  not present in the code, so the proof and code diverged from each other.
  - Note also that they and their concrete counterparts  $a_1$  and  $a_2$  have specific explanations, as suggested in the Variable Introduction Principle (Principle 6), so their explicit introduction would also be justified.

We should worry about correctness first, and about efficiency only if turns out to be necessary later.

- Using the same memory locations for both input and output parameters can also introduce subtle aliasing bugs:
  - Suppose we have a written a library routine

which somehow incorporates into y the information in x.

- Most practical programming languages (unlike GCL) would pass this y by reference so that it can be modified "on the fly" rather than at the end of mystic.
- Suppose then that a user of our library calls mystic(z, z). Is the outcome guaranteed to be correct also in this case? Or will some old input element z[i] get overwritten by the corresponding new output element z[i] before its old value is needed for computing some other output element z[j]?
- The classic example is the low-level routine copy(p, q, n) which copies n bytes from the area starting at address p into an area starting at address q. What happens if these two areas overlap?

**Principle 9** (Separate Inputs and Outputs). Correctness reasoning becomes simpler if procedures allocate and construct their results themselves and "from scratch", instead of reassigning them into the parameters supplied by their callers.

Hence procedures should be designed with separate input and output parameters. If performance dictates, then they can be later combined – at a risk: What if our procedure accidentally modifies something which the caller (or some other part of the whole program) expects to remain unmodified?

(In GCL design time terms, use separate **value** and **result** parameters in your first version. If you really need some efficiency-related property such as in-place sorting, then revise your design to combine them – but remember the pitfalls!)

# 5.4.2 Pivot Partitioning

- Let us now develop the missing *partition* procedure according to its stated specification.
- Let us choose very naïvely the first element b[p] as our *pivot value* x around which to partition the input  $b[p \dots q]$ .
  - A better choice would for x be e.g. the *median of three* values b[p], b[(p+q) div 2] and b[q], since it is more likely to find a pivot near the "middle" of the input values b[p], b[p+1], b[p+2], ..., b[q] (Cormen et al., 2001, Chapter 7.2).
  - Or we could even allow the current pivot x to vary during the partition. This would lead into *interval* partitioning (Dromey, 1989, Chapter 9.5.2).
- Note also that the *partition* specification was intentionally just strong enough for *quicksort* correctness but no stronger: its postcondition just requires us to permute b[p...q] so that
  - -A[r] must be equal to our chosen x, and

- prefix  $b[p \dots r] \leq x$  and suffix  $b[r \dots q] \geq x$ , but
- the elements equal to x can be in either of these two parts.

This gives us more flexibility is designing our procedure body.

• Let us start massaging its stated postcondition:

We do not repeat this part below.  $\overbrace{perm(B,b) \wedge same(B,b,p,q) \wedge p \leq r \wedge r \leq q \wedge}^{\text{We do not repeat this part below.}}$ 

$$b[p \dots r] \leq b[r \dots q].$$

 $\bullet$  Let us introduce new variables i and j such that

$$b[p\dots i]$$
 is the prefix  $\leq x$  
$$b[j\dots q]$$
 is the suffix  $\geq x$  
$$b[i+1\dots j-1]$$
 is the still unprocessed part between them this far:

$$b[p \dots i] \le x \wedge b[j \dots q] \ge x \underbrace{\wedge x = b[r] \wedge i = r \wedge j = r}_{\text{Drop these to get the invariant.}}.$$

• Our procedure body gets the following form:

```
x, i, j := b[p], p, q+1;

{ inv: b[p \dots i] \le x \land b[j \dots q] \ge x

bound: length of unprocessed part b[i+1 \dots j-1] }

do i+1 \le j-1 \rightarrow

shorten the unprocessed part

od;

r, b[p], b[i] := i, b[i], b[p]
```

Note that the loop initialization ensures that the prefix is nonempty. Hence the part after the loop can safely take its last index i as the **return**ed index r.

- One way to shorten the unprocessed part is by incrementing i.
  - Calculating wp(inv, i := i + 1) yields that  $b[i + 1] \le x$  must hold for it to be allowed.
  - This i+1 is a valid index by the loop guard and  $j-1 \le q \le \text{upper}(p)$ . Here the first inequality comes from the initialization of j and not incrementing it during the loop. The second comes in turn from the partition precondition.
- Similarly we can also decrement j to get:

$$\begin{array}{lll} x\,, & i\,, & j\,:=\,b[p]\,, & p\,, & q+1\,;\\ \{\,\, \mathrm{inv}\colon b[p\ldots i] \leq x \wedge b[j\ldots q] \geq x \\ & \,\, \mathrm{bound}\colon \, \mathrm{length} \,\, \mathrm{of} \,\, \mathrm{unprocessed} \,\, \mathrm{part} \,\, b[i+1\ldots j-1]\,\,\}\\ \mathbf{do} \,\, i+1 \leq j-1 \to \\ & \,\, \mathbf{if} \,\,\, b[i+1] \leq x \\ & \,\, i\,:=\,i+1 \\ & \|\,\, b[j-1] \geq x \\ & \,\, j\,:=\,j-1 \end{array}$$

• This missing third case reduces to the preceding two by swapping b[i+1] and b[j-1].

```
{ pre: B = b \land lower(b) \le p \land p < q \land q \le upper(b)
  post: perm(B, b) \wedge same(B, b, p, q) \wedge
       p \le r \land r \le q \land b[p \dots r] \le b[r \dots q]
proc partition (value p, q: \mathbb{Z};
                   value result b: array of \tau;
                   result r : \mathbb{Z});
x, i, j := b[p], p, q+1;
\{ \text{ inv: } b[p \dots i] \le x \land b[j \dots q] \ge x \}
  bound: length of uprocessed part b[i+1...j-1]
do i+1 \leq j-1 \rightarrow
     if b[i+1] \leq x
         i := i + 1
      ||b[j-1] \ge x
         j := j - 1
      ||b[i+1] > x \wedge b[j-1] < x \rightarrow
         [i,j,b[i+1],b[j-1] := i+1,j-1,b[j-1],b[i+1]
     fi
od:
r, b[p], b[i] := i, b[i], b[p]
```

(Bentley and McIlroy (1993) provide a detailed account on engineering an efficient partitioning algorithm for quicksorting. In particular, they show how the idea of loop invariants is used in practical algorithm design: without expressing them in formal logic, yet reasoning about them systematically.)

#### 5.5 Iteration is Tail Recursion

• Note that the 2nd recursive *quicksort* call can be eliminated:

```
 \begin{array}{c} \mathbf{proc} \ \ quicksort(\mathbf{value} \ l, \ u \colon \mathbb{Z}; \\ \mathbf{value} \ \mathbf{result} \ a \colon \mathbf{array} \ \mathbf{of} \ \tau); \\ \mathbf{if} \ \ l < u \to \\ partition(l, u, a, m); \\ quicksort(l, m - 1, a); \\ quicksort(m + 1, u, a) \\ \parallel \ \ l \geq u \to \mathbf{skip} \\ \mathbf{fi} \\ \mathbf{can} \ \mathbf{be} \ \mathbf{rewritten} \ \mathbf{as} \\ \mathbf{proc} \ \ quicksort3(\mathbf{value} \ l, \ u \colon \mathbb{Z}; \\ \mathbf{value} \ \mathbf{result} \ \ a \colon \mathbf{array} \ \mathbf{of} \ \tau); \\ \mathbf{do} \ \ \ l < u \to \\ partition(l, u, a, m); \\ \end{array}
```

```
quicksort3(l, m - 1, a);
l := m + 1
od
```

• That is, we replaced

the current parameter values with the new ones in the call — here only l changes, u and a stay as they were

calling the procedure with repeating its body.

- This was possible because the eliminated call was the *last* thing *quicksort* would do before returning to its caller.
- Such calls are called *tail calls*.
  - A programming language implementation can perform a tail call without increasing its recursion stack depth.
  - It can namely pop the current activation record *before* pushing the activation record of this tail call.
  - Hence a tail call is an elegant "goto this procedure and report your results directly back to my caller, not me".
- If a recursive call is tail (like in here) then it is called *tail recursion*.
  - Tail recursion is the kind which permits elimination like this.
  - Or the converse view: loop constructs (such as do) are syntactic sugar for tail recursion, since that particular special case of general recursion is so common in practical programming.

(In fact, some programming languages such as Scheme (Dybvig, 1996) do not even provide separate iteration constructs, just general recursion. Instead the standard specifies that the implementation must eliminate tail calls. Then a programmer specifies iteration as tail recursion. This makes the programming language definition more uniform, and iteration becomes just a space-saving optimization instead of a separate idea.)

• The general tail recursion elimination transformation is from

```
\begin{array}{lll} \mathbf{proc} & p(\mathbf{value} \ \vec{x}; \ \mathbf{value} \ \mathbf{result} \ \vec{y}; \ \mathbf{result} \ \vec{z}); \\ \mathbf{if} & guard_1 \rightarrow \mathcal{B}_1; p(\vec{a_1}, \vec{y}, \vec{z}) \\ & \parallel & \vdots \\ & \parallel & guard_m \rightarrow \mathcal{B}_m; p(\vec{a_m}, \vec{y}, \vec{z}) \\ & \parallel & guard_{m+1} \rightarrow \mathcal{B}_{m+1} \ \parallel \dots \ \parallel & guard_n \rightarrow \mathcal{B}_n \\ \mathbf{fi} \end{array}
```

into

```
\begin{array}{lll} \mathbf{proc} & p(\mathbf{value} \ \vec{x}\,; \ \mathbf{value} \ \mathbf{result} \ \vec{y}\,; \ \mathbf{result} \ \vec{z}\,)\,; \\ \mathbf{do} & guard_1 \to \mathcal{B}_1; \vec{x} := \vec{a_1} \\ & \| & \vdots \\ & \| & guard_m \to \mathcal{B}_m; \vec{x} := \vec{a_m} \\ \mathbf{od}\,; \\ & \mathbf{if} & guard_{m+1} \to \mathcal{B}_{m+1} \ \| \dots \ \| \ guard_n \to \mathcal{B}_n \ \mathbf{fi} \end{array}
```

- That is, if a recursive call
  - is the last thing that this current branch performs before returning back to its caller
  - reports its **result**s  $\vec{y}$  and  $\vec{z}$  back to the caller unmodified

then it can be replaced with updating the **value** parameters  $\vec{x}$  into new values  $\vec{a_i}$  and looping.

In quicksort3 we updated l into m+1 before looping.

• The other branches are retained after the **do** loop.

In quicksort3 this part was

if 
$$l \geq u \rightarrow \mathbf{skip}$$
 fi

which could be further discarded since its guard is guaranteed to be TRUE because it is the opposite of the **do** loop guard.

• Because

wp(the original 
$$p, \phi$$
)

must ensure  $\phi$  in every possible recursion call order, it must ensure  $\phi$  especially in the orders where a tail recursive branch is chosen whenever possible. Hence

wp(the original 
$$p, \phi$$
)  $\Rightarrow$  wp(the transformed  $p, \phi$ )

so we can indeed use the transformed p instead wherever the original p is used. But the opposite

wp(the original 
$$p, \phi$$
)  $\Leftarrow$  wp(the transformed  $p, \phi$ )

is not necessarily true: the transformed p may permit more inputs than the original p, namely those for which  $\phi$  is ensured in the "tail recursion first" order but not in general.

• The opposite transformation to introduce tail recursion turns the loop

$$\begin{array}{ccc} \mathbf{do} & guard_1 \to \mathcal{B}_1 \\ & \vdots & \\ & guard_n \to \mathcal{B}_n \end{array}$$

into the call  $q(\vec{u})$ , where  $\vec{u}$  consists of all the variable names appearing in the loop and

proc 
$$q(\mathbf{value\ result\ }\vec{u});$$
  
if  $guard_1 \to \mathcal{B}_1; q(\vec{u})$   
 $[] :$   
 $[] guard_n \to \mathcal{B}_n; q(\vec{u})$   
 $[] \neg (guard_1 \lor ... \lor guard_n) \to \mathbf{skip}$   
fi

is the definition for the corresponding new tail recursive procedure.

- This connection between iteration and tail recursion explains the similarities between designing and verifying loops and recursive procedures:
  - Both have bounding expressions. In a do loop it bounds the number of still remaining iterations, in a recursive procedure the still allowed recursion call depth.

For tail recursion, these two notions coincide. This explains the former in terms of the latter.

- Their designs aim at decreasing their *bound*ing expressions to guarantee termination. Again, tail recursion shows that the idea is the same in both.
- The loop invariant and the precondition of a recursive procedure have the same role: both specify what must hold before each loop round or call.
- The loop guards and the guards for the nonrecursive branches have also similar roles: For a loop their negation must imply the postcondition together with its invariant. Similarly, a guard of a nonrecursive branch must imply the recursion postcondition together with its precondition.

## 5.5.1 Exponentiation Recursively

- Designing a recursive procedure can often be *simpler* than a corresponding loop which may come as a surprise, since usually loops are taught first, and recursion only later, and then as a seemingly complicated, esoteric and scary technique rarely needed and best avoided...!
- Let us redo our earlier fast exponentiation algorithm (Figure 10) from this viewpoint.
- That is, we start designing a recursive procedure

```
{ pre: the type for p post: y = x^p }

proc pow(value x:\mathbb{R};value p:\mathbb{N};result y:\mathbb{R});
\mathcal{B}
```

Note that we do not need the ghost variables X and P here, since x and p are value parameters.

- Next we must choose a *bound* for our procedure, so that we can start developing its body  $\mathcal{B}$  to decrease it.
- Parameters of type  $\mathbb{N}$  are natural candidates, since they have a value 0 where the recursion must stop, since they have no smaller values.
- Here we can try p:

```
{ pre: the type for p post: y = x^p bound: p}

proc pow(value x:\mathbb{R};value p:\mathbb{N};result y:\mathbb{R});

if p = 0 \rightarrow

terminating branch

p > 0 \rightarrow
```

fi

• We could continue with these two branches, and come up with a correct algorithm. Let us however split the recursive branch further into the odd and even branches as in our earlier algorithm:

The terminating branch is dictated by the postcondition:  $x^0 = 1$ .

• We develop the two recursive branches as follows:

```
{ pre: the type for p post: y = x^p bound: p }

proc pow(value x:\mathbb{R};value p:\mathbb{N};result y:\mathbb{R});

if p = 0 \rightarrow
y := 1
 \neg \text{even}(p) \rightarrow
pow(x, p - 1, z);
y := z \cdot x
 \neg \text{even}(p) \wedge (p > 0) \rightarrow
pow(x \cdot x, p \text{ div } 2, z)

fi
```

Note that verifying these branches is straightforward. E.g. in the first branch

- the bound is p > 0 by the guard and decreases to p 1, so the recursive call is permitted, and
- its postcondition is  $z = x^{p-1}$ , and hence the assignment makes  $y = x^p$  as required.

#### 5.5.2 Exponentiation Tail-Recursively

• However, the branch

```
\begin{array}{ll}
\neg \operatorname{even}(p) \to \\
pow(x, p - 1, z); \\
y := z \cdot x
\end{array}
```

of our procedure *pow* is not tail recursive, and hence it is not quite the same as a **do** loop.

- The intuition of this assignment is "the intermediate **result** z obtained from recursion must be further multiplied by the current x to produce a valid final **result** y from this call".
- This suggests adding a *new* parameter a = "the value with which I want you to multiply your answer, so that I don't have to do it myself after calling you". Compare it to a in our original **do** loop (Figure 10).
- This gives:

Here we are using the idea that modifying existing code is done by changing its specification and redeveloping to get it right.