### Floyd-Hoare Logic:

- A formal system for proving correctness
- A program operates on state moving through code changes state
- Hoare logic follows state changes through triples:

$$\{P\} \ C \ \{Q\}$$

where P is an assertion about the state before the execution of line C and Q is an assertion about the state after execution of C.

```
\{P\} \text{ nop } \{P\}
\{a == 1, a != b\} b := 2 \{a == 1, a != b\}
\{??\} a := b+1 \{a > 1\}
```

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```
{P} nop {P}

{a == 1, a != b} b := 2 {a == 1, a != b}

{??} a := b+1 {a > 1}

{b == 20} a := b+1 {a > 1}

{b > 10} a := b+1 {a > 1}

{b > 0} a := b+1 {a > 1}

Weakest precondition
```

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### Floyd-Hoare Logic:

#### • A loop invariant:

Used to prove loop properties. Assertions on state entering the loop and guaranteed to be true at every iteration of the loop.

The invariant will be the postcondition for the loop on exit.

```
while (x < 10) x = x+1
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```
while (x < 10) x = x+1
Start with this:
\{x <= 10\} while (x < 10) x = x+1 \{??\}
```

### Floyd-Hoare Logic:

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### • Example:

```
while (x < 10) x = x+1
```

Start with this:

$$\{x \le 10\}$$
 while  $(x \le 10)$   $x = x+1$   $\{??\}$ 

Move inside the test:

$$\{x < 10 \land x <= 10\} \ x = x+1 \ \{x <= 10\}$$

### Floyd-Hoare Logic:

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Start with this:

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Move inside the test:

$$\{x < 10 \land x <= 10\} \ x = x+1 \ \{x <= 10\}$$

Backing out:

$$\{x < 10 \land x <= 10\}$$
 while (x<10)  $x=x+1 \{\neg(x < 10) \land x <= 10\}$ 

### Floyd-Hoare Logic Rules:

### • Assignment:

Let P[E/x] mean that in predicate P, expression E is substituted for symbol x where x is a free variable.

$$\overline{\{P[E/x]\}\ x := E\ \{P\}}$$

In 
$$\{y == 10\}\ x := y+1 \ \{x == 11\}$$

$$P = \{x == 11\}$$

$$E/x = (y+1)/x = \{y+1\}$$

$$P[E/x] = \{y+1 == 11\} = \{y == 10\}$$

### Floyd-Hoare Logic Rules:

• Composition:

$$\frac{\{P\}\ C_1\ \{Q\},\ \{Q\}\ C_2\ \{R\}}{\{P\}\ C_1; C_2\ \{R\}}$$

### • Example:

In 
$$\{y == 10\}\ x := y+1 \ \{x == 11\}\ and$$
  
 $\{x == 11\}\ z := x+1 \ \{z == 12\}$ 

We can substitute

$${y == 10} x := y+1; z := x+1 {z == 12}$$

### Floyd-Hoare Logic Rules:

• Condition:

$$\frac{\{A \land B\} C_1 \{Q\}, \{\neg A \land B\} C_2 \{Q\}\}}{\{B\} \text{ if } A \text{ then } C_1 \text{ else } C_2 \{Q\}}$$

#### • Example:

In 
$$\{a == T \land b == 6 \land c == 10\} \ x := b$$
  
 $\{(x == 6 \land a == T) \lor (x == 10 \land a == F)\}$   
and  $\{a == F \land b == 6 \land c == 10\} \ x := c$   
 $\{(x == 6 \land a == T) \lor (x == 10 \land a == F)\}$ 

After applying the condition rule:

$$\{(b == 6 \ \land \ c == 10)\}$$
 If a == T then x := b; else x := c 
$$\{(x == 6 \ \land \ a == T) \ \lor \ (x == 10 \ \land \ a == F)\}$$

### Floyd-Hoare Logic Rules:

• Consequence:

$$\frac{P' \to P, \{P\} \ C \{Q\}, \ Q' \to Q}{\{P'\} \ C \{Q'\}}$$

### • Example:

Starting with:

$${P} = {x + 1 == 10} y := x + 1 {y == 10} = {Q}$$

and

$$P' = (x == 9 \rightarrow x + 1 == 10),$$
  
 $Q' = (x == 9 \land y == 10 \rightarrow y == 10),$ 

application of the consequence rule gives:

$$\{x == 9\} \ y := x + 1 \ \{x == 9 \land y == 10\}$$

### Floyd-Hoare Logic Rules:

• While:

$$\frac{\{P \land A\} C \{P\}}{\{P\} \text{ while } A \text{ do } C \{\neg A \land P\}}$$

### • Example:

Starting with:

$$\{x < 10 \land x <= 10\} \ x = x + 1; \ \{x <= 10\}$$

After application of the while rule:

$$\{x \le 10\}$$
 while  $(x < 10)$   $x = x + 1$ ;  $\{\neg(x < 10) \land x <= 10\}$ 

After application of the consequence rule:

$$\{x \le 10\}$$
 while  $(x < 10)$   $x = x + 1$ ;  $\{x = 10\}$ 

#### Weakest Precondition:

If Q is a predicate on states and C is a code fragment, then the weakest precondition for C with respect to Q is a predicate that is true for precisely those initial states in which C must terminate and must produce a state satisfying Q.

#### **Notation:**

wp(C,Q) - weakest precondition wrt Q, given code C.

### Example:

$$\{P\} \ \ {\bf a}:={\bf b}+{\bf 1} \ \ \{{\bf a}>{\bf 11}\}$$
 
$$P={\bf wp}({\bf a}:={\bf b}+{\bf 1},\ {\bf a}>{\bf 11})={\bf b}>{\bf 10}$$

#### Nomenclature:

wp is called a predicate transformer

#### Weakest Precondition:

### Example:

$${Q(y+3*z-5)}$$
  $x := y+3*z-5$   ${Q(x)}$ 

Let v be the value assigned to x.

If  $Q(\mathbf{x})$  is true after assignment so is Q(v) (before and after)

Then Q(y + 3 \* z - 5) is true initially

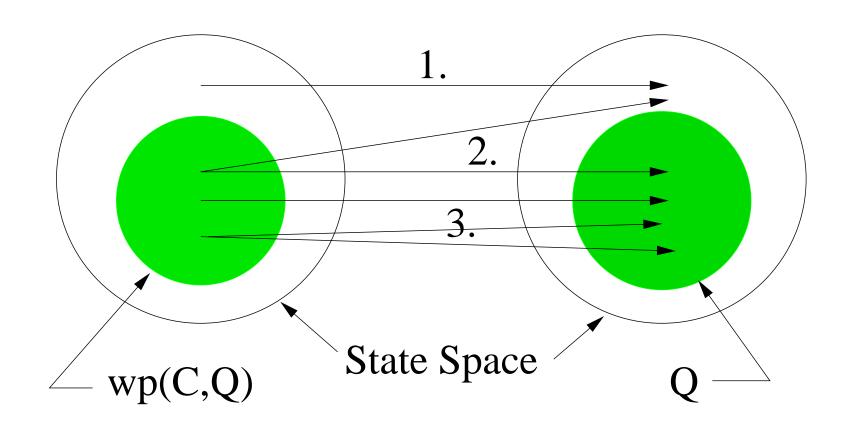
So, Q(x) holds after assignment iff Q(y + 3 \* z - 5) held

#### **Usefulness:**

Any predicate that implies wp(C,Q) also implies Q

Hence, if C is an entire program and Q specifies a property of C and if  $\mathsf{wp}(C,Q)$  is known to be the weakest precondition from the initial state, ACL2 can be used to determine input values that cause Q to be true.

#### Weakest Precondition:



- 1. from initial state to state not satisfying Q
- 2. from initial state to some state statisfying Q
- 3. from initial state to states statisfying Q

### **Strongest Postcondition:**

If P is a predicate on states and C is a code fragment, then the strongest postcondition for C with respect to P is a predicate that is implied by P when acted upon by C

#### **Notation:**

sp(C, P) - strongest postcondition wrt P, given code C.

### Example:

$$\{ {\tt b} > {\tt 10} \} \ {\tt a} := {\tt b} + {\tt 1} \ \{ Q \}$$
 
$$Q = {\tt sp}({\tt a} := {\tt b} + {\tt 1}, \ {\tt b} > {\tt 10}) = {\tt a} > {\tt 11}$$

#### Nomenclature:

sp is also called a predicate transformer

#### **Predicate Transformers:**

- PT semantics are a reformulation of Floyd-Hoare logic
- Used to implement an effective process for transforming F-H logic to predicate logic (so ACL2 can operate on it).

### WP - nop:

$$wp(nop, Q) = Q$$

#### WP - abort:

$$wp(abort, Q) = false$$

### WP - assignment:

$$\begin{aligned} & \operatorname{wp}(x := E, Q) = \forall z.z == E \to Q[z/x] \\ & \operatorname{wp}(x := E, Q) = Q[E/x] \\ & \operatorname{ex.:} \ \operatorname{wp}(x := x + 1, x > 10) = x + 1 > 10 \Leftrightarrow x > 9 \end{aligned}$$

### WP - composition:

```
\begin{split} \operatorname{wp}(C_1; C_2, Q) &= \operatorname{wp}(C_1, \operatorname{wp}(C_2, Q)) \\ \operatorname{ex.:} \ \operatorname{wp}(\mathbf{x} := \mathbf{x} + 2; \mathbf{y} := \mathbf{y} - 2, (\mathbf{x} + \mathbf{y} == 0)) \\ &= \operatorname{wp}(\mathbf{x} := \mathbf{x} + 2, \operatorname{wp}(\mathbf{y} := \mathbf{y} - 2, (\mathbf{x} + \mathbf{y} == 0))) \\ &= \operatorname{wp}(\mathbf{x} := \mathbf{x} + 2, (\mathbf{x} + (\mathbf{y} - 2) == 0)) \\ &= ((\mathbf{x} + 2) + \mathbf{y} - 2) == 0 \\ &= \mathbf{x} + \mathbf{y} == 0 \end{split}
```

#### WP - condition:

$$\begin{split} & \text{wp}(\text{if $E$ then $C_1$ else $C_2,Q) = (E \to wp(C_1,Q)) \ \land \ (\neg E \to wp(C_2,Q))) \\ & \text{ex.: } \text{wp}(\text{if $x > 2$ then $y := 1$ else $y := -1$, $(y > 0))} \\ & = ((x > 2) \to \text{wp}(y := 1, (y > 0))) \land \\ & (\neg (x > 2) \to \text{wp}(y := -1, (y > 0))) \\ & = ((x > 2) \to (1 > 0)) \land (\neg (x > 2) \to (-1 > 0)) \\ & = ((x > 2) \to T) \land (\neg (x > 2) \to F)) \\ & = ((x > 2) \to T) \land ((x > 2) \lor F)) \\ & = (x > 2) \end{split}$$

#### WP - while:

$$\begin{array}{ll} \operatorname{wp}(\operatorname{while} E \text{ do } C, \ Q) = \\ I \wedge & \operatorname{invariant \ true \ before \ execution} \\ \forall y.((E \wedge I) \to \operatorname{wp}(C, I \wedge (x < y)))[y/x] \wedge & \operatorname{invariant \ and \ variant \ preserved} \\ \forall y.((\neg E \wedge I) \to Q)[y/x] & Q \text{ holds on exit} \\ \end{array}$$

where < is well-founded, y represents the state before loop execution

### Alternatively,

wp(while 
$$E$$
 do  $C$ ,  $Q$ ) =  $\exists k. \ (k \ge 0) \land P_k$  where  $P_0 = \neg E \land Q$  and  $P_{k+1} = E \land \text{wp}(C, P_k)$ 

#### Example:

$$\begin{split} & \text{wp}(\text{while n} > 0 \text{ do n} := \text{n} - 1, \ (\text{n} == 0)) \\ & P_0 = \neg(\text{n} > 0) \ \land \ (\text{n} == 0) = (\text{n} == 0) \\ & P_1 = (\text{n} > 0) \ \land \ \text{wp}(\text{n} := \text{n} - 1, (\text{n} == 0)) = (\text{n} == 1) \\ & P_2 = (\text{n} > 0) \ \land \ \text{wp}(\text{n} := \text{n} - 1, (\text{n} == 1)) = (\text{n} == 2) \dots \\ & \exists k. \ (k \ge 0) \land P_k = (\text{n} >= 0). \end{split}$$

### Example:

```
\begin{array}{lll} \operatorname{wp}(\operatorname{while} \, \mathrm{n} \, ! = \, 0 \, \operatorname{do} \, \mathrm{n} := \mathrm{n} - 1, \, \, (\mathrm{n} == 0)) \\ P_0 = \neg (\mathrm{n} \, ! = \, 0) \, \wedge \, \, (\mathrm{n} == 0) = (\mathrm{n} == 0) \\ P_1 = (\mathrm{n} \, ! = \, 0) \, \wedge \, \, \operatorname{wp}(\mathrm{n} := \mathrm{n} - 1, (\mathrm{n} == 0)) = (\mathrm{n} == 1) \\ P_2 = (\mathrm{n} \, ! = \, 0) \, \wedge \, \, \operatorname{wp}(\mathrm{n} := \mathrm{n} - 1, (\mathrm{n} == 1)) = (\mathrm{n} == 2) \ldots \\ \exists k. \, (k \geq 0) \wedge P_k = (\mathrm{n} >= 0). \end{array}
```

### Example:

```
1+3+5\ldots+n=((n+1)/2)^2
\forall n. \ (n \ge 1) \to \sum_{i=1}^{n} (2 * i - 1) = n^2
A program for implementing this:
   \{(n >= 0)\}
   i := 0; s := 0;
   while i != n do
       i := i+1; s := s + (2*i - 1);
   \{(s == n*n)\}
P_0 = ((i == n) \land (s == n * n))
P_1 = ((i == n - 1) \land (s == i * i))
P_2 = ((i == n - 2) \land (s == i * i))...
P_i = ((i == n - j) \land (s == i * i))
wp(while..., (s == n * n)) = ((i <= n) \land (s == i * i))
wp(i := 0; s := 0, ((i <= n) \land (s == i * i)) = ((0 <= n) \land (s == 0))
```

### Computing $P_1$ :

$$egin{aligned} P_1 &= (\mathtt{i} \, ! = \, \mathtt{n}) \ \land \ \mathtt{wp}(\mathtt{i} := \mathtt{i} + \mathtt{1}; \ \mathtt{s} := \mathtt{s} + \mathtt{2} * \mathtt{1} - \mathtt{1}, ((\mathtt{i} == \mathtt{n}) \land (\mathtt{s} == \mathtt{n} * \mathtt{n})) \ &= (\mathtt{i} \, ! = \, \mathtt{n}) \ \land \ \mathtt{wp}(\mathtt{i} := \mathtt{i} + \mathtt{1}, ((\mathtt{i} == \mathtt{n}) \land (\mathtt{s} + \mathtt{2} * \mathtt{i} - \mathtt{1} == \mathtt{n} * \mathtt{n})) \ &= (\mathtt{i} \, ! = \, \mathtt{n}) \ \land \ \mathtt{wp}(\mathtt{i} := \mathtt{i} + \mathtt{1}, ((\mathtt{i} == \mathtt{n}) \land (\mathtt{s} == (\mathtt{i} - \mathtt{1}) * (\mathtt{i} - \mathtt{1})) \ &= (\mathtt{i} \, ! = \, \mathtt{n}) \ \land \ (\mathtt{i} + \mathtt{1} == \mathtt{n}) \land (\mathtt{s} == (\mathtt{i} + \mathtt{1} - \mathtt{1}) * (\mathtt{i} + \mathtt{1} - \mathtt{1})) \end{aligned}$$

### Useful special cases:

 $\operatorname{wp}(C,Q) = T$  C terminates with Q from any initial state  $\operatorname{wp}(C,T) = T$  C terminates from any initial state  $\operatorname{wp}(C,Q) = F$  C produces a state where Q is false from any initial state that results in C terminating  $\operatorname{wp}(C,T) = F$  C does not terminate from any initial state

#### Other properties:

$$\begin{split} \{ \operatorname{wp}(C,Q) \} \ C \ \{Q\} \\ \operatorname{wp}(C,F) &= F \\ \operatorname{wp}(C,Q \ \land \ R) = (\operatorname{wp}(C,Q) \ \land \ \operatorname{wp}(C,R)) \\ \text{if} \ Q \ \to \ R \ \text{then} \ \operatorname{wp}(C,Q) \ \to \ \operatorname{wp}(C,R) \end{split}$$

### A multiply program:

```
{ F1=F1SAVE & F1<2**bits & F2<2**bits & LOW<2**bits }
```

```
LDX #bits
                        load the X register immediate with number bits
        LDA #0
                        load the A register immediate with the value 0
        ROR F1
                        rotate F1 right circular through the carry flag
LOOP
                         branch on carry flag clear to ZCOEF
        BCC ZCOEF
        CLC
                        clear the carry flag
        ADC F2
                         add with carry F2 to the contents of A
ZCOEF ROR A
                        rotate A right circular through the carry flag
                        rotate LOW right circular through the carry flag
        ROR LOW
        DEX
                        decrement the X register by 1
        BNE LOOP
                         branch if X is non-zero to LOOP
```

• A is an 8 bit accumulator - for the high bits of the multiply

{ LOW + 2\*\*bits\*A = F1SAVE\*F2 }

- LOW is an 8 bit accumulator for the low bits of the multiply
- Right rotation of A and then LOW is a 16 bit right rotation through both
- There are Z and C bits, and registers X, F1, F2
- ADC adds F2 and the C bit to A

### Setup weakest preconditions:

```
{ F1=F1SAVE & F1<256 & F2<256 & LOW<256 }
        LDX #bits S_1 \equiv [8/X]S_2()
        LDA #0 S_2 \equiv [0/A]S_3()
        ROR F1 S_3 \equiv [E_4/\text{F1}, E_8/\text{C}]S_4()
LOOP
        BCC ZCOEF S_4 \equiv \text{if } C == 0 S_5() \text{ else } S_7()
                    S_5 \equiv [0/\mathtt{C}]S_6()
         CLC
         ADC F2
                          S_6 \equiv [E_3/A, E_7/C, E_9/Z]S_7()
                  S_7 \equiv [E_2/A, E_6/C]S_8()
ZCOEF ROR A
         ROR LOW S_8 \equiv [E_1/LOW, E_5/C]S_9(X, LOW, C)
                          S_9(X, LOW, C) \equiv [X - 1/X]S_{10}(X, LOW, C)
         DEX
         BNE LOOP
                          S_{10}(X) \equiv \text{if Z} == 0 S_3()
                                   else \{LOW+256*A == F1SAVE*F2\}
         Q = \{ LOW + 256*A = F1SAVE*F2 \}
         E_1: LOW/2 + C*128
                                         E_5: LOW mod 2
         E_2: A/2 + C*128
                                        E_6: A mod 2
         E_3: A+F2+C mod 256 E_7: (A+F2+C)/256
         E_4: F1/2 + C*128
                                 E_8: F1 mod 2
```

 $E_9$ : A+F2+C mod 256 == 0

```
{ F1=F1SAVE & F1<256 & F2<256 & LOW<256 }
         LDX #bits S_1 \equiv [8/X, 0/A]S_3()
         LDA #0
        ROR F1
                   S_3 \equiv [E_4/\text{F1}, E_8/\text{C}]S_4()
LOOP
         BCC ZCOEF S_4 \equiv \text{if } C == 0 S_5() \text{ else } S_7()
                          S_5 \equiv [0/\mathrm{C}]S_6()
         CLC
         ADC F2
                          S_6 \equiv [E_3/A, E_7/C, E_9/Z]S_7()
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ZCOEF ROR A
         ROR LOW S_8 \equiv [E_1/LOW, E_5/C]S_9(X, LOW, C)
                          S_9(X, LOW, C) \equiv [X - 1/X]S_{10}(X, LOW, C)
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                                  E_8: F1 mod 2
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        LDA #0
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                         S_3 \equiv [E_4/{
m F1}, E_8/{
m C}]S_4()
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ZCOEF ROR A
                          S_8 \equiv [E_1/LOW, E_5/C, X - 1/X]S_{10}(X, LOW, C)
        ROR LOW
        DEX
                         S_{10}(X) \equiv \text{if Z} == 0 S_3()
        BNE LOOP
                                   else \{LOW+256*A == F1SAVE*F2\}
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                          S_5 \equiv [0/\mathrm{C}]S_6()
         CLC
         ADC F2
                          S_6 \equiv [E_3/A, E_7/C, E_9/Z]S_7()
                          S_7 \equiv [E_2/A, E_6/C, E_1/LOW, X - 1/X]S_{10}()
ZCOEF ROR A
         ROR LOW
         DEX
         BNE LOOP
                          S_{10}(X) \equiv \text{if Z} == 0 S_3()
                                   else \{LOW+256*A == F1SAVE*F2\}
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        ROR F1
                         S_3 \equiv [E_4/\text{F1}, E_8/\text{C}]S_4()
LOOP
                         S_4 \equiv \text{if } C == 0 \ S_5() \ \text{else } S_7()
        BCC ZCOEF
                          S_5 \equiv [0/C, E_3/A, E_7/C, E_9/Z]S_7()
        CLC
        ADC F2
ZCOEF ROR A
                          S_7 \equiv [E_2/A, E_6/C, E_1/LOW, X - 1/X]S_{10}()
        ROR LOW
        DEX
        BNE LOOP
                         S_{10}(X) \equiv \text{if Z} == 0 S_3()
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                                        E_6: A mod 2
        E_3: A+F2+C mod 256 E_7: (A+F2+C)/256
        E_4: F1/2 + C*128
                                 E_8: F1 mod 2
        E_9: A+F2+C mod 256 == 0
```

### Put conditionals in proper form:

```
{ F1=F1SAVE & F1<256 & F2<256 & LOW<256 }
         LDX #bits S_1 \equiv [8/X, 0/A]S_3()
         LDA #0
        ROR F1
                          S_3 \equiv [E_4/\text{F1}, E_8/\text{C}]S_4()
LOOP
         BCC ZCOEF S_4 \equiv \text{if } C == 0 S_5() \text{ else } S_7()
                           S_5 \equiv [0/C, E_3/A, E_7/C, E_9/Z]S_7()
         CLC
         ADC F2
ZCOEF ROR A
                           S_7 \equiv [E_2/A, E_6/C, E_1/LOW, X - 1/X]S_{10}()
         ROR LOW
         DEX
                          S_{10}(\mathtt{X}) \equiv (\neg \mathtt{Z} \wedge S_3()) \lor
         BNE LOOP
                                    (Z \wedge \{LOW + 256 * A == F1SAVE * F2\})
         Q = \{ LOW + 256*A = F1SAVE*F2 \}
         E_1: LOW/2 + C*128
                                          E_5: LOW mod 2
         E_2: A/2 + C*128
                                          E_6: A mod 2
         E_3: A+F2+C mod 256 E_7: (A+F2+C)/256
         E_4: F1/2 + C*128
                                  E_8: F1 mod 2
         E_9: A+F2+C mod 256 == 0
```

### Put conditionals in proper form:

```
{ F1=F1SAVE & F1<256 & F2<256 & LOW<256 }
         LDX #bits S_1 \equiv [8/X, 0/A]S_3()
         LDA #0
         ROR F1
LOOP
                           S_3 \equiv [E_4/\text{F1}, E_8/\text{C}]S_4()
                           S_4 \equiv (\neg \mathsf{C} \wedge S_5()) \vee (\mathsf{C} \wedge S_7())
         BCC ZCOEF
                            S_5 \equiv [0/C, E_3/A, E_7/C, E_9/Z]S_7()
         CLC
         ADC F2
ZCOEF ROR A
                           S_7 \equiv [E_2/A, E_6/C, E_1/LOW, X - 1/X]S_{10}()
         ROR LOW
         DEX
                           S_{10}(\mathtt{X}) \equiv (\neg \mathtt{Z} \wedge S_3()) \lor
         BNE LOOP
                                     (Z \wedge \{LOW + 256 * A == F1SAVE * F2\})
         Q = \{ LOW + 256*A = F1SAVE*F2 \}
         E_1: LOW/2 + C*128
                                           E_5: LOW mod 2
         E_2: A/2 + C*128
                                           E_6: A mod 2
         E_3: A+F2+C mod 256 E_7: (A+F2+C)/256
         E_4: F1/2 + C*128
                                   E_8: F1 mod 2
         E_9: A+F2+C mod 256 == 0
```

$$S_7 \equiv [E_2/{\tt A}, E_6/{\tt C}, E_1/{\tt LOW}, {\tt X} - {\tt 1/X}]S_{10}$$

$$S_7 \equiv [E_2/\mathtt{A}, E_6/\mathtt{C}, E_1/\mathtt{LOW}, \mathtt{X} - 1/\mathtt{X}]($$

$$(\neg \mathtt{Z} \wedge S_3) \vee (\mathtt{Z} \wedge \{\mathtt{LOW} + 256 * \mathtt{A} = \mathtt{F1SAVE} * \mathtt{F2}\}))$$

$$\begin{split} S_7 &\equiv [E_2/\texttt{A}, E_6/\texttt{C}, E_1/\texttt{LOW}, \texttt{X} - \texttt{1/X}] (\\ &\quad (\neg \texttt{Z} \wedge [E_4/\texttt{F1}, E_8/\texttt{C}] ((\neg \texttt{C} \wedge S_5) \vee (\texttt{C} \wedge S_7))) \vee \\ &\quad (\texttt{Z} \wedge \{\texttt{LOW} + \texttt{256} * \texttt{A} = \texttt{F1SAVE} * \texttt{F2}\})) \end{split}$$

$$S_7 \equiv [E_2/\mathtt{A}, E_6/\mathtt{C}, E_1/\mathtt{LOW}, \mathtt{X} - 1/\mathtt{X}]($$
  $(\neg \mathtt{Z} \wedge [E_4/\mathtt{F1}, E_8/\mathtt{C}]($   $(\neg \mathtt{C} \wedge [0/\mathtt{C}, E_3/\mathtt{A}, E_7/\mathtt{C}, E_9/\mathtt{Z}]S_7) \vee (\mathtt{C} \wedge S_7))) \vee (\mathtt{Z} \wedge \{\mathtt{LOW} + 256 * \mathtt{A} = \mathtt{F1SAVE} * \mathtt{F2}\}))$ 

```
S_7 \equiv [E_2/A, E_6/C, E_1/LOW, X - 1/X](
         (\neg Z \wedge [E_4/F1, E_8/C])
              (\neg \texttt{C} \wedge [0/\texttt{C}, E_3/\texttt{A}, E_7/\texttt{C}, E_9/\texttt{Z}]S_7) \vee
              (\mathsf{C} \wedge S_7))) \vee
         (\mathsf{Z} \land \{\mathsf{LOW} + 256 * \mathsf{A} = \mathsf{F1SAVE} * \mathsf{F2}\}))
(DEFUN WP-ZCOEF (F1 C LOW A F1SAVE F2 X)
  (IF (EQUAL (DEC X) 0)
       (EQUAL (+ (* (+ (* C 128) (FLOOR A 2)) 256)
                     (+ (* (MOD A 2) 128) (FLOOR LOW 2)))
                 (* F1SAVE F2))
       (WP-ZCOEF
           (+ (* (MOD LOW 2) 128) (FLOOR F1 2))
           (* (MOD F1 2) (FLOOR (+ (+ (* C 128) (FLOOR A 2)) F2) 256))
           (+ (* (MOD A 2) 128) (FLOOR LOW 2))
           (+ (* (-1 (MOD F1 2)) (+ (* C 128) (FLOOR A 2)))
                (* (MOD F1 2) (MOD (+ (+ (* C 128) (FLOOR A 2)) F2) 256)))
           F1SAVE
           F2
           (DEC X))))
```