

# SET ASSOCIATIVE MAPPING IN MICROCONTROLLER

– GURUJING  
(1AY23CS079)

# Set-Associative Mapping in Microcontrollers

1. What is Cache Memory
2. Cache Mapping Techniques
3. What is Set-Associative
4. How Address Mapping
5. Benefits ,Limitations & Conclusion

# What is Cache Memory?

Cache is a small, high-speed memory close to the CPU.

- Stores frequently accessed data and instructions.
- Reduces the time to access data from main memory (RAM).
- Critical in microcontrollers to maintain fast performance and low power consumption.

# Cache Mapping Techniques

- **Direct Mapping:** Each block maps to exactly one cache line.
- **Fully Associative Mapping:** Any block can go into any line.
- **Set-Associative Mapping:** Cache is divided into sets; each set contains multiple lines (ways). A block maps to a specific set but can go into any line in that set.

# What is Set-Associative Mapping?

- A hybrid of direct and fully associative mapping.
- Memory blocks are mapped to a set using part of the address.
- Within the set, a block can be placed in any line (way).
- Common configuration: *n-way set associative*, where *n* is the number of lines per set.

# How Address Mapping Works

- A memory address is divided into three parts:
  - 1 **Tag** – Identifies the block.
  - 2 **Set Index** – Selects which set in the cache.
  - 3 **Block Offset** – Locates the data within the block.
- Cache checks all lines in the selected set for a matching tag.

# Benefits & Limitations

## Advantages:

- More flexible data placement within a set.
- Reduces conflict misses (better than direct mapping).

## Disadvantages:

- Slightly more complex than direct mapping.
- More hardware needed for parallel tag comparisons.

# Use in Microcontrollers

- Microcontrollers like **ARM Cortex-M** use set-associative caches (usually 2-way or 4-way).
- Helps improve performance for real-time embedded applications.
- Efficient for tasks like sensor data processing, control loops, and communication protocols.

# Conclusion

- Set-associative mapping balances speed and flexibility.
- Ideal for microcontrollers where performance and efficiency are critical.
- Understanding this mapping helps optimize memory-related operations.

THANK YOU