# Cryptography Lecture 16

Arkady Yerukhimovich

October 23, 2024

### Outline

- 1 Lecture 15 Review
- 2 AES Review
- 3 Feistel Networks and DES (Chapters 6.2.2-6.2.4)
- 4 Building Collision-Resistant Hash Functions (Chapter 6.3.1)

#### Lecture 15 Review

- Review of exam and research project
- Blockciphers
- Confusion-Diffusion paradigm and the avalanche effect
- Substitution-Permutation Paradigm
- AES

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Advanced Encryption Standard (AES)

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- Now, standardized and very widely used
- Hardware support (e.g., AES-NI) makes this very fast: 10<sup>8</sup> per second
- This is the right block-cipher to use for most applications

Available Versions:  $\ell=128(16 \text{ Bytes}), n=128, 192, 256$ 

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State:  $4 \times 4$  array of Bytes

$$\begin{pmatrix}
B_{11} & B_{12} & B_{13} & B_{14} \\
B_{21} & B_{22} & B_{23} & B_{24} \\
B_{31} & B_{32} & B_{33} & B_{34} \\
B_{41} & B_{42} & B_{43} & B_{44}
\end{pmatrix}$$

**1** Add Round Key: View  $k_i = k_{11} ||k_{12}|| \cdots ||k_{44}|$  with  $|k_{ij}| = 1$  Byte

$$\begin{pmatrix} B_{11} \oplus k_{11} & B_{12} \oplus k_{12} & B_{13} \oplus k_{13} & B_{14} \oplus k_{14} \\ B_{21} \oplus k_{21} & B_{22} \oplus k_{22} & B_{23} \oplus k_{23} & B_{24} \oplus k_{24} \\ B_{31} \oplus k_{31} & B_{32} \oplus k_{32} & B_{33} \oplus k_{33} & B_{34} \oplus k_{34} \\ B_{41} \oplus k_{41} & B_{42} \oplus k_{42} & B_{43} \oplus k_{43} & B_{44} \oplus k_{44} \end{pmatrix}$$

**4** Add Round Key: View  $k_i = k_{11} ||k_{12}|| \cdots ||k_{44}|$  with  $|k_{ij}| = 1$  Byte

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SubBytes: Uses a single 8-bit S-box

$$\begin{pmatrix} S(B_{11}) & S(B_{12}) & S(B_{13}) & S(B_{14}) \\ S(B_{21}) & S(B_{22}) & S(B_{23}) & S(B_{24}) \\ S(B_{31}) & S(B_{32}) & S(B_{33}) & S(B_{34}) \\ S(B_{41}) & S(B_{42}) & S(B_{43}) & S(B_{44}) \end{pmatrix}$$

Shift Rows: Shift each row to the left by varying amounts

$$\begin{pmatrix} B_{11} & B_{12} & B_{13} & B_{14} & (0 \text{ shift}) \\ B_{22} & B_{23} & B_{24} & B_{21} & (1 \text{ shift}) \\ B_{33} & B_{34} & B_{31} & B_{32} & (2 \text{ shift}) \\ B_{44} & B_{41} & B_{42} & B_{43} & (3 \text{ shift}) \end{pmatrix}$$

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MixColumns: Apply invertible transformation to Bytes in each column

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#### Observations:

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- Even this very structured permutation seems enough

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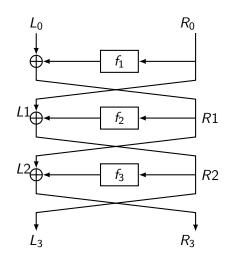
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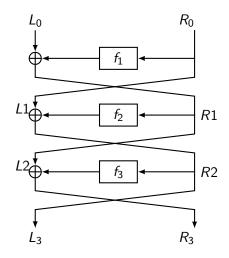
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#### The Challenge

How do we use such  $f_i$  to build an invertible function?



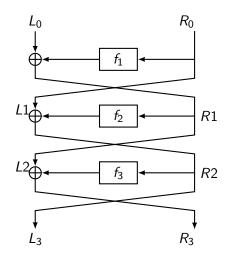
## Evaluating round i:



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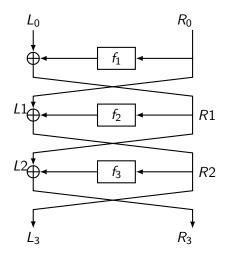
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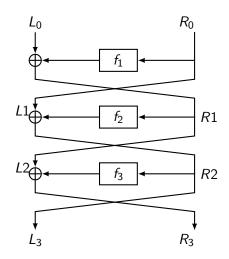
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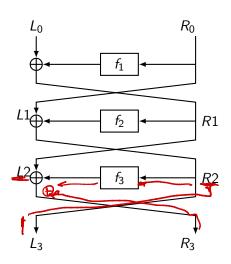
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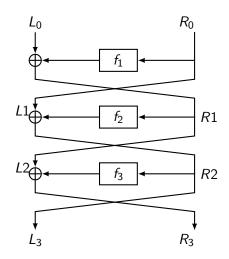


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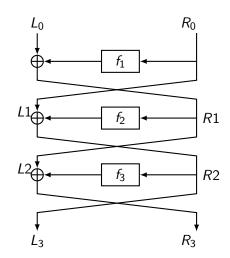
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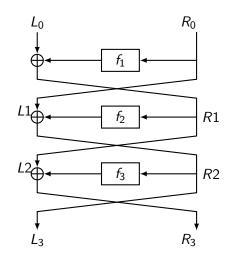
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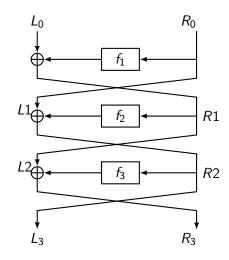
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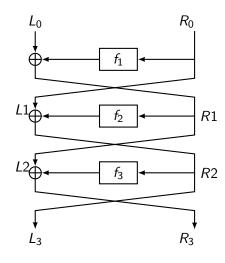
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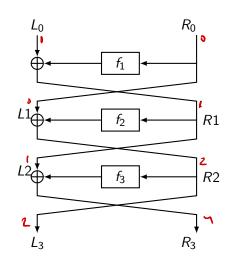
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- Usually f<sub>i</sub> derived from global public f and round key k<sub>i</sub>



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- We will talk about how to extend the key later

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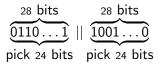
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$$\underbrace{0110\dots1}_{\text{pick 24 bits}} || \underbrace{1001\dots0}_{\text{pick 24 bits}}$$

• Which bits chosen in each round is public

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#### Mixing Permutation:

Output from any S-Box, affects input to 6 S-Boxes in next round

#### DES Avalanche Effect

- After 7 rounds all 32 bits in R affected
- After 8 rounds all 32 bits in L affected

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#### Question

How can we increase the key length of a block cipher?

## **Double Encryption**

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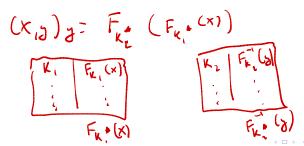
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- ② For each  $k_2 \in \{0,1\}^n$ , compute  $z = F_{k_2}^{-1}(y)$ , store  $(z,k_2)$  in table L'



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- **3** Find all intersections s.t.  $z_1 \in L = z_2 \in L'$  add  $(k_1, k_2)$  to table S

Try 1: Double Encryption (2DES) – double the key size

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- This attack takes  $O(2^n)$  time, so we are no better off than we were with just single encryption.

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Triple encryption with 3 keys

$$F_{k_1,k_2,k_3}^{"}(x) = F_{k_3}(F_{k_2}^{-1}(F_{k_1}(x)))$$

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## Outline

- 1 Lecture 15 Review
- 2 AES Review
- 3 Feistel Networks and DES (Chapters 6.2.2-6.2.4)
- 4 Building Collision-Resistant Hash Functions (Chapter 6.3.1)

Collision-resistant hash function from an (ideal) block-cipher:

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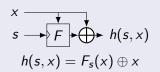
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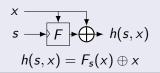
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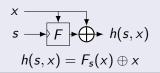
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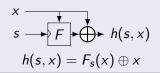
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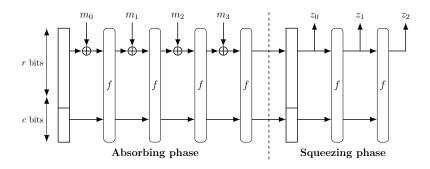
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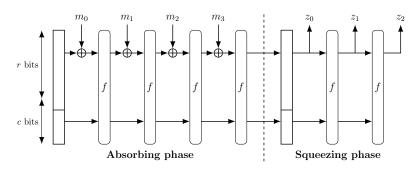
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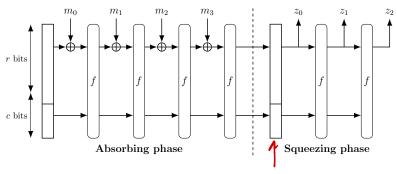
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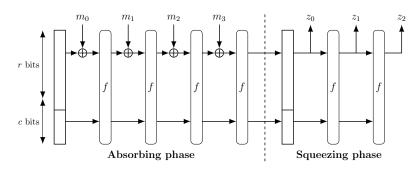




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- Looks like a random function if f is random permutation