Architectural Vulnerabilities due to Speculative Execution

Discussion led by Gabe

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What is all of this about?

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Why is it important?





#### Outline for Today

- Architecture
  - Assembly
  - GHz and pipelines (MIPS 5 stage)
    - Parallelism pipelined and data parallel
  - Branches and pipeline bubbles
- Speculation
  - Fake it, and hope you make it: branch prediction
  - ...but what happens when you don't make it?
- Side effects of speculation
  - Visibility of side-effects

array
256 items

char \*target = memory\_address\_I\_shouldn't\_be\_able\_to\_access;
array[\*target] = 1;

#### 256 items

char \*target = memory\_address\_I\_shouldn't\_be\_able\_to\_access;



```
array
256 items
```

array
256 items