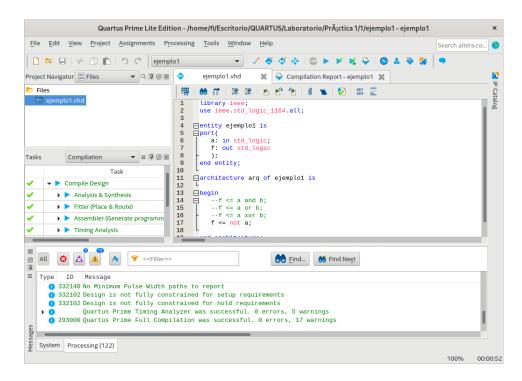
# Laboratorio de Diseño Digital Moderno Experimento No. 1 Construcción de Compuertas Básicas

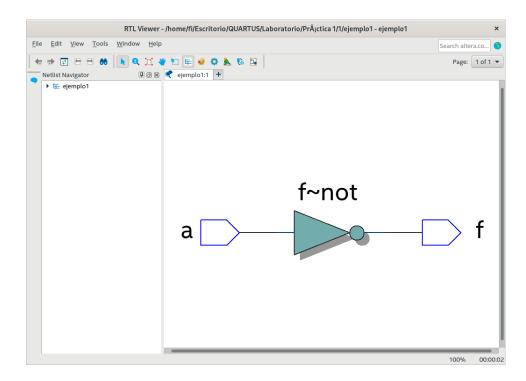
 $May\ 20,\ 2022$ 

# 1 Creación de compuerta lógica NOT

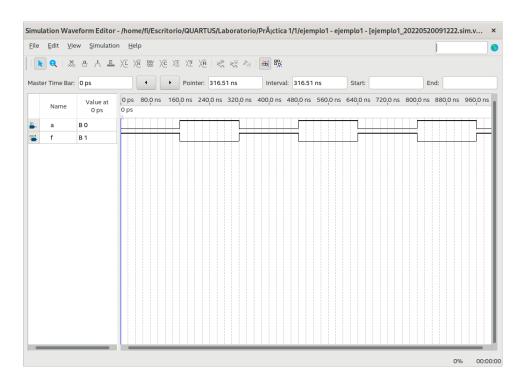
### 1.1 Compilación



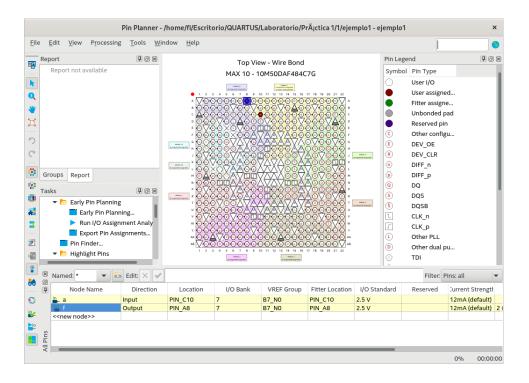
### 1.2 Diagrama RTL



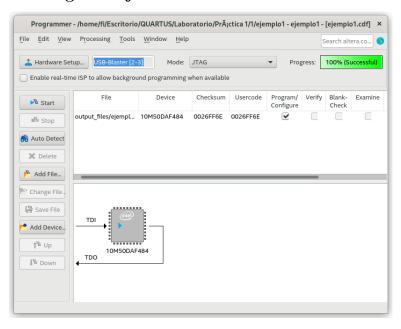
### 1.3 Simulación



### 1.4 Asignación de pines

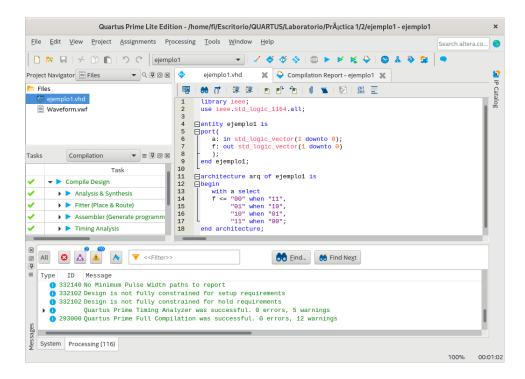


### 1.5 Descarga a tarjeta

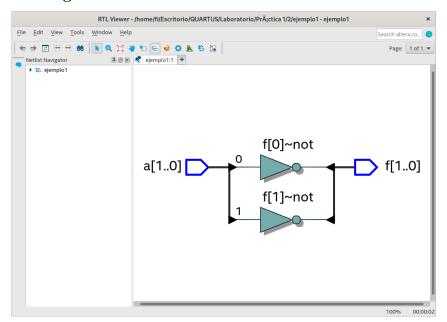


# 2 Creación de compuerta NOT con estructura with-select

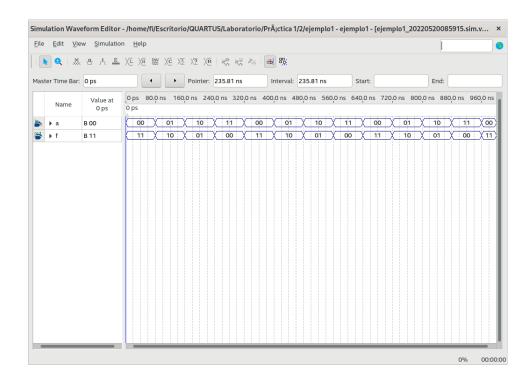
### 2.1 Compilación



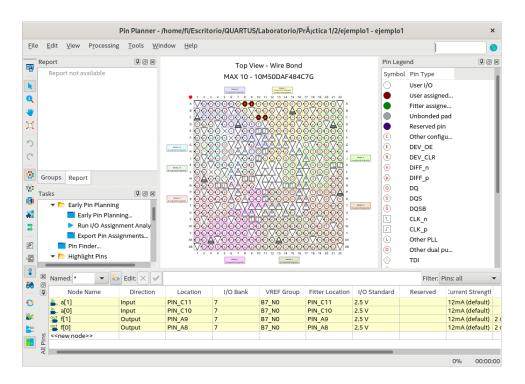
### 2.2 Diagrama RTL



#### 2.3 Simulación



### 2.4 Asignación de pines



## 2.5 Descarga

