

SoFPGA System	: T_PB_SYSTEM
o-SystemName	: T_PB_LONGNAME
o-SystemShort	: T_PB_SHORTNAME
o-DeviceInstances[T PB DEVICE INSTANCE INDEX]	: T_PB_DEVICE_INSTANCE_VECTOR
o-DeviceName	: T_PB_LONGNAME
o-DeviceShort	: T_PB_SHORTNAME
o-Device	: T_PB_DEVICE
o-DeviceName	: T_PB_LONGNAME
o-DeviceShort	: T_PB_SHORTNAME
o-Registers[T PB REGISTER INDEX]	: T_PB_REGISTER_VECTOR = 0..15
o-RegisterName	: T_PB_LONGNAME
o-RegisterShort	: T_PB_SHORTNAME
o-RegisterNumber	: T_UINT_8
o-RegisterKind	: T_PB_REGISTER_KIND = {PB_REGISTER_KIND_READ WRITE READWRITE WRITEK}
o-FieldMappings[T PB REGISTER FIELD MAPPING INDEX]	: T_PB_REGISTER_FIELD_MAPPING_VECTOR = 0..15
o-FieldID	: T_UINT_8
o-Start	: T_UINT_8
o-Length	: T_UINT_8
o-MappingKind	: T_PB_REGISTER_FIELD_MAPPING_KIND = {PB_REGISTER_FIELD_MAPPING_KIND_READ WRITE WRITEK}
o-FieldMappingCount	: T_UINT_8
o-RegisterFields[T PB REGISTER FIELD INDEX]	: T_PB_REGISTER_FIELD_VECTOR = 0..15
o-FieldName	: T_PB_LONGNAME
o-FieldShort	: T_PB_SHORTNAME
o-Length	: T_UINT_8
o-AutoClear	: BOOLEAN
o-FieldKind	: T_PB_REGISTER_FIELD_KIND = {PB_REGISTER_FIELD_KIND_READ WRITE READWRITE}
o-RegisterFieldCount	: T_UINT_8
o-CreatesInterrupt	: BOOLEAN
o-Mappings[T PB PORTNUMBER MAPPING INDEX]	: T_PB_PORTNUMBER_MAPPING_VECTOR = 0..23
o-PortNumber	: T_UINT_8
o-RegID	: T_UINT_8
o-RegisterNumber	: T_UINT_8
o-MappingKind	: T_PB_MAPPING_KIND = {PB_MAPPING_KIND_EMPTY WRITE WRITEK READ}
o-MappingCount	: T_UINT_8
o-BusShort	: T_PB_SHORTNAME
o-DeviceInstanceCount	: T_PB_DEVICE_INSTANCE_INDEX = 0..31
o-Busses[T PB BUS INDEX]	: T_PB_BUS_VECTOR
o-BusName	: T_PB_LONGNAME
o-BusShort	: T_PB_SHORTNAME
o-SuperBusShort	: T_PB_SHORTNAME
o-SuperBusID	: T_PB_BUSID = 0..31
o-SubBusCount	: NATURAL = 0..8
o-SubBusses[0..7]	: T_PB_BUSID_VECTOR
o-DeviceCount	: NATURAL = 0..32
o-Devices[0..31]	: T_PB_DEVICEID_VECTOR
o-TotalDeviceCount	: T_UINT_8
o-BusCount	: T_PB_BUS_INDEX = 0..11