



N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

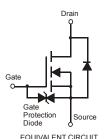
Mechanical Data

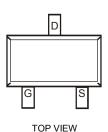
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.006 grams (Approximate)











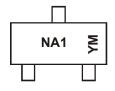
Ordering Information (Note 4)

Part Number	Case	Packaging		
DMG1012UW-7	SOT323	3000 / Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



NA1 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2009		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	W		I	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Chai	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V _{GSS}	±6	V
Continuous Drain Current (Note 5)	I _D	1.0 0.64	А
Pulsed Drain Current (Note 6)	I _{DM}	6	Α

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P _D	0.29	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	425	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

5. Device mounted on FR-4 PCB, with minimum recommended pad layout. 6. Repetitive rating, pulse width limited by junction temperature. Notes:

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage		20	ı	-	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	ı	1	100	nA	V_{DS} = 20V, V_{GS} = 0V		
Gate-Source Leakage	I _{GSS}	ı	ı	±1.0	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V _{GS(th)}	0.5	-	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$		
			0.3	0.45		$V_{GS} = 4.5V, I_D = 600mA$		
Static Drain-Source On-Resistance	R _{DS(on)}	-	0.4	0.6	Ω	V_{GS} = 2.5V, I_{D} = 500mA		
			0.5	0.75		$V_{GS} = 1.8V, I_D = 350mA$		
Forward Transfer Admittance	Y _{fs}	-	1.4	-	S	V _{DS} = 10V, I _D = 400mA		
Diode Forward Voltage	V _{SD}	-	0.7	1.2	V	V _{GS} = 0V, I _S = 150mA		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C _{iss}	ı	60.67	-	pF	101111		
Output Capacitance	Coss	-	9.68	-	pF	$V_{DS} = 16V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	C _{rss}	-	5.37	-	pF	1 - 1.0WH12		
Total Gate Charge	Qg	-	736.6	-	рC	V 45V V 40V		
Gate-Source Charge	Q_{gs}	-	93.6	-	рC	V_{GS} = 4.5V, V_{DS} = 10V,		
Gate-Drain Charge	Q _{gd}	-	116.6	-	рС	1D = 250IIIA		
Turn-On Delay Time	t _{D(on)}	-	5.1	-	ns			
Turn-On Rise Time	t _R	-	7.4	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t _{D(off)}	1	26.7	-	ns	$R_L = 47\Omega, R_G = 10\Omega,$ $I_D = 200 \text{mA}$		
Turn-Off Fall Time	t _F	-	12.3	-	ns	710 20011/A		

7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing. Notes:

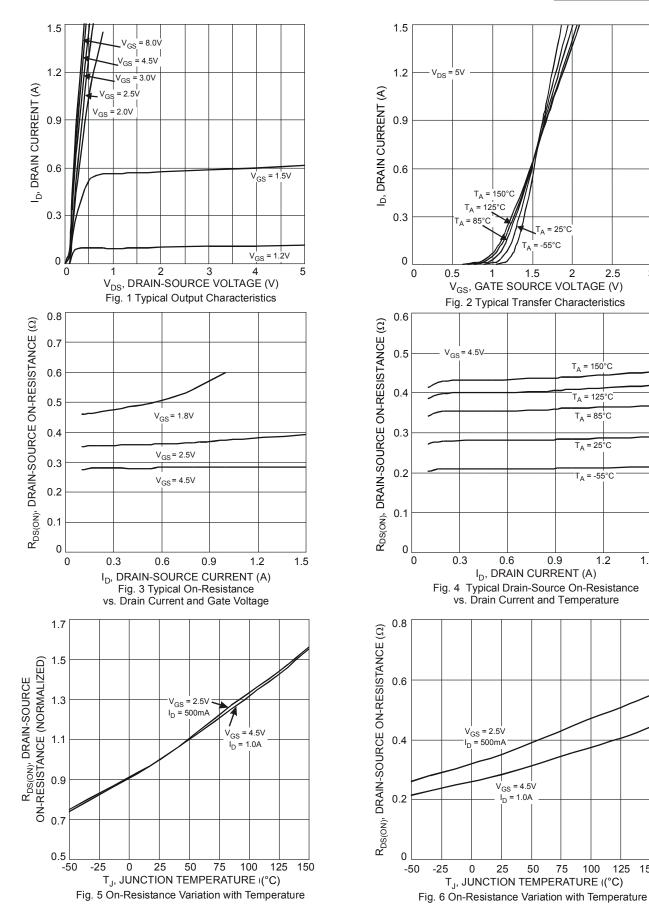
2.5

1.2

1.5

3







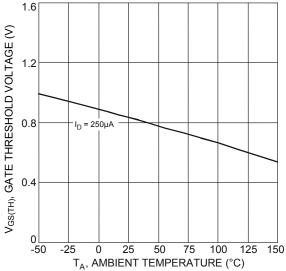
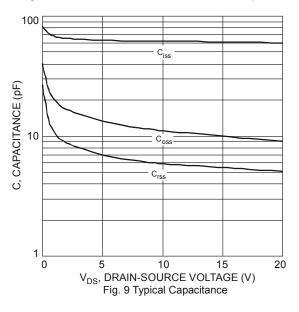
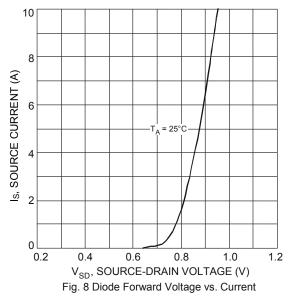
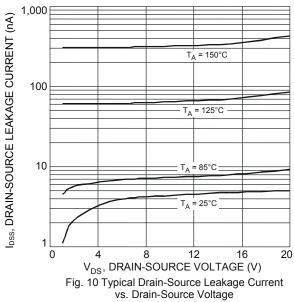


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







D = 0.7

D = 0.5

D = 0.3

D = 0.05

t₁, PULSE DURATION TIME (s)
Fig. 11 Transient Thermal Response

0.1

10

100

1,000

0.001

0.0001

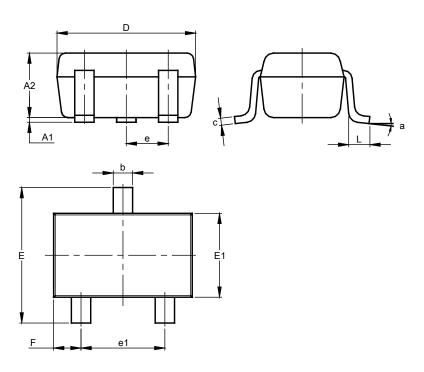
0.001



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

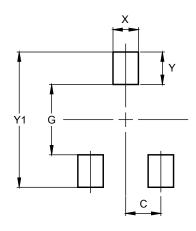


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.80	1.00	0.90			
b	0.20	0.40	0.30			
С	0.08	0.18	0.13			
D	1.80	2.20	2.00			
Е	2.00	2.45	2.225			
E1	1.15	1.35	1.25			
е	-	-	0.65			
e1	1.20	1.40	1.30			
F	0.25	0.475	0.3625			
L	0.25	0.46	0.355			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)		
С	0.650		
G	1.300		
X	0.470		
Y	0.600		
Y1	2.500		



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