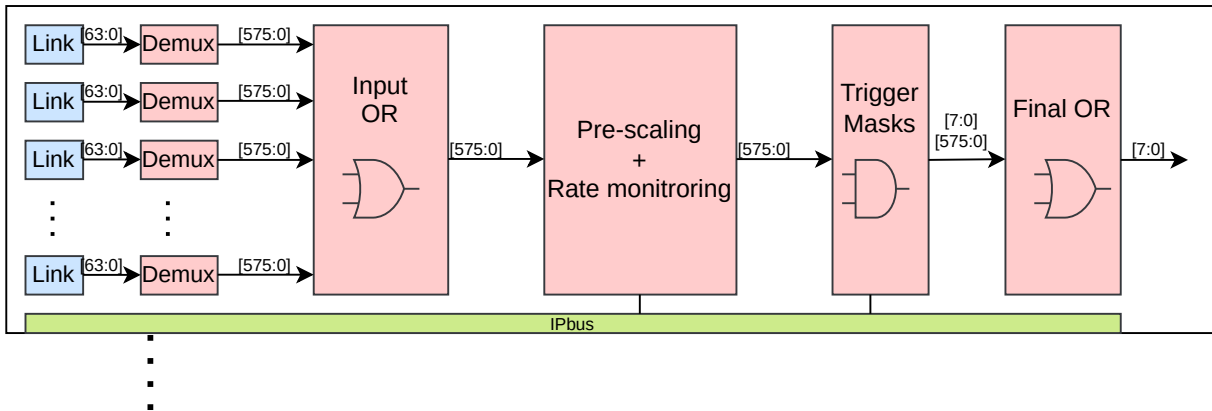


clk_p ▪ lhc_clk
(360MHz) ▪ (40MHz)
domain ▪ domain

24 links
per SLR



To SLR1