

Zynq Programmable System-on-a-Chip

Application Processing Unit

OCM

L2 \$

snoop control unit

L1 I\$

L1D\$

Dual Core
ARM Cortex-A9

ACP
port

Neural Processing Unit

AXI Master
Interface

Scheduler

bus

PU

control

scratchpad

PE

PE

⋮

PE

SIG

...

PU

control

scratchpad

PE

PE

⋮

PE

SIG