

ZPUino Soft Processor - Papilio DUO - Vanilla - v2.0

Author: Alvaro Lopes
alvie.com/zpuino

512KB or 2MB of Program Code Space
(code runs out of SDRAM)

Visit Papilio.cc for Documentation

Free Resources
21 Block RAM (BRAM)
66% of Slices
1 PLL (clock)
2 DCM (clock)

System Clock runs at 96Mhz

Used Resources:
11 of 32 Block RAM (BRAM)
34% of available Slices
1 of 2 PLL (clock)
0 of 2 DCM (clock)

GPIO Pins
gpio_bus_in(200:0)
gpio_bus_out(200:0)

Clocks
clk_96Mhz
clk_1Mhz
clk_osc_32Mhz

AVR_Wishbone_Bridge_Enable
Connect VCC or a Pullup to enable the AVR Wishbone Bridge
The AVR Wishbone Bridge uses pins 10-13
Remove Arduino_10-13 IO Markers to use.

Wishbone Slot 5

Wishbone Slot 6

Wishbone Slot 8

Wishbone Slot 9

Wishbone Slot 10

Wishbone Slot 11

Wishbone Slot 12

Wishbone Slot 13

Wishbone Slot 14

With VGA DMA

Arduino_22 rx tx Arduino_23

UART

Arduino_24 rx tx Arduino_25

UART

Arduino_26 rx tx Arduino_27

UART

Arduino_28 rx tx Arduino_29

UART

Arduino_30 rx tx Arduino_31

UART

Arduino_32 rx tx Arduino_33

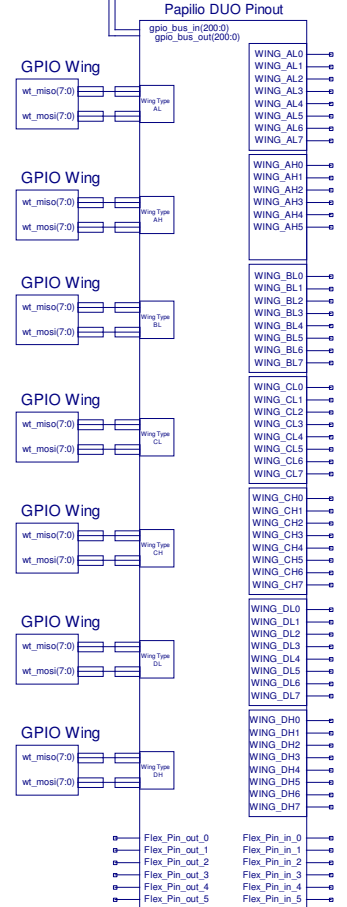
UART

Arduino_34 rx tx Arduino_35

UART

Arduino_36 rx tx Arduino_37

UART



DUO_SW1 INV ARD_RESET

Replace DUO_SW1 with a Pulldown if you want the ATmega32U4 chip to run when this circuit is loaded.
Replace DUO_SW1 with a Pullup if you want to disable the ATmega32U4 chip when this circuit is loaded.

Papilio DUO Reset