

PAPILIO RAM - PIN ASSIGNMENTS

	D	C	
GND	N1 O	M1 15	
2V5	P1 1	N2 14	
3V3	P6 2	N7 13	
5V	P2 3	R1 12	CH
DL	N8 4	P7 11	5V
	P9 5	N9 10	3V3
	M10 6	N10 9	2V5
	R5 7	T4 8	GND
GND	T6 8	T5 7	
2V5	T7 9	R7 6	
3V3	N11 10	P11 5	
5V	T8 11	P8 4	CL
DH	P13 12	N12 3	5V
	T9 13	R9 2	3V3
	P12 14	T12 1	2V5
	R13 15	T13 O	GND

	B	A	
GND	B3 O	A3 15	
2V5	B4 1	A4 14	
3V3	C5 2	A5 13	
5V	C6 3	D7 12	AH
BL	B6 4	A6 11	5V
	C7 5	A7 10	3V3
	B8 6	A8 9	2V5
	E7 7	F8 8	GND
GND	D9 8	C10 7	
2V5	A9 9	C9 6	
3V3	A10 10	B10 5	
5V	A11 11	C11 4	AL
BH	E10 12	D10 3	5V
	A12 13	B12 2	3V3
	A13 14	A14 1	2V5
	B14 15	B15 O	GND

DEVICE: XC3S200A
 PACKAGE: FT256
 SPEED: -4

C8

CLK
 31.25NS
 32MHZ