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Lab 4

In conclusion for this lab, I learned to implement customized ISRs which can be triggered by hardware and software. I learned that there are different registers controlling different modes and settings that an ISR might require. One thing I could have improved is my understanding of the relationship between the ISR and the internal clock. By having a better understanding of this system, it would have taken me less time and effort for me to be able to complete task 3. I found this lab quite interesting and it makes me question what the professor will make me do for the upcoming team project.