

1 Layouts used in 4×4 Approximate Wallace Tree Multiplier

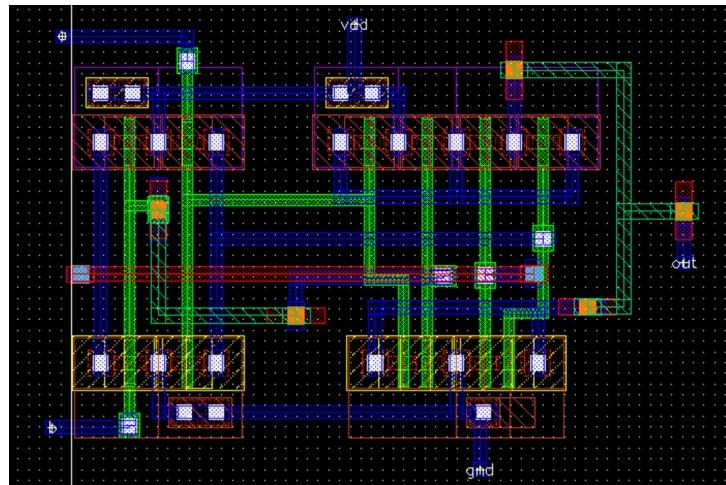


Figure 1: Layout of X-OR Gate

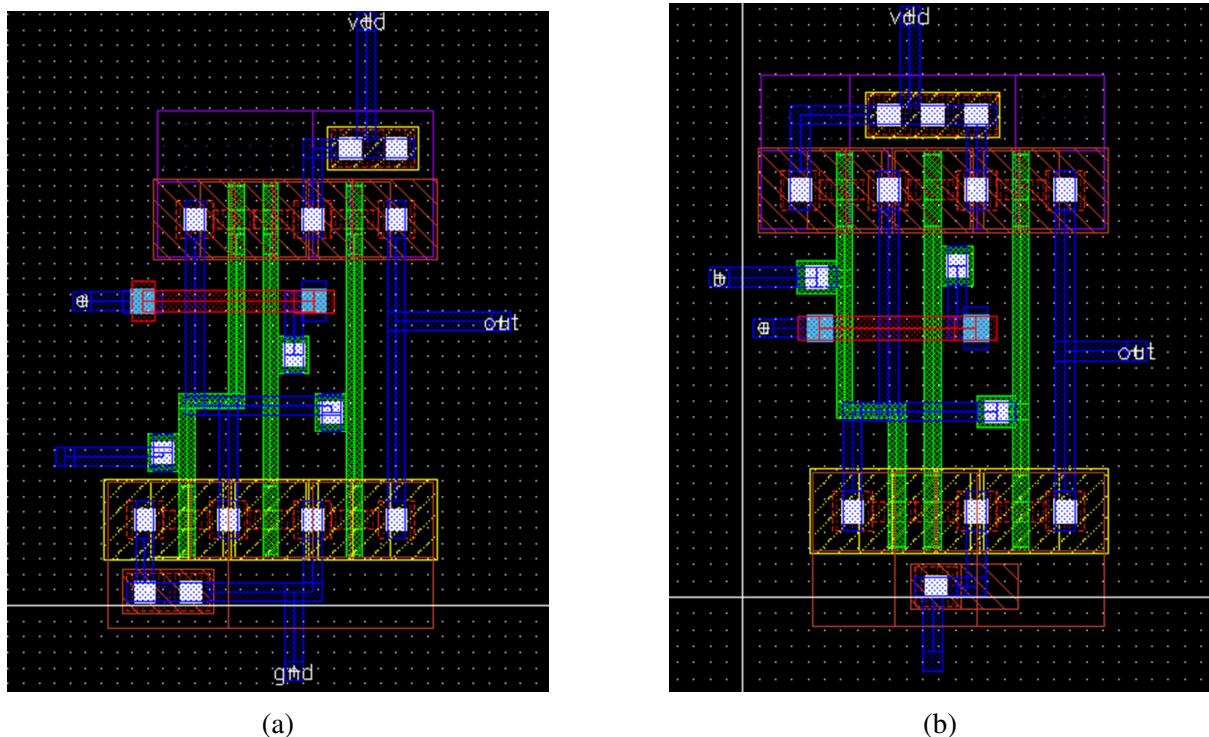


Figure 2: (a): Layout of AND Gate, (b): Layout of OR Gate.

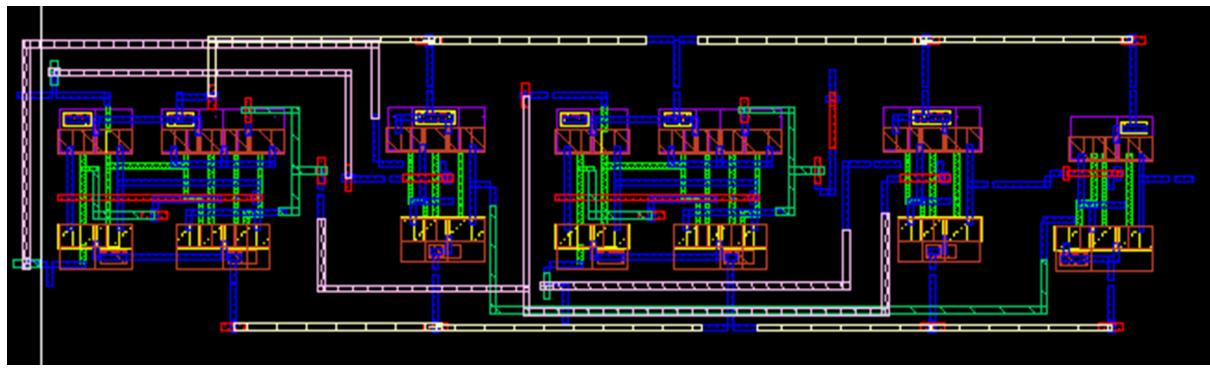


Figure 3: Layout of Exact Full Adder(FA)

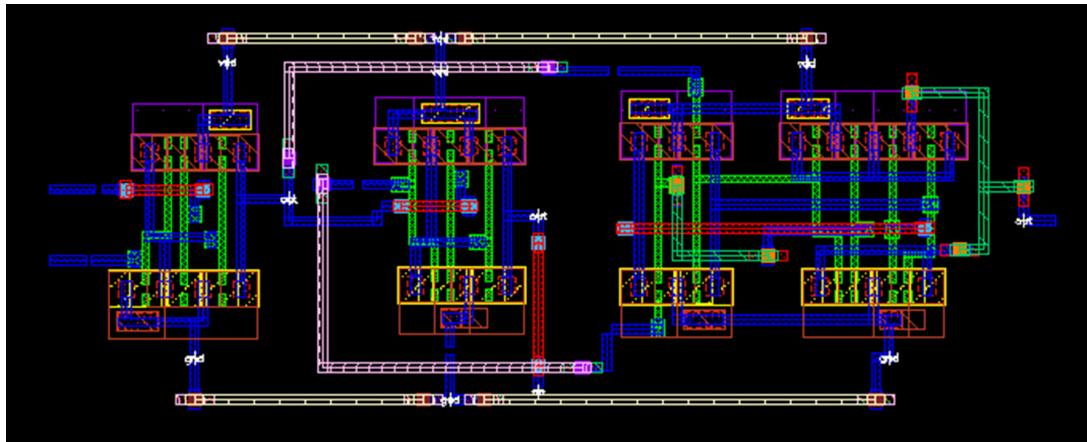


Figure 4: Layout of Approximate Full Adder One (AFA-1)

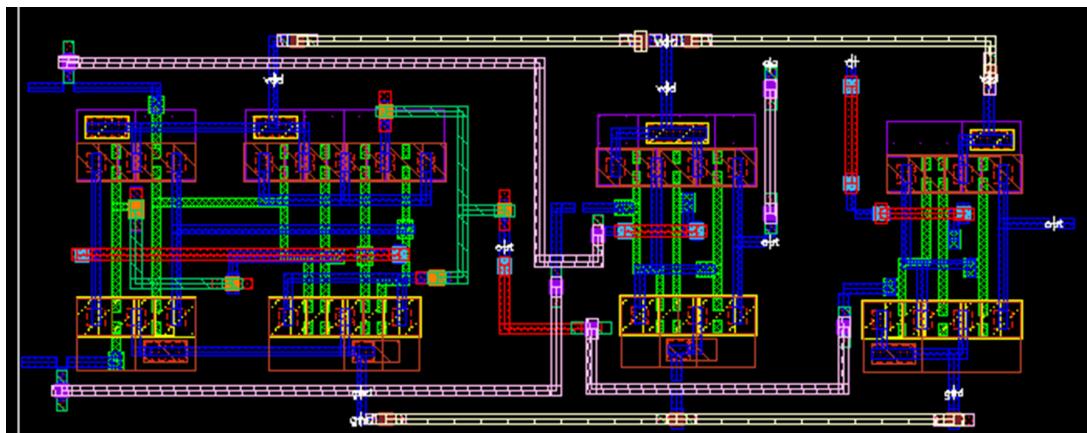


Figure 5: Layout of Approximate Full Adder Two (AFA-2)

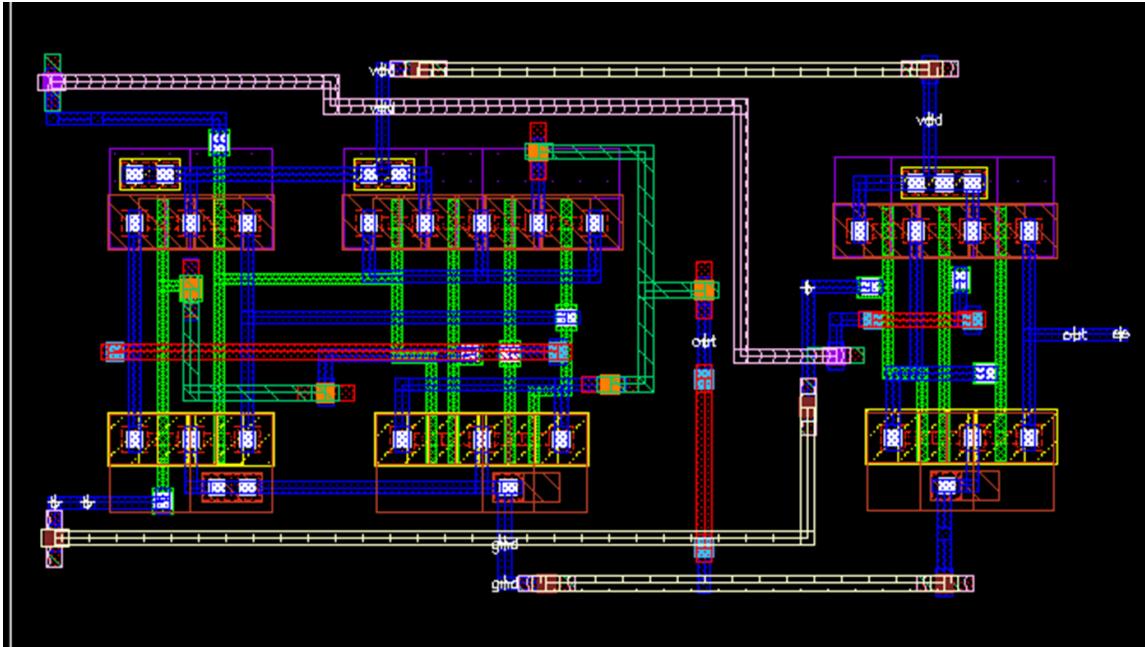


Figure 6: Layout of Half Adder (HA)

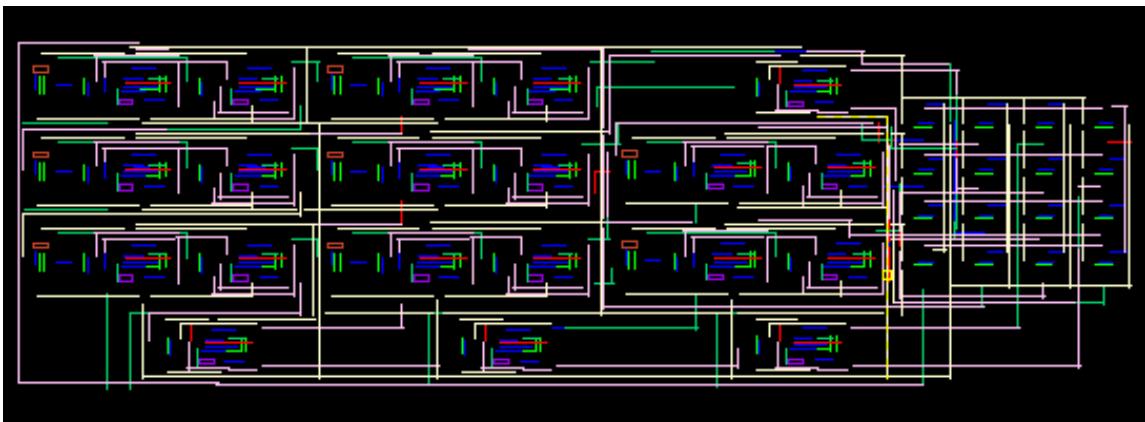


Figure 7: Exact 4x4 Wallace Tree Multiplier(WTM) using exact FA and HA

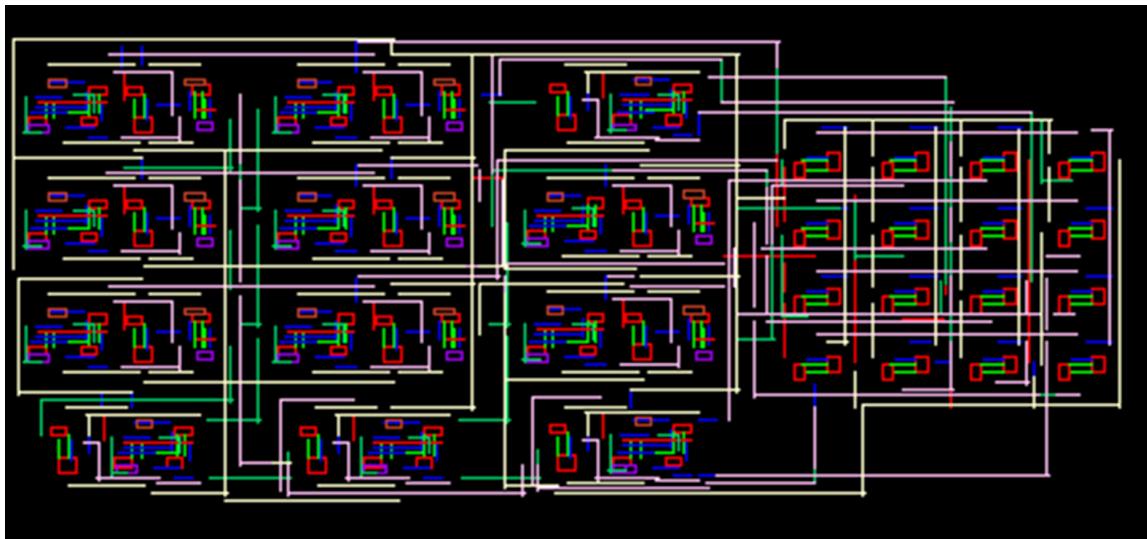


Figure 8: Layout of Approximate 4x4 Wallace Tree Multiplier One(AWTM-1) using AFA-1 and HA

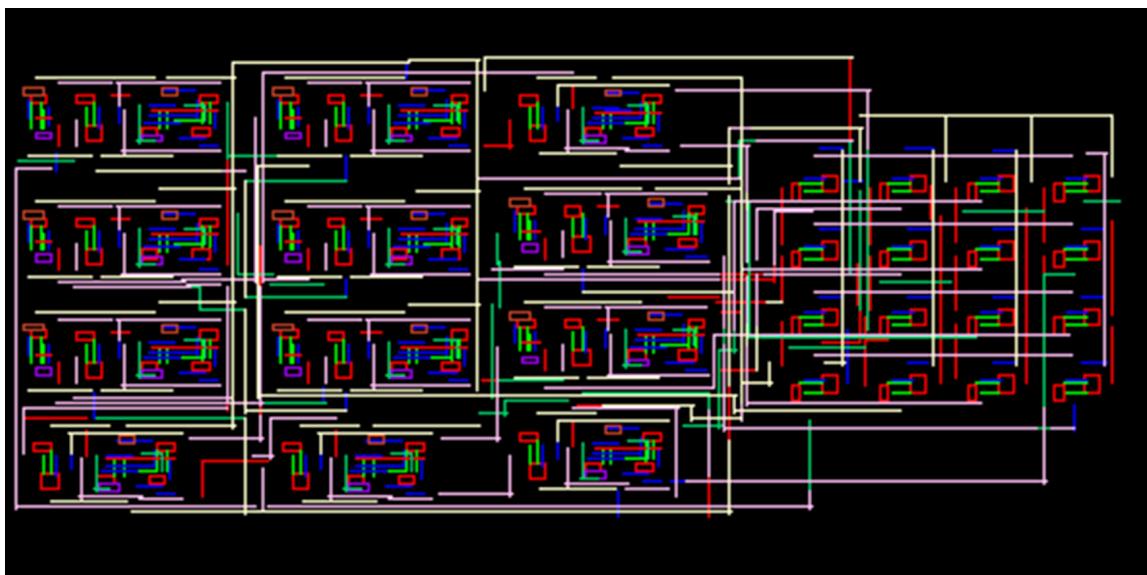


Figure 9: Layout of Approximate 4x4 Wallace Tree Multiplier Two (AWTM-2) using AFA-2 and HA

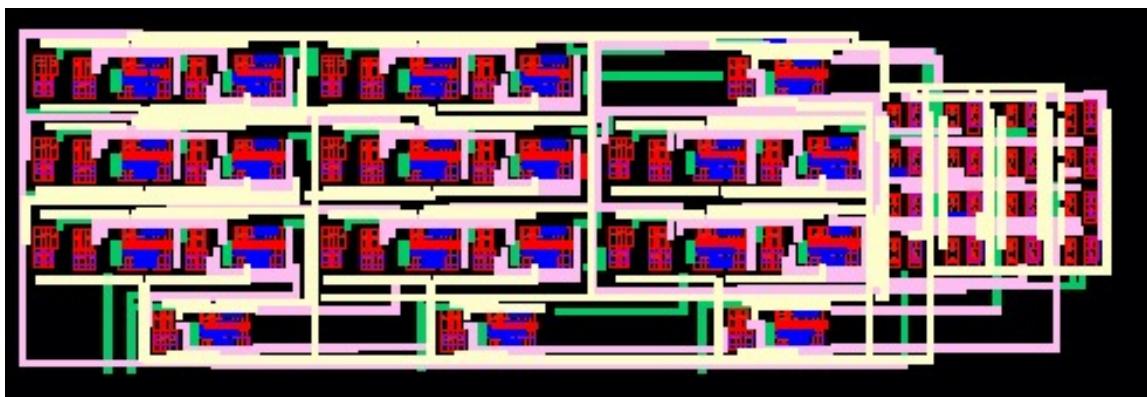


Figure 10: Parasitic extracted layout view of Exact WTM

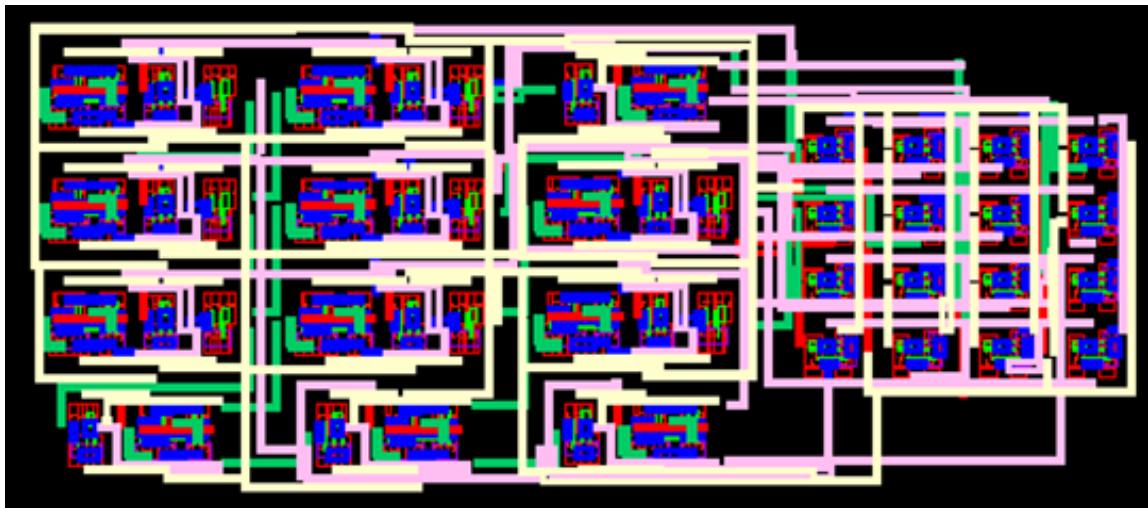


Figure 11: Parasitic extracted layout view of AWTM (One).

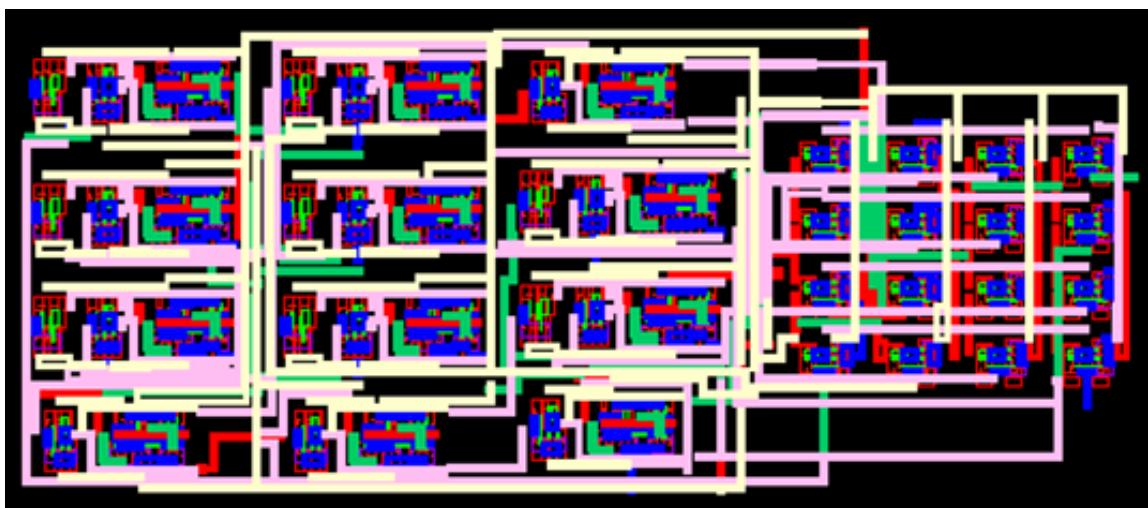


Figure 12: Parasitic extracted layout view of AWTM (two).