

Compiling Proof Obligations

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1 Introduction

This documents studies the relationship between the verification conditions generated for a Java like source language and the verification conditions generated for the bytecode language defined in [3]. We establish an equivalence which we name $\equiv_{\text{mod Names and bools}}$ modulo names and boolean values of the proof obligations on source and bytecode level. This result may have an impact on the application on PCC techniques for complex functional and security properties where full automatisisation is not possible.

The traditional PCC architecture comes along with a certifying compiler. The basic idea is that the certifying compiler infers automatically annotations, automatically generates verification conditions, proves them automatically and then sends both the code and the proof certificate to the counterpart that will run the code. The receiver then, generates the verification conditions and type checks the generated formulas against the proof certificate. This architecture works for properties like well typedness and safe memory read/write but it is not applicable for complex policies where the specification and the proof cannot be done automatically.

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2 Source

We present a source Java-like programming language which supports the following features: object manipulation and creation, method invocation, throwing and handling exceptions, subroutines etc. The first definition that we give hereafter presents all the constructs of our language which evaluate to a value.

Definition 2.1 (Expression) *The grammar for source expressions is defined as follows*

$$\begin{aligned}
 \mathcal{E}^{src} ::= & \text{constInt} \\
 & | \text{true} \\
 & | \text{false} \\
 & | \mathcal{E}^{src} \text{ op } \mathcal{E}^{src} \\
 & | \mathcal{E}^{src}.f \\
 & | \text{var} \\
 & | (Class) \mathcal{E}^{src} \\
 & | \text{null} \\
 & | \text{this} \\
 & | \mathcal{E}^{\mathcal{R}} \\
 & | \mathcal{E}^{src}.m(\mathcal{E}^{src}) \\
 & | \text{new } Class(\mathcal{E}^{src}) \\
 \\
 \mathcal{E}^{\mathcal{R}} ::= & \mathcal{E}^{src} \mathcal{R} \mathcal{E}^{src} \\
 & | \mathcal{E}^{src} \text{ instanceof } Class \\
 \\
 \mathcal{R} \in & \{ \leq, <, \geq, >, =, \neq \}
 \end{aligned}$$

We now give an informal description of the meaning of the expressions of the above grammar:

- **constInt** is any integer literal
- **true** and **false** are the unique boolean constants
- **constRef** is a reference to an object in the memory heap
- $\mathcal{E}^{src} \text{ op } \mathcal{E}^{src}$ which stands for an arithmetic expression with any of the arithmetic operators $+, -, div, rem, *$
- $\mathcal{E}^{src}.f$ is a field access expression where the field with name f is accessed
- the cast expression $(Class)\mathcal{E}^{src}$ which is applied only to expressions from a reference type
- the expression **null** stands for the null reference which does not point to any location in the heap
- **this** refers to the current object
- $\mathcal{E}^{src}.m(\mathcal{E}^{src})$ stands for a method invocation expression. Note that here we consider only methods with one argument which return a value
- **new** $Class(\mathcal{E}^{src})$ stands for an object creation expression of class $Class$. We consider only constructors which take only one argument for the sake of readability

The language is also provided with relational expressions, which evaluate to the boolean values:

- $\mathcal{E}^{src} \mathcal{R} \mathcal{E}^{src}$ where $\mathcal{R} \in \{\leq, <, \geq, >, =, \neq\}$ stands for the relation between two expressions
- \mathcal{E}^{src} **instanceof** $Class$ states that \mathcal{E}^{src} has as type the class $Class$ or one of its subclasses

The expressions can be of object types or basic types. Formally the types are

$JavaType ::= Class, Class \in \text{ClassTypes} \mid \text{int} \mid \text{boolean}$

The next definition gives the control flow constructs of our language as well as the expressions that have a side effect

Definition 2.2 (Statement) *The grammar for expressions is defined as follows :*

```

STMT ::=  STMT; STMT
          | if ( $\mathcal{E}^{\mathcal{R}}$ ) then {STMT} else {STMT}
          | try {STMT} catch (Exc ) {STMT}
          | try {STMT} finally {STMT}
          | try {STMT} catch (Exc ) {STMT} finally {STMT}
          | throw  $\mathcal{E}^{src}$ 
          | while ( $\mathcal{E}^{\mathcal{R}}$ )[INV,modif] {STMT}
          | return  $\mathcal{E}^{src}$ 
          |  $\mathcal{E}^{src} = \mathcal{E}^{src}$ 
          |  $\mathcal{E}^{src}$ 

```

From the definition we can see that the language supports also the following constructs :

est-ce que
je dois dire
qu'on con-
sidere un
sousesemble
de Class qui
represente les
exceptions ?

- $STMT; STMT$, i.e. statements that execute sequentially
- $\text{if } (\mathcal{E}^R) \text{ then } \{STMT\} \text{ else } \{STMT\}$ which stands for an if statement. The semantics of the construct is the standard one, i.e. if the relation expression \mathcal{E}^R evaluates to true then the statement in the **then** branch is executed, otherwise the statement in the **else** branch is executed
- $\text{try } \{STMT\} \text{ catch } (Class) \{STMT\}$ which states that if the statement following the **try** keyword throws an exception of type **Exc** then the exception will be caught by the statement following the **catch** keyword
- $\text{try } \{STMT\} \text{ finally } \{STMT\}$
- $\text{try } \{STMT\} \text{ catch } (Exc) \{STMT\} \text{ finally } \{STMT\}$
- $\text{while } (\mathcal{E}^R)[INV, \text{modif}] \{STMT\}$ states for a loop statement where the body statement $STMT$ will be executed until the relational expression \mathcal{E}^R evaluates to true.
- $|\text{return } \mathcal{E}^{src}$ is the statement by which the execution will be finished
- $\mathcal{E}^{src} = \mathcal{E}^{src}$ stands for an assignment expression, where the value of the left expression is updated with the value of the right expression
- finally, every expression \mathcal{E}^{src} is a statement

3 Compiler

We now turn to specify a simple compiler from the source language presented in Section 2 into the bytecode language. The compiler does not perform any optimizations.

The compiler function is denoted with $\ulcorner \urcorner$ and its signature is :

$$\ulcorner \urcorner : nat * STMT * nat \longrightarrow list \ I$$

The compiler function takes three arguments: a natural number s from which the labeling of the compilation of $STMT$ starts, the compiled statement $STMT$ and a natural number which is the greatest label in the compilation of $STMT$ and returns a list of bytecode instructions.

the exception handler function

3.1 Compiling expressions in bytecode instructions

- integer or boolean constant access
 - integer constant access

$$\ulcorner s, \text{constInt}, s \urcorner = s : \text{push constInt}$$

- boolean constant access

$$\ulcorner s, \text{true}, s \urcorner = s : \text{push 1}$$

$$\ulcorner s, \text{false}, s \urcorner = s : \text{push 0}$$

Note: the source boolean expressions are compiled down to integers

- method invocation

$$\begin{aligned} \lceil s, \mathcal{E}_1^{src}.m(\mathcal{E}_2^{src}), e \rceil &= \begin{array}{l} \lceil s, \mathcal{E}_1^{src}, e' \rceil; \\ \lceil e' + 1, \mathcal{E}_2^{src}, e - 1 \rceil; \\ e : \text{invoke } m \end{array} \end{aligned}$$

- field access

$$\lceil s, \mathcal{E}^{src}.f, e \rceil = \begin{array}{l} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{getfield } f \end{array}$$

- local variable access

$$\lceil s, \mathbf{var}, s \rceil = s : \text{load } \mathbf{reg}_i$$

where \mathbf{reg}_i is the local variable at index i

- arithmetic expressions

$$\lceil s, \mathcal{E}_1^{src} \text{ op } \mathcal{E}_2^{src}, e \rceil = \begin{array}{l} \lceil s, \mathcal{E}_1^{src}, e' \rceil; \\ \lceil e' + 1, \mathcal{E}_2^{src}, e - 1 \rceil; \\ e : \text{arith_op} \end{array}$$

- cast expression

$$\lceil s, (\mathbf{Class}) \mathcal{E}^{src}, e \rceil = \begin{array}{l} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{checkcast } \mathbf{Class} ; \end{array}$$

- instanceof expression

$$\lceil s, \mathcal{E}^{src} \mathbf{instanceof} \mathbf{Class}, e \rceil = \begin{array}{l} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{instanceof } \mathbf{Class}; \end{array}$$

- null expression

$$\lceil s, \mathbf{null}, s \rceil = s : \text{push } \mathbf{null}$$

- object creation

$$\lceil s, \mathbf{new} \mathbf{Class}(\mathcal{E}^{src}), e \rceil = \begin{array}{l} s : \text{new } \mathbf{Class}; \\ s + 1 : \text{dup}; \\ \lceil s + 2, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{invoke } \mathbf{constr}(\mathbf{Class}); \end{array}$$

- this instance

$$\lceil s, \mathbf{this}, s \rceil = s : \text{load } \mathbf{reg}_0$$

3.2 Compiling control statements in bytecode instructions

- compositional statement

$$\begin{aligned} \lceil s, \mathcal{S}TMT_1; \mathcal{S}TMT_2, e \rceil &= \\ \lceil s, \mathcal{S}TMT_1, e' \rceil; & \\ \lceil e' + 1, \mathcal{S}TMT_2, e \rceil & \end{aligned}$$

about the compilation of exception handlers

a redundant jump added in the compilation in order to see explicitly the relation between stmt1 and stmt2

- if statement

$$\begin{aligned} & \lceil s, \text{if } (\mathcal{E}^{\mathcal{R}}) \text{ then } \{STM T_1\} \text{ else } \{STM T_2\}, e \rceil = \\ & \lceil s, \mathcal{E}^{\mathcal{R}}, e' \rceil; \\ & e' + 1 : \text{if } e'' + 2; \\ & \lceil e' + 2, STM T_2 \rangle, e'' \rceil \\ & e'' + 1 : \text{goto } e + 1; \\ & \lceil e'' + 2, STM T_1, e \rceil; \end{aligned}$$

- assignment statement. We consider the case for instance field assignment as well as assignments to method local variables and parameters.

- field assignment.

$$\begin{aligned} & \lceil s, \mathcal{E}_1^{src}.f = \mathcal{E}_2^{src}, e \rceil = \\ & \lceil s, \mathcal{E}_1^{src}, e' \rceil; \\ & \lceil e', \mathcal{E}_2^{src}, e - 1 \rceil; \\ & e : \text{putfield } f; \end{aligned}$$

- method local variable or parameter update

$$\begin{aligned} & \lceil s, \text{var} = \mathcal{E}^{src}, e \rceil = \\ & \lceil s, \mathcal{E}^{src}, e - 1 \rceil \\ & e : \text{store } \text{reg}_i; \end{aligned}$$

- try catch statement

$$\begin{aligned} & \lceil s, \text{try } \{STM T_1\} \text{ catch } (\text{ExcClass } \text{var}) \{STM T_2\}, e \rceil = \\ & \lceil s, STM T_1, e' \rceil; \\ & e' + 1 : \text{goto } e + 1; \\ & \lceil e' + 2, STM T_2, e - 1 \rceil; \end{aligned}$$

$\text{addExceptionHandler}(s, e', e' + 2, \text{Class})$

The compiler compiles the normal statement $STM T_1$ and the exception handler $STM T_2$.

- try finally statement

```

 $\lceil s, \text{try } \{STM_1\} \text{ finally } \{STM_2\}, e \rceil =$ 
 $\lceil s, STM_1, e' \rceil;$ 
 $e' + 1 : \text{jsr } e' + 7;$ 
 $e' + 2 : \text{goto } e + 1;$ 

{ default exception handler }
 $e' + 3 : \text{store } l;$ 
 $e' + 4 : \text{jsr } e' + 7;$ 
 $e' + 5 : \text{load } l;$ 
 $e' + 6 : \text{athrow};$ 

{ compilation of the subroutine }
 $e' + 7 : \text{store } k;$ 
 $\lceil e' + 8, STM_2, e - 1 \rceil$ 
 $e : \text{ret } k$ 

```

$\text{addExceptionHandler}(s, e', e' + 8, \text{Exception}))$

We keep close to the JVM (short for Java Virtual Machine) specification, which requires that the subroutines must be compiled using `jsr` and `ret` instructions. The `jsr` actually jumps to the first instruction of the compiled subroutine which starts at index s and pushes on the operand stack the index of the next instruction of the `jsr` that caused the execution of the subroutine. The first instruction of the compilation of the subroutine stores the stack top element in the local variable at index k (i.e. stores in the local variable at index k the index of the instruction following the `jsr` instruction). Thus, after the code of the subroutine is executed, the `ret` k instruction jumps to the instruction following the corresponding `jsr`.

Note:

1. we assume that the local variable e and k are not used in the compilation of the statement STM_1 .
2. here we also assume that the statement STM_1 does not contain a `return` instruction

The compiler adds a default exception handler whose implementation guarantees that in exceptional termination case, the subroutine is also executed. The exception handler is added in the exception handler table.

- try catch finally statement

$\lceil s, \text{try } \{STM_1\} \text{ catch } (Class) \{STM_2\} \text{ finally } \{STM_3\}, e \rceil =$

$\lceil s, \text{try } \{ \text{try } \{STM_1\} \text{ catch } (Class) \{STM_2\} \} \text{ finally } \{STM_3\}, e \rceil$

- throw exception statement

$\lceil s, \text{athrow } \mathcal{E}^{src}, e \rceil =$
 $\begin{array}{l} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{athrow}; \end{array}$

- loop statement

$$\begin{aligned} \lceil s, \text{while } (\mathcal{E}^{\mathcal{R}})[\text{INV}, \text{modif}] \{STMT\}, e \rceil = \\ s : \text{goto } e'; \\ \lceil s + 1, STMT, e' \rceil; \\ [\lceil \text{INV}^{\text{spec}} \rceil, \lceil \text{modif}^{\text{spec}} \rceil] \\ \lceil e' + 1, \mathcal{E}^{\mathcal{R}}, e - 1 \rceil; \\ e : \text{if } s + 1; \end{aligned}$$

- return statement

$$\lceil s, \text{return } \mathcal{E}^{src}, e \rceil = \begin{array}{l} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e : \text{return} \end{array}$$

3.3 Properties of the compiler function

In this last subsection, we will look at the properties of the compiled statements.

Property 3.3.1 (Compilation of statements) *For any statement $STMT$, start label s and end label e , the compiler will produce a sequence of bytecode instruction $\lceil s, STMT, e \rceil$ such that:*

$$\begin{aligned} \forall i, (& \text{inList}(\lceil s, STMT, e \rceil, ins_i)) \wedge \\ & (ins_i \rightarrow ins_k) \wedge \\ & \neg(\text{inList}(\lceil s, STMT, e \rceil, ins_k)) \\ & \neg \text{isExceptionHandlerStart}(ins_k) \Rightarrow \\ & k = e + 1 \end{aligned}$$

The property states that if there are instructions inside the compiled statement $\lceil s, STMT, e \rceil$ which are in execution relation¹ with an instruction ins_k which is not the start of an exception handler and which is outside $\lceil s, STMT, e \rceil$ then $k = e + 1$. The conditions $\neg(\text{inList}(\lceil s, STMT, e \rceil, ins_k))$ and $\neg \text{isExceptionHandlerStart}(ins_k)$ eliminate the case when the execution relation is between an instruction inside $\lceil s, STMT, e \rceil$ which may throw an exception and the start instruction of the proper handler exception handler.

The proof is done by induction on the structure of the compiled statement.

¹see Def. ??

Proof:

We scratch the proof for the compilation of the if statement.

{ by definition of the compiler function for if statements in section 3.2 }
 $\ulcorner s, \text{if } (\mathcal{E}^R) \text{ then } \{STMT_1\} \text{ else } \{STMT_2\}, e^\top =$
 $\ulcorner s, \mathcal{E}^R, e'^\top;$
 $e' + 1 : \text{if } e'' + 2;$
 $\ulcorner e' + 2, STMT_2, e''^\top$
 $e'' + 1 : \text{goto } e + 1;$
 $\ulcorner e'' + 2, STMT_1, e^\top;$

{ induction hypothesis for \mathcal{E}^R and $STMT_1$ and $STMT_2$ }

(1) $\forall i, s \geq i \leq e', ins_i \rightarrow ins_k) \wedge$
 $\neg(inList(\ulcorner s, STMT, e^\top, ins_k))$
 $\neg isExceptionHandlerStart(ins_k) \Rightarrow$
 $k = e' + 1$

(2) $\forall i, e' + 2 \geq i \leq e'', ins_i \rightarrow ins_k) \wedge$
 $\neg(inList(\ulcorner s, STMT, e^\top, ins_k))$
 $\neg isExceptionHandlerStart(ins_k) \Rightarrow$
 $k = e'' + 1$

(3) $\forall i, e'' + 2 \geq i \leq e, ins_i \rightarrow ins_k) \wedge$
 $\neg(inList(\ulcorner s, STMT, e^\top, ins_k))$
 $\neg isExceptionHandlerStart(ins_k) \Rightarrow$
 $k = e + 1$

(4) { from (1),(2) and (3) we get that
jumps from \mathcal{E}^R and $STMT_1$ go inside the compilation of
 $\text{if } (\mathcal{E}^R) \text{ then } \{STMT_1\} \text{ else } \{STMT_2\}$
as $e' + 1$ and $e'' + 1$ are labels in the compilation of the statement and
and that jumps from $STMT_2$ go to $e + 1$ }

(5) { the instruction $e' + 1 : \text{if } e'' + 2;$ may jump
to $e'' + 2$ which is inside the compilation of the if statement }

(6) { the instruction $e'' + 1 : \text{goto } e + 1;$ jumps to $e + 1$ }

from (4), (5) and (6) the lemma holds in that case

Another property of the compiler is that it produces statements that cannot be jumped from outside inside their compilation, i.e. the control flow can reach the instructions representing the compilation $\ulcorner s^\top STMT e$ of statement $STMT$ only by passing through the beginning of the compilation i_s .

Property 3.3.2 (Compilation of statements) *For all statements $STMT'$ and $STMT$, such that*

- $STMT$ is such that it has as substatement $STMT'$, which we denote with $STMT[STMT']$

- their compilations are $\lceil s, \mathcal{S}TM T, e \rceil$ and $\lceil s', \mathcal{S}TM T', e' \rceil$

then :

$$\begin{aligned} & \neg(\exists i_j, \exists i_k, \\ & \quad inList(\lceil s, \mathcal{S}TM T[\mathcal{S}TM T'], e \rceil, i_j) \wedge \\ & \quad \neg inList(\lceil s', \mathcal{S}TM T', e' \rceil, i_j) \wedge \\ & \quad inList(\lceil s', \mathcal{S}TM T', e' \rceil, i_k) \wedge \\ & \quad s \neq k \wedge i_j \rightarrow i_k) \end{aligned}$$

Before proceeding with the properties of the bytecode resulting from the expression compilation, we introduce the notion of block of bytecode instructions.

Definition 3.3.1 (Block of instructions) *If the list of instructions $l = [ins_{i_1} \dots ins_{i_k}]$ is such that*

- none of the instructions is a target of an instruction ins_{i_j} which does not belong to l except for ins_{i_1}
- none of the instructions in the set is a jump instruction, i.e. $\forall m, m = 1..k \Rightarrow \neg(ins_{i_m} \in \{goto, if\})$

We denote such a list of instruction with $ins_{i_1}; \dots; ins_{i_k}$

The next lemma establishes that the compilation of an expression \mathcal{E}^{src} results in a block of bytecode instructions.

Property 3.3.3 (Compilation of expressions) *For any expression \mathcal{E}^{src} , starting label s and end label e , the compilation $\lceil s, \mathcal{E}^{src}, e \rceil$ is a block of bytecode instruction in the sense of Def. 3.3.1 such that $ins_s; \dots; ins_e$ and such that $\neg(\exists 0 \leq j < s, ins_j \in \lceil s, \mathcal{E}^{src}, e \rceil, ins_j \rightarrow^l ins_{j+1})$*

Following the definition 3.3.1 of block of bytecode instructions, the property states that the compilation of an expression results in a list of instructions that cannot be jumped from outside its compilation except for the first instruction of the compilation. This follows from lemma 3.3.2.

Definition 3.3.1 also requires that there are no jump instructions in the list of instructions representing the compilation of an expression. This is established by induction over the structure of the expression.

The lemma also says that there is no loop edge in $\lceil s, \mathcal{E}^{src}, e \rceil$ in the sense of Def. ?? in Chapter ??, Section ??. This is the case, as there are no jump instructions inside the compiled expression and all the instructions inside an expression are sequential .

4 Weakest precondition calculus for source programs

4.1 Source assertion language

The properties that our predicate calculus treats are from first order predicate logic. In the following, we give the formal definition of the assertion language into which the properties are encoded.

this is not well explained

give the definition of the function $isExceptionHandlerStart(ins_k)$

this is not sufficient.

There must be established a property that the \rightarrow^l is only in the compilation of while statements

Formulas 1 (Definition) *The set of formulas is defined inductively as follows*

$$\begin{aligned} \mathcal{F}^{src} ::= & \quad \psi(\mathcal{E}^{spec}, \mathcal{E}^{spec}) \\ & | \text{instances}(\mathcal{E}^{spec}) \\ & | T \\ & | \perp \\ & | \mathcal{F}^{src} \wedge \mathcal{F}^{src} \\ & | \mathcal{F}^{src} \vee \mathcal{F}^{src} \\ & | \mathcal{F}^{src} \Rightarrow \mathcal{F}^{src} \\ & | \forall x(\mathcal{F}^{src}(x)) \\ & | \exists x(\mathcal{F}^{src}(x)) \end{aligned}$$

$$\mathbb{P} ::= \quad == | \neq | \leq | \geq | > | < :$$

$$\begin{aligned} \mathcal{E}^{spec} ::= & \quad \text{constInt} \\ & | \text{true} \\ & | \text{false} \\ & | \text{boundVar} \\ & | \mathcal{E}^{spec} \text{ op } \mathcal{E}^{spec} \\ & | \mathcal{E}^{spec}.f \\ & | \text{var} \\ & | \text{null} \\ & | \text{this} \\ & | \backslash \text{typeof}(\mathcal{E}^{spec}) \\ & | \backslash \text{result} \end{aligned}$$

Note that the expressions in the assertion language are very similar to the expression in the programming language presented in subsection 2.

We define a function which maps expressions from the programming language into the expressions of the assertion language which is denoted and is typed as follows:

$$\lceil \cdot \rceil_{src2spec} : \mathcal{E}^{src} \rightarrow \mathcal{E}^{spec}$$

The function is defined as follows:

$$\begin{aligned} \lceil \text{constInt} \rceil_{src2spec} &= \text{constInt} \\ \lceil \text{true} \rceil_{src2spec} &= \text{true} \\ \lceil \text{false} \rceil_{src2spec} &= \text{false} \\ \lceil \mathcal{E}^{src} \text{ op } \mathcal{E}^{src} \rceil_{src2spec} &= \lceil \mathcal{E}^{src} \rceil_{src2spec} \text{ op } \lceil \mathcal{E}^{src} \rceil_{src2spec} \\ \lceil (Class) \mathcal{E}^{src} \rceil_{src2spec} &= \lceil \mathcal{E}^{src} \rceil_{src2spec} \\ \lceil \mathcal{E}^{src}.m(\mathcal{E}^{src}) \rceil_{src2spec} &= \text{boundVar} \\ \lceil \mathcal{E}^{src}.f \rceil_{src2spec} &= \lceil \mathcal{E}^{src} \rceil_{src2spec}.f \\ \lceil \text{this} \rceil_{src2spec} &= \text{this} \\ \lceil \text{new } Class(\mathcal{E}^{src}) \rceil_{src2spec} &= \text{boundVar} \\ \lceil \mathcal{E}^{src} \text{ instanceof } Class \rceil_{src2spec} &= \backslash \text{typeof}(\lceil \mathcal{E}^{src} \rceil_{src2spec}) <: Class \wedge \lceil \mathcal{E}^{src} \rceil_{src2spec} \neq \text{null} \\ \lceil \mathcal{E}^{src} \mathcal{R} \mathcal{E}^{src} \rceil_{src2spec} &= \lceil \mathcal{E}^{src} \rceil_{src2spec} \mathcal{R} \lceil \mathcal{E}^{src} \rceil_{src2spec} \end{aligned}$$

4.2 Weakest Predicate Transformer for the Source Language

The weakest precondition calculates for every statement $STMT$ from our source language, for any normal postcondition $Post$ and exceptional postcondition function $\text{excPost}^{src} (\text{Exc} \rightarrow STMT \rightarrow \mathcal{F}^{src})$, the predicate Pre such that if it holds in the pre state of $STMT$ and if $STMT$ terminates normally then $Post$ holds in the poststate and if $STMT$ terminates on exception Exc then $\text{excPost}^{src}(Exc, STMT)$ holds. The weakest precondition function has the following signature:

$$\text{wp}^{src} : STMT \rightarrow \mathcal{F}^{src} \rightarrow (\text{Exc} \rightarrow \mathcal{F}^{src}) \rightarrow \mathcal{F}^{src}$$

Before looking at the definition of the weakest predicate transformer we define the exceptional postcondition function excPost^{src} .

4.2.1 Exceptional Postcondition Function

We now look at how the exceptional postconditions for expressions(statements) are managed. As we said the weakest predicate transformer takes into account the normal and exceptional termination of an expression(statement). In both cases the expression(statement) has to satisfy some condition : the normal postcondition in case of normal termination and the exceptional postcondition for exception Exc if it terminates on exception Exc

We introduce a function excPost^{src} which maps exception types to predicates

$$\text{excPost}^{src} : \text{ETypes} \longrightarrow \text{Predicate}$$

The function excPost^{src} returns the predicate $\text{excPost}^{src}(\text{Exc})$ that must hold in a particular program point if at this point an exception of type Exc is thrown.

We also use function updates for excPost^{src} which are defined in the usual way

$$\text{excPost}^{src}[\oplus \text{Exc}' \rightarrow P](\text{Exc}, \text{exp}) = \begin{cases} P & \text{if } \text{Exc} <: \text{Exc}' \\ \text{excPost}^{src}(\text{Exc}, \text{exp}) & \text{else} \end{cases}$$

4.2.2 Expressions

We define the weakest precondition predicate transformer function over expressions. As we will see in the definition below this definition allows us to get the side effect conditions of the expression evaluation, namely the conditions for normal and exceptional termination.

- integer and boolean constant access
($\text{const} \in \{\text{constInt}, \text{true}, \text{false}, \text{constRef}\}$)

$$\text{wp}^{src}(\text{const}, \text{nPost}^{src}, \text{excPost}^{src}) = \text{nPost}^{src}$$

- field access expression

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src}.f, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_1^{src}, \\ & \quad \left[\begin{array}{l} \vdash \mathcal{E}_1^{src} \neg src2spec \neq \text{null} \Rightarrow \text{nPost}^{src} \\ \wedge \\ \vdash \mathcal{E}_1^{src} \neg src2spec = \text{null} \Rightarrow \text{excPost}^{src}(\text{NullPointerExc}, \mathcal{E}_1^{src}) \end{array} \right], \\ & \quad \text{excPost}^{src}) \end{aligned}$$

- arithmetic expressions

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src} \text{ op } \mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_1^{src}, \text{wp}^{src}(\mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}), \text{excPost}^{src}) \end{aligned}$$

- method invocation

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src}.m(\mathcal{E}_2^{src}), \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_1^{src}, \text{wp}^{src}(\mathcal{E}_2^{src}, \\ & \quad \left\{ \begin{array}{l} \mathcal{E}_1^{src} \neq \text{null} \Rightarrow \\ \quad m.\text{Pre}^{src} [\text{this} \leftarrow \mathcal{E}_1^{src}] \\ \quad \quad [\text{arg} \leftarrow \mathcal{E}_2^{src}] \\ \wedge \\ \forall \text{boundVar}, \forall m \in m.\text{modif}^{src} \\ \quad \left\{ \begin{array}{l} \backslash \text{typeof}(\text{boundVar}) <: m.\text{retType} \wedge \\ \quad m.\text{nPost}^{src} [\text{this} \leftarrow \mathcal{E}_1^{src}] \\ \quad \quad [\text{arg} \leftarrow \mathcal{E}_2^{src}] \\ \Rightarrow \text{nPost}^{src}[\vdash \mathcal{E}_1^{src}.m(\mathcal{E}_2^{src}) \neg src2spec \leftarrow \text{boundVar}] \end{array} \right. \\ \wedge \\ \forall E \in m.\text{exceptions}^{src}, \\ \forall m \in m.\text{modif}^{src} \\ \quad m.\text{excPostSpec}^{src}(E) \Rightarrow \text{excPost}^{src}(E) \\ \mathcal{E}_1^{src} = \text{null} \Rightarrow \text{excPost}^{src}(\text{NullPtrExc}) \end{array} \right\}, \\ & \quad \text{excPost}^{src}), \\ & \quad \text{excPost}^{src}) \end{aligned}$$

- Cast expression

$$\begin{aligned} & \text{wp}^{src}((\text{Class}) \mathcal{E}^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}^{src}, \\ & \quad \left[\begin{array}{l} \backslash \text{typeof}(\vdash \mathcal{E}^{src} \neg src2spec) <: \text{Class} \Rightarrow \\ \quad \text{wp}^{src}(\mathcal{E}^{src}, \text{nPost}^{src}, \text{excPost}^{src}) \\ \wedge \\ \neg \backslash \text{typeof}(\vdash \mathcal{E}^{src} \neg src2spec) <: \text{Class} \Rightarrow \\ \quad \text{excPost}^{src}(\text{CastExc}, \mathcal{E}^{src}) \end{array} \right], \\ & \quad \text{excPost}^{src}) \end{aligned}$$

may be give
an example

- Null expression

$$\text{wp}^{src}(\text{null}, \text{nPost}^{src}, \text{excPost}^{src}) = \text{nPost}^{src}$$

- this

$$\text{wp}^{src}(\text{this}, \text{nPost}^{src}, \text{excPost}^{src}) = \text{nPost}^{src}$$

- instance creation

$$\begin{aligned} & \text{wp}^{src}(\text{new } \text{Class}(\mathcal{E}^{src}), \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}^{src}, \\ & \left\{ \begin{array}{l} \text{constr}(\text{Class}).\text{Pre}^{src} [arg \leftarrow \ulcorner \mathcal{E}^{src} \urcorner_{src2spec}] \\ \wedge \\ \forall \text{boundVar}, \\ \quad \text{not instances}(\text{boundVar}) \wedge \\ \quad \text{boundVar} \neq \text{null} \wedge \\ \quad \forall m \in \text{constr}(\text{Class}).\text{modif}^{src}, \\ \quad \text{constr}(\text{Class}).\text{nPost}^{src} \Rightarrow \text{nPost}^{src} \left[\begin{array}{l} \text{this} \leftarrow \text{boundVar} \\ arg \leftarrow \ulcorner \mathcal{E}^{src} \urcorner_{src2spec} \end{array} \right] \\ \quad [\backslash \text{typeof}(\text{boundVar}) \leftarrow \text{Class}] \\ \wedge \\ \forall \text{Exc} \in \text{constr}(\text{Class}).\text{exceptions}^{src}, \\ \quad \forall m \in \text{constr}(\text{Class}).\text{modif}^{src}, \\ \quad \text{constr}(\text{Class}).\text{excPostSpec}^{src}(\text{Exc}) \Rightarrow \text{excPost}^{src}(\text{Exc}) \end{array} \right\}, \\ & \text{excPost}^{src}) \end{aligned}$$

Let us see the relational expressions supported in the source programming language

- Instanceof expression

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}^{src} \text{ instanceof } \text{Class}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}^{src}, \text{nPost}^{src}, \text{excPost}^{src}) \end{aligned}$$

- Binary relation over expressions

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src} \mathcal{R} \mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_1^{src}, \text{wp}^{src}(\mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}), \text{excPost}^{src}) \end{aligned}$$

4.2.3 Statements

- integer and boolean constant access

$$\begin{aligned} & \text{wp}^{src}(\text{STMT}_1; \text{STMT}_2, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\text{STMT}_1, \text{wp}^{src}(\text{STMT}_2, \text{nPost}^{src}, \text{excPost}^{src}), \text{excPost}^{src}) \end{aligned}$$

- assignment

– local variable assignemnt

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src} = \mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_2^{src}, \\ & \quad \text{wp}^{src}(\mathcal{E}_1^{src}, \text{nPost}^{src}[\ulcorner \mathcal{E}_1^{src} \urcorner_{src2spec} \leftarrow \ulcorner \mathcal{E}_2^{src} \urcorner_{src2spec} \rceil], \text{excPost}^{src}), \\ & \quad \text{excPost}^{src}) \end{aligned}$$

– instance field assignemnt

$$\begin{aligned} & \text{wp}^{src}(\mathcal{E}_1^{src}.f = \mathcal{E}_2^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}_1^{src}, \\ & \quad \text{null} \neq \ulcorner \mathcal{E}_1^{src} \urcorner_{src2spec} \Rightarrow \\ & \quad \quad \text{nPost}^{src}[f \leftarrow f \oplus [\ulcorner \mathcal{E}_1^{src} \urcorner_{src2spec} \rightarrow \ulcorner \mathcal{E}_2^{src} \urcorner_{src2spec} \rceil]] \\ & \quad \text{wp}^{src}(\mathcal{E}_2^{src}, \wedge \\ & \quad \quad \text{null} = \ulcorner \mathcal{E}_1^{src} \urcorner_{src2spec} \Rightarrow \\ & \quad \quad \quad \text{excPost}^{src}(\text{NullPointerException}) \\ & \quad \text{excPost}^{src}), \\ & \quad \text{excPost}^{src}) \end{aligned}$$

• if statement

$$\begin{aligned} & \text{wp}^{src}(\text{if } (\mathcal{E}^{src}) \\ & \quad \text{then}\{STM T_1\} \\ & \quad \text{else}\{STM T_2\} \\ & \quad \text{wp}^{src}(\mathcal{E}^{src}, \\ & \quad \quad \ulcorner \mathcal{E}^{src} \urcorner_{src2spec} \Rightarrow \text{wp}^{src}(STM T_1, \text{nPost}^{src}, \text{excPost}^{src}) \\ & \quad \quad \wedge \\ & \quad \quad \neg \ulcorner \mathcal{E}^{src} \urcorner_{src2spec} \Rightarrow \text{wp}^{src}(STM T_2, \text{nPost}^{src}, \text{excPost}^{src}) \\ & \quad \quad \text{excPost}^{src}) \end{aligned}$$

• throw exceptions

$$\begin{aligned} & \text{wp}^{src}(\text{throw } \mathcal{E}^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}^{src}, \\ & \quad \ulcorner \mathcal{E}^{src} \urcorner_{src2spec} \neq \text{null} \Rightarrow \text{excPost}^{src}(\backslash \text{typeof}(\mathcal{E}^{src})) \\ & \quad \ulcorner \mathcal{E}^{src} \urcorner_{src2spec} = \text{null} \Rightarrow \text{excPost}^{src}(\text{NullPointerException}) \\ & \quad \text{excPost}^{src}) \end{aligned}$$

• try catch statement

$$\begin{aligned} & \text{wp}^{src}(\text{try } \{STM T_1\} \text{ catch}(\text{Exc } c) \{STM T_2\}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(STM T_1, \\ & \quad \text{nPost}^{src}, \\ & \quad \text{excPost}^{src} \oplus [\text{Exc} \longrightarrow \text{wp}^{src}(STM T_2, \text{nPost}^{src}, \text{excPost}^{src})]) \end{aligned}$$

- try finally

$$\begin{aligned} & \text{wp}^{src}(\text{try } \{STM T_1\} \text{ finally } \{STM T_2\}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(STM T_1, \\ & \quad \text{wp}^{src}(STM T_2, \text{nPost}^{src}, \text{excPost}^{src}), \\ & \quad \text{excPost}^{src} \oplus [\text{Exception} \longrightarrow \text{wp}^{src}(STM T_2, \text{excPost}^{src}(\text{Exception}), \text{excPost}^{src})]) \end{aligned}$$

where exc is the exception object thrown by $STM T_1$.

- try catch finally

$$\begin{aligned} & \text{wp}^{src}(\text{try } \{STM T_1\} \\ & \quad \text{catch}(\text{Class } c) \{STM T_2\} \text{ finally } \{STM T_3\}, \text{nPost}^{src}, \text{excPost}^{src}) \\ & = \\ & \text{wp}^{src}(\text{try } \{\text{try } \{STM T_1\} \text{ catch}(\text{Class } c) \{STM T_2\}\} \\ & \quad \text{finally } \{STM T_3\}, \text{nPost}^{src}, \text{excPost}^{src}) \end{aligned}$$

- loop statement

$$\begin{aligned} & \text{wp}^{src}(\text{while } (\mathcal{E}^{src}) [\text{INV}, \text{modif}] \{STM T\}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{INV} \wedge \\ & \forall m, m \in \text{modif}, \\ & \text{INV} \Rightarrow \\ & \quad \text{wp}^{src}(\mathcal{E}^{src}, \\ & \quad \quad \begin{aligned} & \quad \quad \quad \vdash \mathcal{E}^{src} \neg src2spec = \text{true} \Rightarrow \text{wp}^{src}(STM T, \text{INV}, \text{excPost}^{src}) \\ & \quad \quad \quad \vdash \mathcal{E}^{src} \neg src2spec = \text{false} \Rightarrow \text{nPost}^{src} \end{aligned} \\ & \quad \text{excPost}^{src}) \end{aligned}$$

- return statement

$$\begin{aligned} & \text{wp}^{src}(\text{return } \mathcal{E}^{src}, \text{nPost}^{src}, \text{excPost}^{src}) = \\ & \text{wp}^{src}(\mathcal{E}^{src}, \text{nPost}^{src}[\backslash \text{result} \leftarrow \vdash \mathcal{E}^{src} \neg src2spec], \text{excPost}^{src}) \end{aligned}$$

where $\backslash \text{result}$ is a specification variable that can be met in the post-condition and denotes to the value returned of a non void method

5 Weakest predicate transformer for Bytecode language

In the following, we introduce a new formulation of the wp function which will be based on the compiler from source to bytecode language. The motivation for this new definition is that it will allow to reason about the relation between source and bytecode proof obligations. Of course, it is also important to see what is the relation between the new definition of the wp introduced here and the definition given earlier in Chapter ??, section ?. We will argue under what conditions both formulations of the wp function produce the same formulas.

We give now a definition of the wp function for a single instruction which takes

explicitly the postcondition and the exceptional postcondition function upon which the precondition will be calculated. Its signature is the following:

$$wp^{bc} : I \rightarrow \mathcal{F}^{bc} \rightarrow (\text{ExcType} \rightarrow \mathcal{F}^{bc}) \rightarrow \mathcal{F}^{bc}$$

For instance, the wp definition for `getfield` is :

$$\begin{aligned} wp^{bc}(\text{getfield } f, \psi, \text{excPost}^{src2bc}, \mathbf{m}) = \\ \text{st}(\text{cntr}) \neq \text{null} \Rightarrow \psi[\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))] \wedge \\ \text{st}(\text{cntr}) = \text{null} \Rightarrow \text{excPost}^{src2bc}(\text{NullPtrExc}) \end{aligned}$$

Note that this differs from the definition of the wp given in Chapter ??, section ?? where the postcondition is a function of the successor of the current instruction. We do not give the rest of the rules because they are the same as the rules presented in ?? except for the fact that the local postconditions are given explicitly.

We also define the weakest predicate transformer function for a sequence of instruction that always execute sequentially as follows:

Definition 5.1 (*wp for a block of instructions*)

$$\begin{aligned} wp_{seq}^{bc}(ins_1; \dots; ins_k, \psi, \text{excPost}^{src2bc}, \mathbf{m}) = \\ wp_{seq}^{bc}(ins_1; \dots; ins_{k-1}, wp^{bc}(ins_k, \psi, \text{excPost}^{src2bc}, \mathbf{m}), \text{excPost}^{src2bc}, \mathbf{m}) \end{aligned}$$

We turn now to the rules for compiled expressions. Note that from Property 3.3.2 it follows that the compilation of any expression is a sequence of instructions of instructions that execute sequentially and there is no jump from outside inside the sequence. Thus, we use the predicate transformer for a sequence of bytecode instructions defined above in order to define the predicate transformer for expressions.

Definition 5.2 (*wp for compiled expressions*) *For any expression \mathcal{E}^{src} , postcondition ψ and exceptional postcondition function excPost^{src2bc} the wp function for the compilation $\lceil \mathcal{E} \rceil$ is $wp_{seq}^{bc}(\lceil \mathcal{E} \rceil, \psi, \text{excPost}^{src2bc}, \mathbf{m})$*

For instance, the rule of the wp for the compilation of access field expression $\mathcal{E}^{src}.f$ where its compilation is

$$\begin{aligned} &\mathcal{E}_1^{src}; \\ &\text{getfield } f \end{aligned}$$

produce the following formula

$$wp_{seq}^{bc} \left(\begin{array}{c} \lceil s, \mathcal{E}^{src}, e - 1 \rceil; \\ e \text{ getfield } f \end{array}, \psi, \text{excPost}^{src2bc}, \mathbf{m} \right)$$

This is equivalent to :

$$wp_{seq}^{bc}(\lceil s, \mathcal{E}^{src}, e - 1 \rceil, wp^{bc}(\text{getfield } f, \psi, \text{excPost}^{src2bc}, \mathbf{m}), \text{excPost}^{src2bc}, \mathbf{m})$$

The function which calculates the wp predicate of a compiled statement is called wp_{stmt}^{bc} and has the following signature :

$$wp_{stmt}^{bc} : \text{Set}(I) \rightarrow \mathcal{F}^{bc} \rightarrow (\text{Exc} \rightarrow \mathcal{F}^{bc}) \rightarrow \mathcal{F}^{bc}$$

The definition of wp_{stmt}^{bc} uses the compiler function defined in Section 3.2

- sequential statement compilation $\lceil s, \text{STMT}_1; \text{STMT}_2, e^\top \rceil$ which by definition is

$$\begin{aligned} wp_{stmt}^{bc}(\lceil s, \text{STMT}_1; \text{STMT}_2, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil) &=_{def} \\ wp_{stmt}^{bc}(\lceil s, \text{STMT}_1, e'^\top; , & \\ wp_{stmt}^{bc}(\lceil e' + 1, \text{STMT}_2, s^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil, & \\ \text{excPost}^{src2bc}, \mathbf{m} \rceil) & \end{aligned}$$

- if statement compilation $\lceil s, \text{if } (\mathcal{E}^R) \text{ then } \{\text{STMT}_1\} \text{ else } \{\text{STMT}_2\}, e^\top \rceil$

$$wp_{stmt}^{bc}(\lceil s, \text{if } (\mathcal{E}^R) \text{ then } \{\text{STMT}_1\} \text{ else } \{\text{STMT}_2\}, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil) =_{def}$$

$$\begin{aligned} & wp_{seq}^{bc}(\lceil s, \mathcal{E}^R, e'^\top; , \\ & \mathcal{R}(\text{st}(\text{cntr}) , \text{st}(\text{cntr} - 1)) \Rightarrow \\ & wp_{stmt}^{bc}(\lceil e'' + 2, \text{STMT}_1, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil [t \leftarrow t - 2] \\ & \wedge \\ & \neg \mathcal{R}(\text{st}(\text{cntr}) , \text{st}(\text{cntr} - 1)) \Rightarrow \\ & wp_{stmt}^{bc}(\lceil e' + 2, \text{STMT}_2, e''^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil [t \leftarrow t - 2] \\ & \text{excPost}^{src2bc}, \mathbf{m} \rceil) \end{aligned}$$

- assignment expression. We will look only at the case for compiled field assignment expressions $\lceil s, \mathcal{E}_1^{src}.f = \mathcal{E}_2^{src}, e^\top \rceil$.

$$\begin{aligned} wp_{stmt}^{bc}(\lceil s, \mathcal{E}_1^{src}.f = \mathcal{E}_2^{src}, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil) &=_{def} \\ wp_{seq}^{bc}(\lceil s, \mathcal{E}_1^{src}, e'^\top, & \\ wp_{seq}^{bc}(\lceil e' + 1, \mathcal{E}_2^{src}, e - 1^\top; , \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil, & \\ \text{excPost}^{src2bc}, \mathbf{m} \rceil) & \end{aligned}$$

- try catch statement compilation

$$\begin{aligned} wp_{stmt}^{bc}(\lceil s, \text{try } \{\text{STMT}_1\} & \\ \text{catch } (\text{ExcClass var}) \{\text{STMT}_2\} , e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil) &=_{def} \\ wp_{stmt}^{bc}(\lceil s, \text{STMT}_1, e'^\top; e' + 1 : \text{goto } e + 1; , & \\ \psi, & \\ \text{excPost}^{src2bc}[\oplus \text{ExcClass} \rightarrow wp_{stmt}^{bc}(\lceil e' + 2, \text{STMT}_2, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m} \rceil), \mathbf{m}] & \end{aligned}$$

- try finally statement compilation $\lceil s, \text{try } \{\text{STMT}_1\} \text{ finally } \{\text{STMT}_2\}, e^\top \rceil$

$$\begin{aligned}
& wp_{stmt}^{bc}(\ulcorner s, \text{try } \{STM T_1\} \\
& \quad \text{finally } \{STM T_2\} \urcorner, e^\top, \psi, \text{excPost}^{src2bc}, \mathbf{m}) =_{def} \\
& \quad \ulcorner s, STM T_1, e'^\top \\
& wp_{stmt}^{bc}(e' + 1 : \text{jsr } e' + 7; \quad , \\
& \quad e' + 2 : \text{goto } e + 1; \\
& \quad e' + 7 : \text{store } k; \\
& wp_{stmt}^{bc}(\ulcorner e' + 8, STM T_2, e - 1^\top \urcorner, \psi, \text{excPost}^{src2bc}, \mathbf{m}), \\
& \quad e : \text{ret } k \\
& \quad e' + 3 : \text{store } l; \\
& \text{excPost}^{src2bc}[\oplus \text{ExcClass} \rightarrow wp_{stmt}^{bc}(e' + 4 : \text{jsr } e' + 7; \\
& \quad e' + 5 : \text{load } l; \quad , \psi, \text{excPost}^{src2bc}, \mathbf{m})], \mathbf{m}) \\
& \quad e' + 6 : \text{athrow};
\end{aligned}$$

- throw exception compilation $\ulcorner s, \text{throw } \mathcal{E}^{src}, e^\top$

$$\begin{aligned}
& wp_{stmt}^{bc}(\ulcorner s, \text{throw } \mathcal{E}^{src}, e^\top \urcorner, \psi, \text{excPost}^{src2bc}, \mathbf{m}) =_{def} \\
& wp_{seq}^{bc}(\ulcorner s, \mathcal{E}^{src}, e - 1^\top \urcorner, wp^{bc}(e / \text{athrow}, \psi, \text{excPost}^{src2bc}, \mathbf{m}), \text{excPost}^{src2bc}, \mathbf{m})
\end{aligned}$$

- loop statement

5.1 Properties of the wp functions

The previous subsection introduced a new formulation of the wp function for bytecode which takes into account the source statement from which it is compiled. However, it is important to establish a relation between this new definition and the wp formulation given in Chapter wp??. The following statements establish the relation between the two versions of the wp calculus.

Lemma 5.1.1 (Equivalence of the formulations for single instructions)

For all instructions i_j and i_k which belong to method \mathbf{m} , formula ψ , and function $\text{excPost}^{src2bc} : \text{ExcType} \rightarrow \mathcal{F}^{bc}$ such that

- $i_j \rightarrow i_k$
- $\psi = \text{inter}(i_j, i_k)$
- $\forall \text{Exc}, \text{excPost}^{src2bc}(\text{Exc}) = \mathbf{m}.\text{excPost}(\text{Exc}, j)$

the following holds

$$wp^{bc}(i_j, \psi, \text{excPost}^{src2bc}, \mathbf{m}) = wp(i_j, \mathbf{m})$$

The proof is done by case analysis on the instruction i_j . In the following, we scratch the case for a `getfield` instruction *Proof*:

note that the current formulation is not true because of the exceptions

$\{ \text{ by hypothesis } \}$
 $i_j = \text{getfield } f$
 $\{ \text{ by definition of the wp function } \}$
 $(1) \text{wp}^{bc}(\text{getfield } f, \psi, \text{excPost}^{src2bc}, \mathbf{m}) =$
 $\text{st}(\text{cntr}) \neq \text{null} \Rightarrow \psi[\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))]$
 \wedge
 $\text{st}(\text{cntr}) = \text{null} \Rightarrow \text{excPost}^{src2bc}(\text{NullPtrExc})$

 $\{ \text{ by definition of the wp function } \}$
 $(2) \text{wp}(\text{getfield } f, \mathbf{m}) =$
 $\text{st}(\text{cntr}) \neq \text{null} \Rightarrow \text{inter}(i_j, i_k) [\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))]$
 \wedge
 $\text{st}(\text{cntr}) = \text{null} \Rightarrow \text{excPost}(\text{NullPtrExc}, j)$

 $\{ \text{ from the initial hypothesis, (1) and (2) the lemma holds in that case } \}$

Lemma 5.1.2 (wp for a block of instructions) *For every block of instructions $ins_1; \dots; ins_j$ and instruction ins_k in method \mathbf{m} , formula ψ and function $\text{excPost}^{src2bc} : \text{ExcType} \rightarrow \mathcal{F}^{bc}$ such that*

- $ins_j \rightarrow ins_k$
- $\psi = \text{inter}(ins_j, ins_k)$
- $\forall \text{Exc}, \text{excPost}^{src2bc}(\text{Exc}) = \mathbf{m}.\text{excPost}(\text{Exc}, j)$
- $\forall 1 \leq i, k \leq j, \text{findExcHandler}(\text{Exc}, i, \mathbf{m}.\text{excHndlS}) = \text{findExcHandler}(\text{Exc}, k, \mathbf{m}.\text{excHndlS})$

then the following holds

$$\text{wp}_{seq}^{bc}(ins_1; \dots; ins_j, \psi, \text{excPost}^{src2bc}, \mathbf{m}) = \text{wp}(ins_1, \mathbf{m})$$

The proof is done by induction on the length of the sequence of instructions.
Proof:

Lemma 5.1.3 (wp for compiled expressions) *For every compiled expression $\ulcorner s, \mathcal{E}^{src}, e \urcorner$ in method \mathbf{m} formula ψ and function $\text{excPost}^{src2bc} : \text{ExcType} \rightarrow \mathcal{F}^{bc}$ such that*

- $\psi = \text{inter}(e, e+1)$ then the following holds
- $\forall \text{Exc}, \text{excPost}^{src2bc}(\text{Exc}) = \mathbf{m}.\text{excPost}(\text{Exc}, j)$
- $\forall 1 \leq i, k \leq j, \text{findExcHandler}(\text{Exc}, i, \mathbf{m}.\text{excHndlS}) = \text{findExcHandler}(\text{Exc}, k, \mathbf{m}.\text{excHndlS})$

then the following holds:

$$\text{wp}_{seq}^{bc}(\ulcorner s, \mathcal{E}^{src}, e \urcorner, \psi, \text{excPost}^{src2bc}, \mathbf{m}) = \text{wp}(ins_s, \mathbf{m})$$

Proof: From Property 3.3.1 of the compiler it follows that for every expression \mathcal{E}^{src} , start label s and end label e , the resulting compilation $\lceil s, \mathcal{E}^{src}, e \rceil$ is a block of instructions. We can apply the previous lemma 5.1.2 and we get the result.

Lemma 5.1.4 *For every compiled statement $\lceil s, STMT, e \rceil$ in method m , formula ψ and function $\text{excPost}^{src2bc} : \text{ExcType} \rightarrow \mathcal{F}^{bc}$ such that*

- $\psi = \text{inter}(e, e + 1)$
- $\forall \text{Exc}, \text{excPost}^{src2bc}(\text{Exc}) = m.\text{excPost}(\text{Exc}, j)$

then the following holds: if $\psi = \text{inter}(e, e + 1)$ then the following holds

$$wp_{stmt}^{bc}(\lceil s, STMT, e \rceil, \psi, \text{excPost}^{src2bc}, m) = wp(\text{ins}_s, m)$$

6 Auxiliary Properties

Before stating the main theorem we need some auxiliary properties. First, we establish that adding a `goto` instruction to a sequence of instructions does not change the weakest predicate of the augmented bytecode sequence.

Lemma 1 *Assume that we have the block of bytecode instructions $i_1; \dots; i_k$ where $\text{next}(i_k) = i_{k+1}$*

$$wp_{seq}^{bc}(i_1; \dots; i_k, \psi, \text{excPost}, m) = wp_{seq}^{bc}(i_1; \dots; i_k; \text{goto } l, \psi, \text{excPost}, m)$$

The proof is based on the fact that the instruction `goto` does not have side effects and thus, the following holds: $wp_{seq}^{bc}(\text{goto } l, \psi, \text{excPost}, m) = \psi$

We now turn to see how the execution of the compilation $\lceil \mathcal{E}^{src} \rceil$ of an expression \mathcal{E}^{src} affects the operand stack. In particular, we claim that if the execution of the compiled expression $\lceil \mathcal{E}^{src} \rceil$ terminates normally then the stack top contains the value of the expression $\lceil \mathcal{E}^{src} \rceil^{spec}$. This actually reflects how we expect that the virtual machine execute bytecode programs.

This fact in terms of weakest preconditions can be expressed as follows:

Lemma 2 (Wp of a compiled expression) *For any expression \mathcal{E}^{src} from our source language, for any formula $\psi : \mathcal{F}^{src}$ of the source assertion language and any formula $\phi : \mathcal{F}^{bc}$ such that ϕ may only contain stack expressions of the form $\text{st}(\text{cntr} - k)$, $k \geq 0$, there exist $Q, R : \mathcal{F}^{src}$ such that the following holds*

$$\begin{aligned}
& \bullet \quad wp^{src}(\mathcal{E}^{src}, \psi, \text{excPost}) \equiv \\
& \quad Q \Rightarrow \psi \\
& \quad \wedge \\
& \quad R \\
& \quad \leftrightarrow \\
& \quad wp_{seq}^{bc}(\lceil \mathcal{E}^{src} \rceil, \phi, \lceil \text{excPost} \rceil, m) \equiv \\
& \quad \lceil Q \rceil^{spec} \Rightarrow \phi \left[\begin{array}{l} \text{cntr} \leftarrow \text{cntr} + 1 \\ \text{st}(\text{cntr} + 1) \leftarrow \lceil \mathcal{E}^{src} \rceil^{spec} \end{array} \right] \\
& \quad \wedge \\
& \quad \lceil R \rceil^{spec}
\end{aligned}$$

•

$$\begin{aligned}
& wp^{src}(\mathcal{E}^{src}[\mathcal{E}_1^{src}], \psi, \text{excPost}) \equiv \\
& \forall \text{boundVar}, Q \Rightarrow \psi[\ulcorner \mathcal{E}_1^{src} \urcorner^{src2spec} \leftarrow \text{boundVar}] \\
& \wedge \\
& R \\
& \leftrightarrow \\
& wp_{seq}^{bc}(\ulcorner \mathcal{E}^{src} \urcorner, \psi, \ulcorner \text{excPost} \urcorner, \mathbf{m}) \equiv \\
& \forall \text{boundVar}, \ulcorner Q \urcorner^{spec} \Rightarrow \phi \left[\begin{array}{l} \text{cntr} \leftarrow \text{cntr} + 1 \\ \text{st}(\text{cntr} + 1) \leftarrow \ulcorner \mathcal{E}^{src}[\text{boundVar}] \urcorner^{spec} \end{array} \right] \\
& \wedge \\
& \ulcorner R \urcorner^{spec}
\end{aligned}$$

We proceed with several cases of the proof, which is done by induction over the structure of the formula

Proof :

1. $\mathcal{E}^{src} = \text{const}, \text{const} \in \text{constInt}, \text{true}, \text{false}$

$$\begin{aligned}
& \{ \text{source case} \} \\
& (1) wp^{src}(\text{const}, \psi, \text{excPost}) \\
& \{ \text{following the definition of the wp function for source expressions in subsection 4.2} \} \\
& \equiv \psi
\end{aligned}$$

$$\begin{aligned}
& \{ \text{bytecode case} \} \\
& (2) wp_{seq}^{bc}(\ulcorner \text{const} \urcorner, \phi, \ulcorner \text{excPost} \urcorner, \mathbf{m}) \\
& \{ \text{following the definition of the compiler function in subsection 3.1} \} \\
& \equiv wp_{seq}^{bc}(\text{push} \ulcorner \text{const} \urcorner^{spec}, \phi, \ulcorner \text{excPost} \urcorner, \mathbf{m}) \\
& \{ \text{following the definition of the wp function for bytecode in subsection 5} \} \\
& \equiv \phi \left[\begin{array}{l} \text{cntr} \leftarrow \text{cntr} + 1 \\ \text{st}(\text{cntr} + 1) \leftarrow \ulcorner \text{const} \urcorner^{spec} \end{array} \right]
\end{aligned}$$

$$\{ \text{from (1) and (2) and } Q, R = T \text{ this case holds} \}$$

$$2. \mathcal{E}^{src} = \mathcal{E}^{src}.f$$
$$\begin{aligned} & \{ \text{source case} \} \\ (1) & \text{wp}^{\text{src}}(\mathcal{E}^{\text{src}}.f, \psi, \text{excPost}) \\ & \{ \text{following the definition of the wp function} \\ & \text{for source expressions in subsection 4.2} \} \\ & \quad \vdash \mathcal{E}^{\text{src}} \sqcap \text{src2spec} \neq \mathbf{null} \Rightarrow \psi \\ \equiv & \text{wp}^{\text{src}}(\mathcal{E}^{\text{src}}, \bigwedge_{\vdash \mathcal{E}^{\text{src}} \sqcap \text{src2spec} \neq \mathbf{null} \Rightarrow \text{excPost}(\text{NullPtrExc})}, \text{excPost}) \end{aligned}$$
$$\begin{aligned} & \{ \text{bytecode case} \} \\ (2) & \text{ } wp_{seq}^{bc}(\ulcorner \mathcal{E}^{src}.f \urcorner, \phi, \ulcorner \text{excPost}^\top, \mathbf{m} \urcorner) \\ & \{ \text{following the definition of the compiler function in subsection 3.1} \} \end{aligned}$$
$$\equiv wp_{seq}^{bc}(\ulcorner \mathcal{E}^{src} \urcorner, \text{getfield } f, \phi, \text{excPost}, m)$$

{following the definition of the wp function for bytecode in subsection 5 }

$$\begin{aligned} &\equiv wp_{seq}^{bc}(\ulcorner \mathcal{E}^{src} \urcorner, \\ &\quad \text{st}(\text{cntr}) \neq \text{null} \Rightarrow \\ &\quad \phi[\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))] \\ &\quad \wedge \\ &\quad \text{st}(\text{cntr}) = \text{null} \Rightarrow \ulcorner \text{excPost} \urcorner(\text{NullPtrExc}) \\ &\quad \ulcorner \text{excPost} \urcorner, m) \\ &\{ \text{from (1) and (2) we apply the induction hypothesis} \} \end{aligned}$$
$$\begin{aligned} & \exists Q', R' : \mathcal{F}^{src}, \\ & \quad \vdash \mathcal{E}^{src} \sqcap_{src2spec} \neq \text{null} \Rightarrow \psi \\ (\mathcal{G}) \quad & \text{wp}^{src}(\mathcal{E}^{src}, \bigwedge_{\vdash \mathcal{E}^{src} \sqcap_{src2spec} \neq \text{null} \Rightarrow \text{excPost}(\text{NullPtrExc})}, \text{excPost}) \end{aligned}$$
$$\begin{array}{l} \equiv \\ Q' \Rightarrow \bigwedge \\ \quad \vdash \mathcal{E}^{src \sqcap src2spec} \neq \text{null} \Rightarrow \psi \\ \quad \vdash \mathcal{E}^{src \sqcap src2spec} \neq \text{null} \Rightarrow \text{excPost}(\text{NullPtrExc}) \end{array}$$
$$\wedge$$

$$R'$$
$$(4) \text{wp}_{seq}^{bc}(\ulcorner \mathcal{E}^{src} \urcorner, \\ \text{st}(\text{cntr}) \neq \text{null} \Rightarrow \\ \phi[\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))] \\ \wedge \\ \text{st}(\text{cntr}) = \text{null} \Rightarrow \ulcorner \text{excPost} \urcorner(\text{NullPntrExc}) \\ \ulcorner \text{excPost} \urcorner, \text{m})$$
$$\begin{array}{c}
\equiv \\
\vdash Q' \Rightarrow \text{st}(\text{cntr}) \neq \text{null} \Rightarrow \phi[\text{st}(\text{cntr}) \leftarrow f(\text{st}(\text{cntr}))] \\
\wedge \\
\text{st}(\text{cntr}) \neq \text{null} \Rightarrow \vdash \text{excPost}^\top(\text{NullPtrExc}) \\
\wedge \\
\vdash R' \top \\
\equiv
\end{array}
\quad
\begin{array}{c}
[\text{cntr} \leftarrow \text{cntr} + 1] \\
[\text{st}(\text{cntr} + 1) \leftarrow \vdash \mathcal{E}^{src \top spec}]
\end{array}$$

$$\begin{aligned}
& \{ \phi \text{ contains only stack expressions } \text{st}(\text{cntr} - k), k \geq 0 \text{ and properties of substitution} \} \\
& \vdash Q' \Rightarrow \begin{aligned} & \vdash \mathcal{E}^{src} \neg spec \neq \text{null} \Rightarrow \phi \begin{aligned} & [\text{cntr} \leftarrow \text{cntr} + 1] \\ & [\text{st}(\text{cntr} + 1) \leftarrow \vdash f(\mathcal{E}^{src}) \neg spec] \end{aligned} \\ & \wedge \\ & \vdash \mathcal{E}^{src} \neg spec \neq \text{null} \Rightarrow \vdash \text{excPost} \neg (\text{NullPtrExc}) \\ & \wedge \\ & \vdash R' \end{aligned} \\
& \{ \text{from (3) and (4) this case holds} \}
\end{aligned}$$

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