

# Weekly Progress Report

## Capstone Team 8 RISC-V CleanFlight/BetaFlight Port

### Week #13

**Date: 03/30 - 04/06**

#### Authors:

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#### Summary:

##### Bliss check ins:

- **Monday**
  - **Done**
    - Located LDFLAGS in the Kendryte SDK
  - **Doing**
    - Researching Linker/Compiler Flags
- **Tuesday**
  - **Done**
    - Researching Linker/Compiler Flags
  - **Doing**
    - Re-building hello\_world example
- **Wednesday**
  - **Done**
  - **Doing**
    - Re-building hello\_world example
    - Figuring out how to flash the board
- **Thursday**
  - **Done**
  - **Doing**
    - Re-building hello\_world example
    - Figuring out how to flash the board
- **Friday**
  - **Done**
    - Re-building hello\_world example

- **Doing**
  - Figuring out how to flash the board
  - Flashing gpio\_led example to the board using Kendryte SDK

#### Eric check ins:

- **Monday**
  - **Done**
    - Finished adding CFLAGS, LD\_FLAGS, ARCH\_FLAGS to MAIXBIT.mk
  - **Doing**
    - Looking into how the top-level Makefile points to ARM GNU toolchain
- **Tuesday**
  - **Done**
    - Added directory paths for RISC-V toolchain to Makefile (now points to proper toolchain to build for RISC-V)
  - **Doing**
    - Debugging linker errors from BF make process during MAIXBIT build
- **Wednesday**
  - **Done**
  - **Doing**
    - Still debugging linker errors from make process
- **Thursday**
  - **Done**
    - Finished debugging linker errors
  - **Doing**
- **Friday**
  - **Done**
  - **Doing**
    - Installing Kendryte RISC-V toolchain on my VM - currently it is throwing errors. Will resolve these over the weekend.

#### Niko check ins:

- **Monday**
  - **Done**
  - **Doing** debugging and testing
- **Tuesday**
  - **Done** .hex file with the Betaflight Makefile, Spring Planning
  - **Doing** debugging
- **Wednesday**
  - **Done** documentation
  - **Doing** debugging
- **Thursday**
  - **Done** building a demo example from Kendryte
  - **Doing** debugging

- Friday
  - Done
  - Doing debugging and testing

Ruben check ins:

- Monday
  - Done
    - Researched mem config for FC
    - Code change for mem config
  - Doing
- Tuesday
  - Done
    - Pushed code change to capstone-port
    - Had one-one meeting with Roy
  - Doing
    - Started with main/platform.h code
- Wednesday
  - Done
  - Doing
    - Code changes to main/platform.h code
- Thursday
  - Done
    - Made changes to main/platform.h code
  - Doing
    - Integrating my changes to Niko's code
- Friday
  - Done
  - Doing
    - Integrating my changes to Niko's code

**Weekly Issues:**

1. Ex
2. Ex2

**Notes:**

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