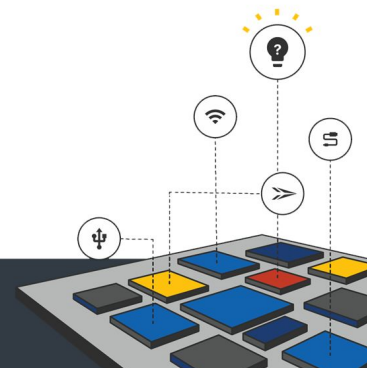
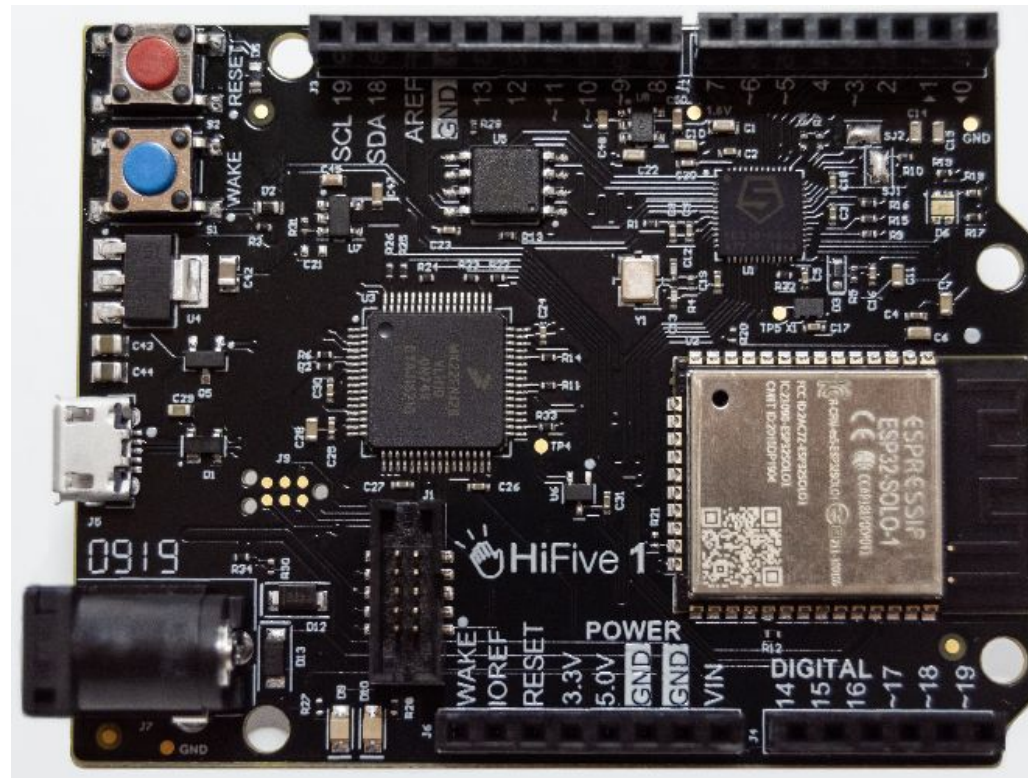
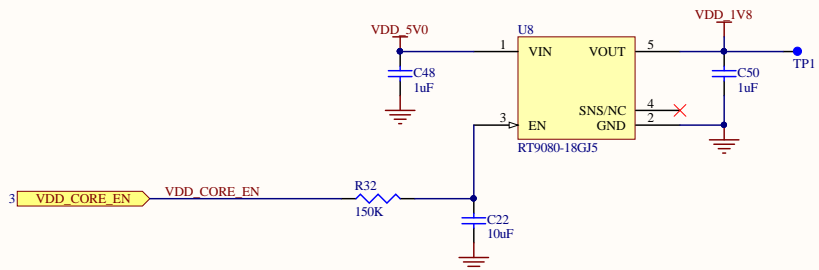
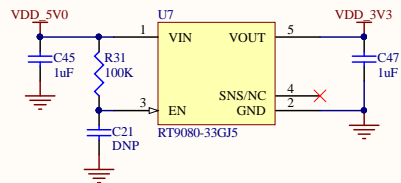
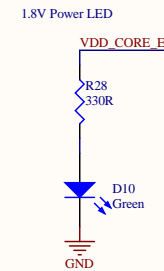
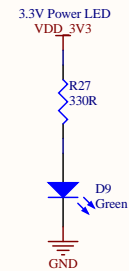
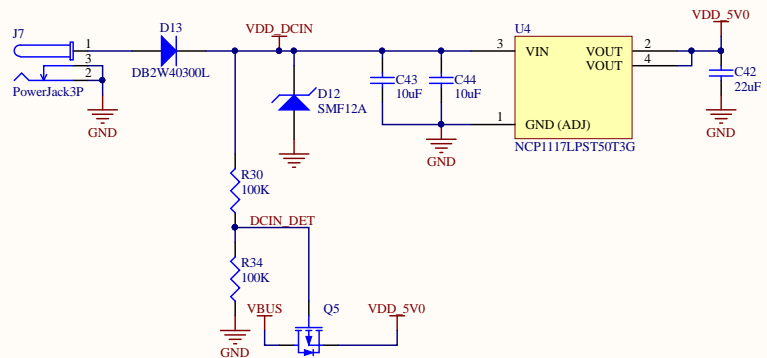


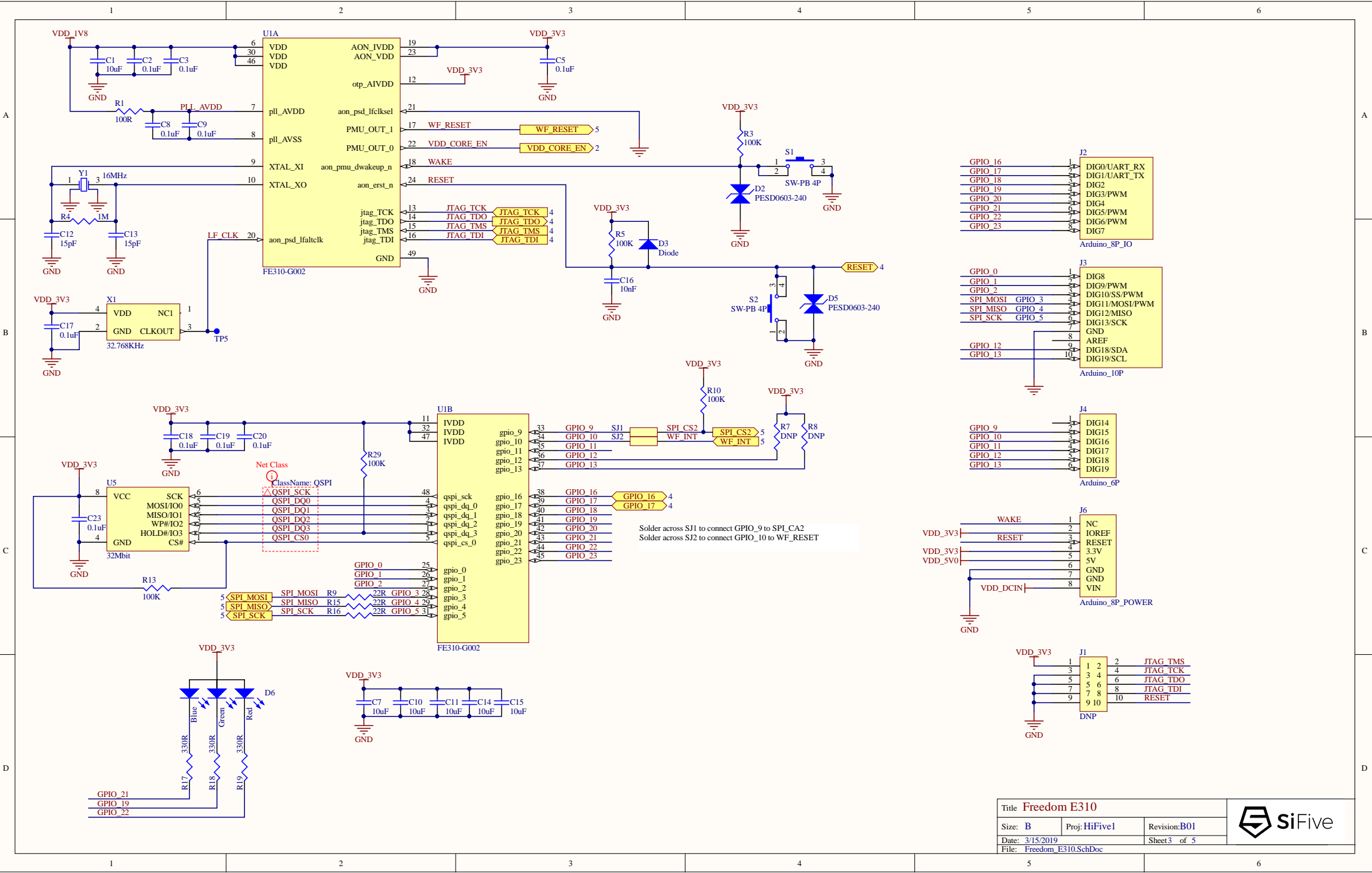
HiFive 1 Rev B01

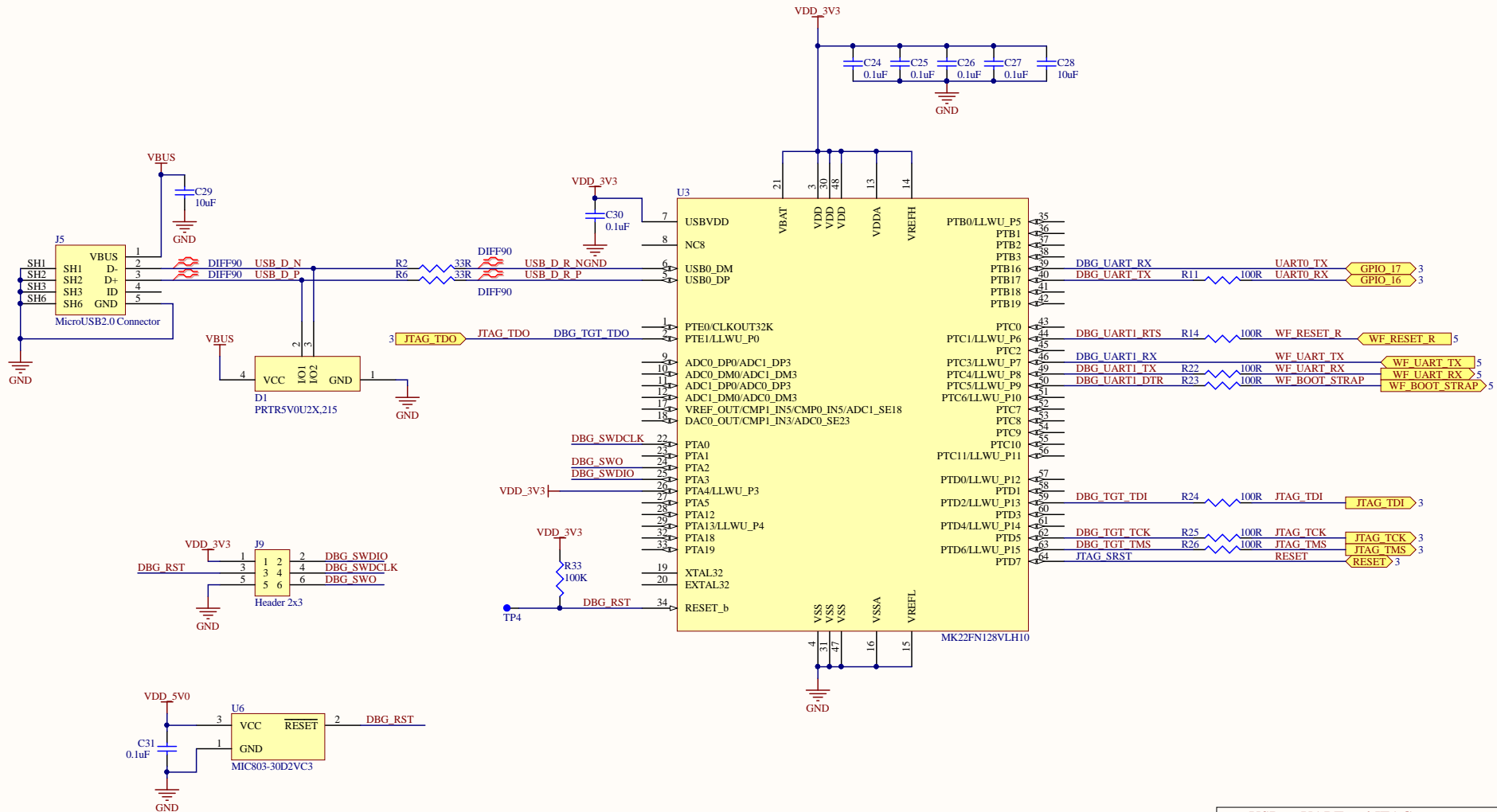




Title Power Supply		
Size: B	Proj: HiFive1	Revision: B01
Date: 3/15/2019		Sheet 2 of 5
File: Power.SchDoc		



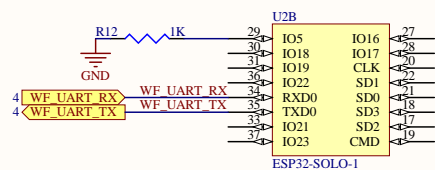
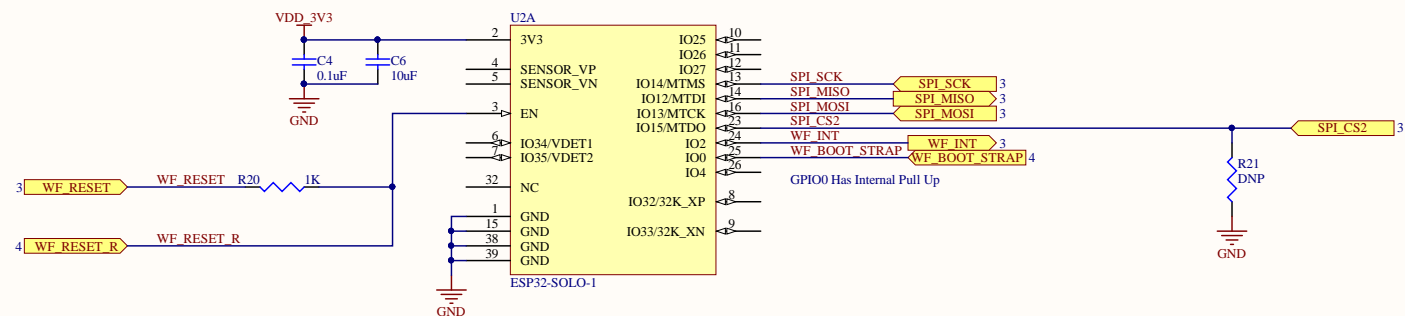




Title: USB to UART and JTAG

Size: B Proj: HiFive1 Revision: B01
Date: 3/15/2019 Sheet 4 of 5
File: Debug_SchDoc





Timing of SDIO Slave Boot Strap					
Pin	Default	Falling-edge Input	Falling-edge Input	Rising-edge Input	Rising-edge Input
		Falling-edge Output	Rising-edge Output	Falling-edge Output	Rising-edge Output
MTDO	Internal Pull-up	0	0	1	1
GPIO5	Internal Pull-up	0	1	0	1

Title **WiFi and Bluetooth**

Size: **B**

Proj: **HiFive1**

Revision: **B01**

Date: **3/15/2019**

Sheet **5** of **5**

File: **WiFi_BT_SchDoc**

