Makefile Flowchart

Specify a target FC (ex: SPRACINGF3)

Makefile selects .c source files from src/main directory to be compiled. (Based on FC specified above).

Store selected .c source files in variable: "SRC"

Create an empty .o file for each .c file listed in "SRC"

Store empty .o files in variable: "TARGET_OBJS"

Compilation → Compile .c source files listed in "SRC" into .s assembly files. Store in obj/main/<target>

Assembly → Assemble the new .s files into .o files. Store in obj/main/<target>

(This step essentially fills all the empty .o files listed in "TARGET_OBJS").

Link → Link the new .o files using a target-specific .elf file.

Executable → Create the final .hex executable file. Store in obj/main

(This is the file that gets flashed into the FC's program memory).