# 承 认 书 Specification For Approval

Customer: (客户)		
Description:(产品描述)	LED	
Part number:(产品型号)	FT-2020RGBC4UDA-IC	
Date: (日期)	2019-05-06	
Approved By: (客户承认)		

Approval	Engineering	<b>Design</b>	Sales
核准	工程	制作	业务
余清泉	黄越	欧阳绪金	

Prepared By:(我司承认)

### ■ Maximum Rating(Ta=25°C)

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Parameter	Symbol	Rating	Unit		
IC Power Supply Voltage	VCC	+4.5~+5.5	V		
LED voltage	Vled	3-7.5	V		
Rate of data signal	F <sub>CLK</sub>	20	MHZ		
The max led output Current	Iomax	18	mA		
Channel current deviation	D <sub>IO</sub>	Channel<3%,chip<5%	%		
Power dissipation;	$P_{D}$	<40050	mW		
Soldering Temperature*1	$T_{SD}$	260	°C		
Operating Temperature Range	-40°C to+85°C				
Storage Temperature Range		-40°C to+105°C			

Notes 1: The maximum of soldering time is 5 seconds in T<sub>SD</sub>

### ■ Typical Product Characteristics( $Ta=25^{\circ}C$ )

Characteristics	Symbol		Min.	Тур.	Max.	Unit	Test condition
Forward Voltage	$V_{\mathrm{F}}$		4.5		5.5	V	I <sub>F</sub> =18mA
		R	-	230	-		
Lumin our Totansitu	T	G	-	320	-		I 10 A
Luminous Intensity	Iv	В	-	80	-	mcd	I <sub>F</sub> =18mA
		W	-	530	-		
	λd	R	615	-	630	nm	I <sub>F</sub> =18mA
Dominant Wavelength		G	520	-	535		
		В	460	-	475		
Calar Caardinata	X			0.2257			I 10 A
Color Coordinate	у			0.2234			I <sub>F</sub> =18mA
View Angle	2θ <sub>1/</sub>	2	-	120	-	deg	I <sub>F</sub> =18mA

### **■** Electrical Characteristics (Ta=25°C)

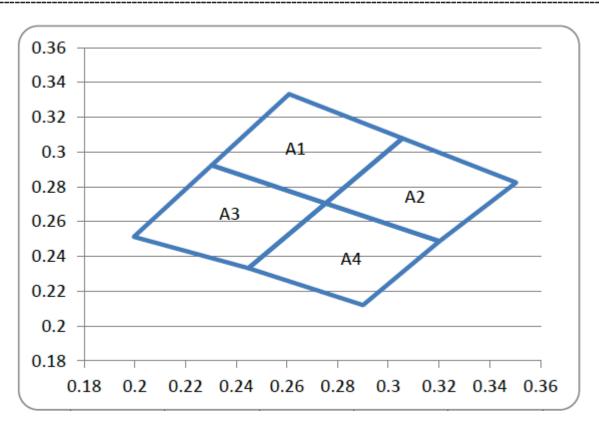
Characteristics	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	$V_{cc}$			5.0		V
Input voltage	V <sub>IN</sub>		-0.4		5	V
Rate of data signal	Fclk		0	15	-	MHZ
	V <sub>IH</sub>	D <sub>IN</sub> , SET	0.7 V <sub>cc</sub>	-	-	V
Input voltage level	VIL	D <sub>IN</sub> , SET	-	-	0.3 V <sub>cc</sub>	V
The clock high level widch	T <sub>CLKH</sub>		30	1	1	ns
The clock low level widch	T <sub>CLKL</sub>		30	-	-	ns
Data set up time	TSETUP		10	-	-	ns
Data hold time	T <sub>HOLD</sub>		5	-	-	ns

### ■ Range of Bins

#### 1) Luminous Intensity-White ( $I_F = 18mA$ )

Bin Code	Min. IV (mcd)	Max. IV (mcd)
11	350	460
12	460	600
13	600	780
14	780	1000

### **■** Color Coordinate Comparison-White



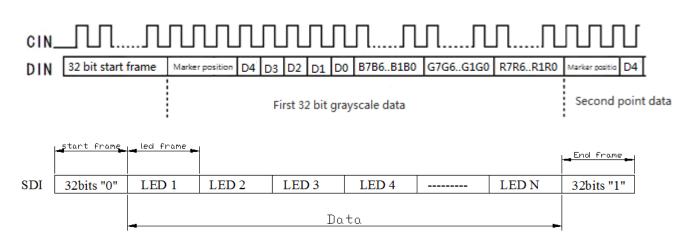
#### **Color Ranks**

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A1	0. 2609	0. 3332	0. 3056	0. 3078	0. 2752	0. 2705	0. 2303	0. 2923
A2	0. 3056	0. 3078	0. 3504	0. 2824	0. 3202	0. 2487	0. 2752	0. 2705
A3	0. 2303	0. 2923	0. 2752	0. 2705	0. 2448	0. 2332	0.1996	0. 2513
A4	0. 2752	0. 2705	0. 3202	0. 2487	0.29	0. 212	0. 2448	0. 2332

### ■ Switching Characteristics (Ta=25°C)

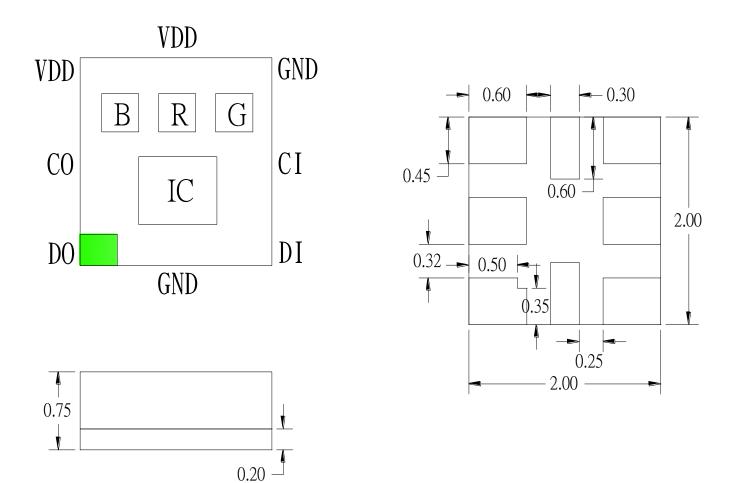
Characteristics	Symbol	Condition	Min.	Тур.	Max.	Unit
Transfer time	Ттнн	CL=30pF,RL=1KΩ	-	-	15	ns
Transfer time	T <sub>THL</sub>	CE-30p1,RE-111	-	-	15	ns
	Tpd		-	-	12	ns
Signal delay time	Тсо	CL=30pF,RL=1KΩ	-	-	12	ns
Signal rise and fall time	$T_{R}$		-	-	500	ns
Signal fise and fair time	T <sub>F</sub>	Vcc=5V			400	ns
The output minimum PWM opening width	TONMIN	I <sub>OUT=</sub> 18mA	200	-	-	ns
The output signal	Ton	I 10 A	-		80	ns
maximum opening and closing time	T <sub>Off</sub>	I <sub>OUT=</sub> 18mA	-	-	80	ns

### ■ Communication protocol and timing (Ta=25°C)

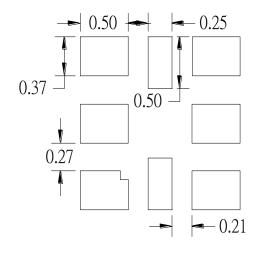


- 1. The first 32 bit "0" is the starting frame, and the CIN is on the rise time, and the timing DIN is preceded by CIN
- 2. It is marked as "1"
- 3. D4 D3 D2 D1 and D0 are 32 level brightness adjustment, D4 is the highest
- **4.** The gray level data to high, the sequence is B/G/R

#### Dimensions

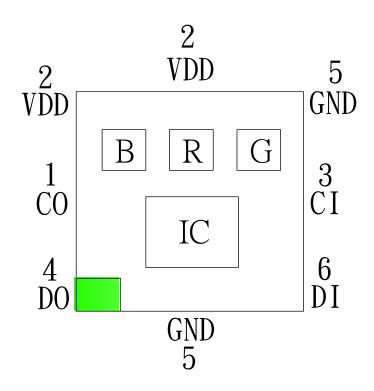


### Recommend pad layout



- § All dimensions are in millimeters.
- § Tolerance is  $\pm 0.1$ mm unless other specified
- § Specifications are subject to change without notice

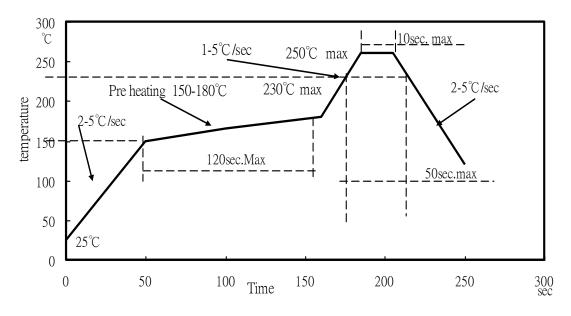
### ■ PIN Configuration



No.	Symbol	Function description
1	СО	Clock output
2	VDD	supply voltage
3	CI	Clock input
4	DO	Data output
5	GND	Ground
6	DI	Data input

#### **■** Reflow Profile

#### 1. I<sub>R</sub> reflow soldering Profile for Lead Free solder



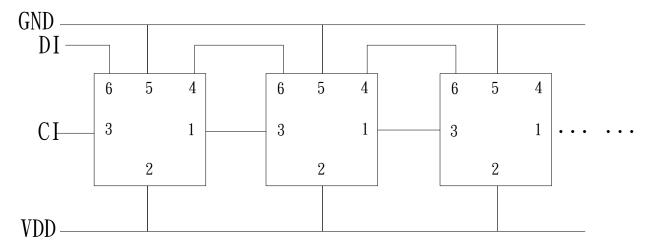
#### **Notes:**

- 1. We recommend the reflow temperature at  $240^{\circ}\text{C}$  ( $\pm 5^{\circ}\text{C}$ ), and the maximum soldering temperature should be limited to  $250^{\circ}\text{C}$ .
- 2. Don't cause stress to the silicone resin while it is exposed to high temperature.
- 3. Number of reflow process shall not be more than 1 time.

### ■ Test Circuit and Precautions for Use

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#### 1. Typical application circuit



#### 2. Precautions for Use

#### 2.1. Over-current-proof

Customer must apply resistors for protection; otherwise slight voltage shift will cause big current change (Burn-out will happen).

#### 2.2. Storage

1). To store the products is recommended with following conditions:

Humidity: 60% R.H. Max.

Temperature:  $5^{\circ}\text{C} \sim 30^{\circ}\text{C} (41^{\circ}\text{F} \sim 86^{\circ}\text{F})$ 

2). Shelf life in sealed bag: 12 month at  $<5^{\circ}\text{C} \sim 30^{\circ}\text{C}$  and <60% R.H. after the package is Opened, the products should be used within 1 week or they should be keeping to stored at  $\leq 20\%$  R.H. with zip-lock sealed.

#### 2.3. Baking

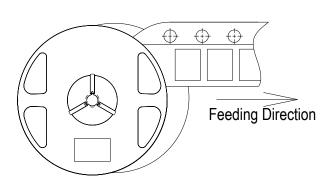
If the package has been opened for more than 1 week, it is recommended to bake the products with the following instruction:

- 1).  $60\pm3^{\circ}$ C X 6hrs and <5%RH, for reel
- 2). 125±3°C X 2hrs, for single LED

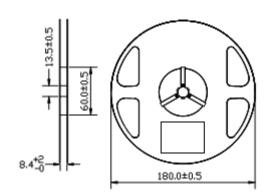
It shall be normal to see slight color fading of carrier (light yellow) after baking in process

#### Packing

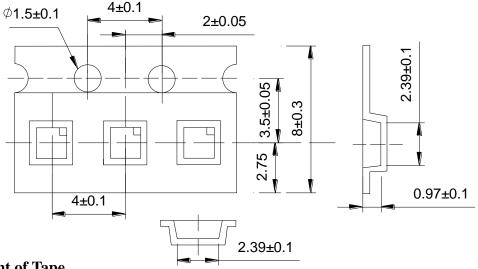
#### Feeding Direction



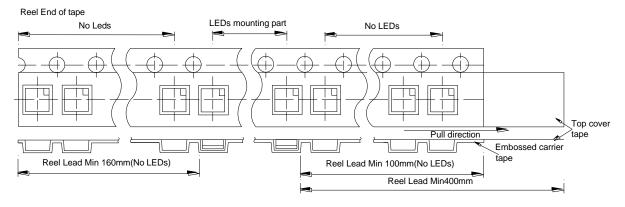
#### • Dimensions of Reel (Unit: mm)



#### • Dimensions of Tape (Unit: mm)



#### Arrangement of Tape

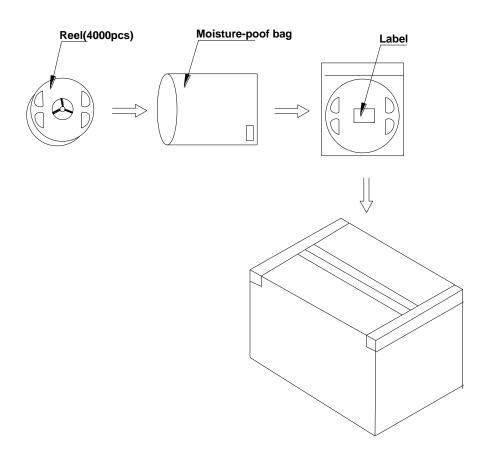


#### **Notes:**

- 1. Empty component pockets are sealed with top cover tape;
- 2. The max loss number of SMD is 2pcs;
- 3. The cathode is oriented towards the tape sprocket hole in accordance with ANSI/EIA RS-481 specifications;
- 4. 4000pcs per reel

### Packing

Packaging Specifications



**Notes:** 

Reeled product (max.4000) is packed in a sealed moisture-proof bag and into carton box.

#### Precautions

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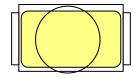
#### 1. Abnormal situation caused by improper setting of collet

To choose the right collet is the key issue in improving the product's quality. LED is different from other electronic components, which is not only about electrical output but also for optical output. This characteristic made LED more fragile in the process of SMT. If the collet's lowering down height is not well set, it will bring damage to the gold wire at the time of collet's picking up and loading which will cause the LED fail to light up, light up now and then or other quality problems

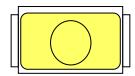
#### 2. How to choose the collet

During SMT, please choose the collet that has larger outer diameter than the lighting area of lens, in case that improper position of collet will damage the gold wire inside the LED. Different collets fit for different products, please refer to the following pictures cross out

#### Outer diameter of collet should be larger than the lighting area







Picture 2(X)

#### 3. Other points for attention

- A. No pressure should be exerted to the epoxy shell of the SMD under high temperature.
- B. Do not scratch or wipe the lens since the lens and gold wire inside are rather fragile and cross out easy to break.
- C. LED should be used as soon as possible when being taken out of the original package, and should be stored in anti-moisture and anti-ESD package.

#### 4. This usage and handling instruction is only for your reference.

### Test Items and Results of Reliability

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Test Item	Test Conditions	Duration/ Cycle	Ac/Re	Number of Damage	Reference
Normal Temperature Life	$Ta=23^{\circ}C(\pm 5^{\circ}C)$ $I_{F}=18mA$	1008 hrs	0/1	0/22	JESD22 A-108
High Temperature Life	$Ta=85^{\circ}C(\pm 5^{\circ}C)$ $I_{F}=18mA$	1008 hrs	0/1	0/22	JESD22 A-108
High Humidity Heat Life	$Ta=85^{\circ}C(\pm 5^{\circ}C)$ $RH=85\%$ $I_{F}=18mA$	1008 hrs	0/1	0/22	JESD22 A-108
Thermal shock	-45°C/30min~105°C /30min (±5°C)	1008 hrs	0/1	0/22	JESD22 A-104
Electrostatic Discharge (ESD) Test	According to the SPEC	3 cycles	0/1	0/22	AEC Q101-001
Low Temperature Storage	T <sub>a</sub> =-40°C	1008 hrs	0/1	0/22	JESD22-A103D
High Temperature Storage	T <sub>a</sub> =125°C	1008 hrs	0/1	0/22	JESD22-A103D

*Criteria for Judging						
T4	Carrala al	C 114:	Criteria for Judgment of Pass			
Item	Symbol	Condition	Min	Max		
Forward Voltage	$V_{\mathrm{F}}$	I <sub>F</sub> =18mA	-	USL*1×1.1		
Reverse Current	$I_R$	V <sub>R</sub> = 5V	-	10μΑ		
Luminous Intensity	Iv	I <sub>F</sub> =18mA	LSL*2×0.7	-		

 $[Note] \quad USL^{*1} \hbox{: Upper Specification Level}$ 

LSL\*2: Lower Specification Level