Job Description: Fresher VLSI Physical Design Engineer

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Location: [City, State, Country] (e.g., Bengaluru, Karnataka, India / Santa Clara, California, USA)

About the Role:

We are seeking a highly motivated and enthusiastic Fresher VLSI Physical Design Engineer to join our dynamic team. This entry-level position offers an excellent opportunity to kickstart your career in the exciting field of semiconductor design. You will work alongside experienced engineers, contributing to the physical implementation of cutting-edge integrated circuits (ICs) for various applications. This role is ideal for recent graduates eager to learn and grow in a fast-paced, innovative environment.

Responsibilities:

As a Fresher VLSI Physical Design Engineer, your responsibilities will include, but are not limited to:

- Assisting in various stages of the physical design flow, including floorplanning, power planning, placement, clock tree synthesis (CTS), routing, and physical verification (DRC/LVS).
- Learning and applying industry-standard EDA (Electronic Design Automation) tools for physical design.
- Working closely with senior engineers to understand design specifications and implementation challenges.
- Performing basic design rule checking (DRC) and layout versus schematic (LVS) checks.
- Contributing to timing analysis and closure efforts under guidance.
- Participating in design reviews and team meetings.
- Documenting design steps, methodologies, and results.
- Continuously learning and staying updated with new technologies and methodologies in VLSI physical design.

Qualifications:

Education:

- Bachelor's or Master's degree in Electronics and Communication Engineering (ECE), Electrical Engineering (EE), VLSI Design, or a related field.

Technical Skills (Preferred, but not mandatory for all):

- Strong fundamental understanding of VLSI design concepts, digital electronics, and semiconductor physics.
- Basic knowledge of physical design concepts (e.g., standard cells, libraries, timing, power).
- Familiarity with any EDA tools used in physical design (e.g., Cadence Innovus, Synopsys Fusion Compiler, Mentor Graphics Calibre) is a plus.
- Basic scripting skills (e.g., Tcl, Python, Perl) are advantageous.
- Understanding of static timing analysis (STA) concepts.
- Knowledge of Linux/Unix operating systems.

Soft Skills:

- Excellent problem-solving and analytical skills.
- Strong communication and interpersonal skills.
- Ability to work effectively both independently and as part of a team.
- High level of curiosity and a strong desire to learn and adapt to new technologies.
- Attention to detail and a commitment to quality.

What We Offer:

- A challenging and rewarding work environment.
- Opportunity to work on state-of-the-art semiconductor technologies.
- Mentorship from experienced industry professionals.
- Comprehensive training and development programs.
- Competitive salary and benefits package.
- A collaborative and supportive team culture.

If you are a passionate and driven individual looking to start your career in VLSI physical design, we encourage you to apply!