

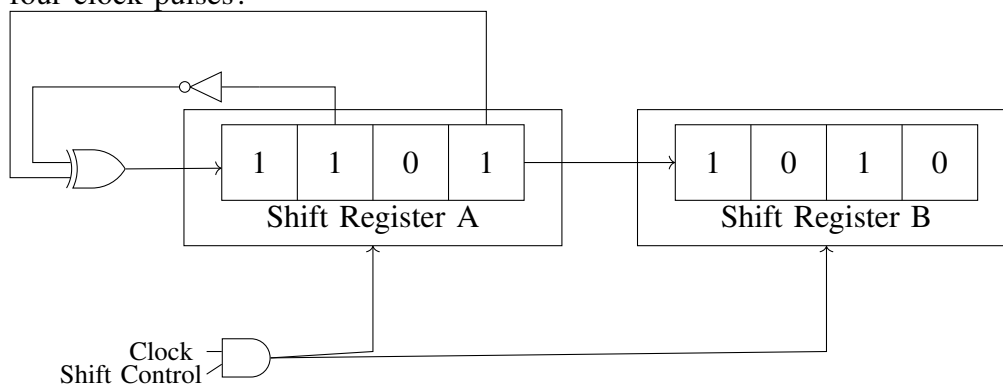
# GATE:IN-42-2023

EE23BTECH11025 - Anantha Krishnan

## I. QUESTION

In the circuit shown, the initial binary content of shift register A is 1101 and that of shift register B is 1010. The shift registers are positive-edge triggered, and the gates have no delay.

When the shift control is high, what will be the binary content of the shift registers A and B after four clock pulses?



- 1)  $A = 1101, B = 1101$
- 2)  $A = 1110, B = 1001$
- 3)  $A = 0101, B = 1101$
- 4)  $A = 1010, B = 1111$