

GATE:IN-42-2023

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I. QUESTION

Consider the 2-bit multiplexer(MUX) shown in the figure. For OUTPUT to be the XOR of C and D , the values of A_0, A_1, A_2 and A_3 are:

- 1) $A_0 = 0, A_1 = 0, A_2 = 1, A_3 = 1$
- 2) $A_0 = 1, A_1 = 0, A_2 = 1, A_3 = 0$
- 3) $A_0 = 0, A_1 = 1, A_2 = 1, A_3 = 0$
- 4) $A_0 = 1, A_1 = 1, A_2 = 0, A_3 = 0$

