Installation of CAD tools

This is a step-by-step guide to install tools and their dependencies from their respective git repositories on your system (Ubuntu 22.04). Before, that follow the steps below to download and install git on your system.

```
@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/<mark>Desktop$</mark> sudo add-apt-repository ppa:git-core/ppa
  PPA publishes dbgsym, you may need to include 'main/debug' component
Repository: 'deb https://ppa.launchpadcontent.net/git-core/ppa/ubuntu/ jammy main'
  The most current stable version of Git for Ubuntu.
 For release candidates, go to https://launchpad.net/-git-core/+archive/candidate .
More info: https://launchpad.net/-git-core/+archive/ubuntu/ppa
More info: https://launchpad.net/-git-core/+archive/ubuntu/ppa
Adding repository.
Press [ENTER] to continue or Ctrl-c to cancel.
Found existing deb entry in /etc/apt/sources.list.d/git-core-ubuntu-ppa-jammy.list
Adding deb entry to /etc/apt/sources.list.d/git-core-ubuntu-ppa-jammy.list
Found existing deb-src entry in /etc/apt/sources.list.d/git-core-ubuntu-ppa-jammy.list
Adding disabled deb-src entry to /etc/apt/sources.list.d/git-core-ubuntu-ppa-jammy.list
Adding disabled deb-src entry to /etc/apt/sources.list.d/git-core-ubuntu-ppa-jammy.list
Adding key to /etc/apt/trusted.gpg.d/git-core-ubuntu-ppa-gpg with fingerprint EIDD270288B4E6030699E45FA1715D88E1DF1F24
Hit:1 http://in.archive.ubuntu.com/ubuntu jammy InRelease
Hit:2 http://security.ubuntu.com/ubuntu jammy-security InRelease
Hit:3 http://in.archive.ubuntu.com/ubuntu jammy-backports InRelease
Hit:5 https://jan.archive.ubuntu.com/ubuntu jammy-backports InRelease
Hit:5 https://download.docker.com/linux/ubuntu jammy InRelease
                https://download.docker.com/linux/ubuntu jammy InRelease
Hit:6 https://ppa.launchpadcontent.net/git-core/ppa/ubuntu jammy InRelease
                     sad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$ sudo apt update
 Hit:1 https://download.docker.com/linux/ubuntu jammy InRelease
                   ttp://security.ubuntu.com/ubuntu jammy-security inketea
Hit:3 http://in.archive.ubuntu.com/ubuntu jammy-security Inketease
Hit:4 http://in.archive.ubuntu.com/ubuntu jammy InRelease
Hit:5 http://in.archive.ubuntu.com/ubuntu jammy-updates InRelease
Hit:5 https://ipa.launchpadcontent.net/git-core/ppa/ubuntu jammy In
Hit:6 http://in.archive.ubuntu.com/ubuntu jammy-backports InRelease
Reading package lists.. Done
Building dependency tree... Done
Reading state information... Done
                                                                                                                                                                   InRelease
  13 perkeges om be upgraded. Rum 'ept list upgradeble' te ee them.
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:-/Desktop$ sudo apt install git
Reading package lists... Done
      ituing dependency tree... Deno
wading state information... Done
it is already the newest version (1:2.43.0-0ppal-ubuntu22.04.1)
  The following packages were automatically installed and are no longer required:
bridge-utils ubuntu-fan
Jse 'Sudo apt autoremove' to remove them.
           graded, 0 newly installed, 0 to remove and 12 not upgraded.
d@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$
```

Analog design flow

List of tools that will be used in designing analog circuits: XSchem: for schematic capture, Ngpsice: for simulation, Magic: layout tool, netgen: to perform LVS. Follow the steps below to install all the tools listed above and their dependencies from git.

Xschem installation:

xschem dependencies:

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libx11-6 -y >log
[sudo] password for sysad:
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libxrender1
                                                                                                         -v >loa
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libxcb1 -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libcairo2 -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install tcl8.6 -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install tk8.6 -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD tools/xschem$ sudo apt-get install flex -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libxpm4 -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$
                                                                     sudo apt-get install
                                                                                            libx11-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libxrender-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libx11-xcb-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libcairo2-dev -y > sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install tcl8.6-dev -y >log
                                                                     sudo apt-get install libcairo2-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install tk8.6-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install bison -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libxpm-dev -y >log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ sudo apt-get install libjpeg-dev -y >log
svsad@svsad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD tools/xschem$
```

XSchem installation:

Type "xschem" in the terminal and press ENTER, to verify installation of the tool.

If you get error when you run "make" command, please run the command: sudo apt install make

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/<mark>Desktop$</mark> cd /home/sysad/CAD_tools/
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools$ git clone https://github.com/StefanSchippers/xschem.git xschem
       remote: Counting objects: 100% (7049/7049), done.
remote: Compressing objects: 100% (1534/1534), done.
remote: Total 24927 (delta 5610), reused 6945 (delta 5509), pack-reused 17878
Receiving objects: 100% (24927/24927), 46.68 MiB | 9.45 MiB/s, done.
         PSECULAR OF THE PROPERTY OF THE PROPERTY OF THE PSECULAR OF TH
                                                                                                                                                                                                                                         -DPLUGIN_GUI
                                                                                                                                                                                                                                                                                        -Isrc//default
                                                                                                                                                                                                                                                                                         -c -u nmoks.o h
-isrc//default -c src//default/f
                                                                                                                                                                                                                                          DPLUGIN_GUI
 Configuration complete, ready to compile
 sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/xschem$ make && make install
 cd src && make
make[1]: Entering directory '/home/sysad/CAD_tools/xschem/src
 gcc -c -pipe -O2 -I/usr/include/cairo -I/usr/include/glib-2.0 -I/usr/lib/x86_64-linux-gnu/glib-2.0/inclu
 include/libpng16 -I/usr/include/tcl8.6
                                                                                                                                                 -o icon.o icon.c
 gcc -c -pipe -02 -I/usr/include/cairo -I/usr/include/glib-2.0 -I/usr/lib/x86_64-linux-gnu/glib-2.0/inclu
 include/libpng16 -I/usr/include/tcl8.6 -o callback.o callback.c
gcc -c -pipe -02 -I/usr/include/cairo -I/usr/include/glib-2.0 -I/usr/lib/x86_64-linux-gnu/glib-2.0/inclu
```

Installation of NgSpice and dependencies:

Ngspice dependencies:

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$ sudo apt-get install libtool -y > ngspice_log
[sudo] password for sysad:
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$ sudo apt-get install autoconf -y > ngspice_log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$ sudo apt-get install libxaw7-dev -y > ngspice_log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$ sudo apt-get install libreadline-dev -y > ngspice_log
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/Desktop$
```

Ngspice installation:

```
Librolize: copying file "MA/Itopions.ma'
Librolize: copying file "MA/Itopions.
```

PDK installation:

Installation of magic and dependencies:

Magic dependencies:

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$ sudo apt-get install libgl-dev > magic_logs
[sudo] password for sysad:
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$ sudo apt-get install libglul-mesa-dev > magic_logs
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
```

Magic installation:

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools$ git clone https://github.com/RTimothyEdwards/magic
Cloning into 'magic'...
                   Enumerating objects
  remote: Counting objects: 100% (12430/12430), done.
remote: Compressing objects: 100% (3690/3690), done.
remote: Total 12430 (delta 8097), reused 12344 (delta 8034), pack-reused 0
  Receiving objects: 100% (12430/12430), 7.37 MiB | 4.95 MiB/s, done. Resolving dettas: 100% (8097/3097), done. sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools$ cd magic/
sysadesysad-HP-Elite-Tower-600-69-Desktop-PC:-/CAD_tools/mchecking build system type... x86 64-pc-linux-gnu checking host system type... x86 64-pc-linux-gnu checking target system type... x86-64-pc-linux-gnu checking for gcc... gcc checking whether the C compiler works... yes checking for C compiler default output file name... a.out checking for suffix of executables... checking whether we are cross compiling... no checking for suffix of object files... o checking whether we are using the GNU C compiler... yes checking whether gcc accepts -g... yes checking for gcc option to accept ISO C89... none needed checking for gcc option to accept ISO C89... none needed checking for grund the C preprocessor... gcc -E checking for g++... g++ checking whether we are using the GNU C++ compiler... yes checking whether we are using the GNU C++ compiler... yes checking for library containing strerror... none required
     ysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$ ./configure
   Configuration Summary (principle requirements):
   X11:
                                    ves
   Pvthon3:
                                    yes
   OpenGL:
   Vector fonts: yes
   Cairo:
                                    ves
   Tcl/Tk:
                                    yes
  "Use 'make' to compile and 'make install' to install.
   sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$ make @@ make install
   make: *** No rule to make target '@@'.
   sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$ magic
    sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:
   sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/CAD_tools/magic$
```

Start designing: To create a new directory and start XSchem(or magic), you can follow these steps using the command line.

Before starting xschem, copy xschemrc file.

```
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-$ mkdir designs
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-$ cd designs/
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs$ mkdir schematic
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs$ cd schematic/
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs$/schematic$ cp /usr/local/share/pdk/skyl30A/libs.tech/xschem/xschemrc .
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$ xschem
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$ xschem
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$ xschem
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$ xschem
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$ xschem
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$
SKWATER STDCELLS: /usr/local/share/pdk/skyl30A/libs.ref/skyl30_fd_sc_hd/spice
setup tcp bespice: success : listening to TCP port: 2022
[schem [-/designs/schematic]
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$
usysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:-/designs/schematic$
```

Digital design flow:

iVerilog is used to verify the functionality of our design and OpenLane to generate GDS. iVerilog installation:

```
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:~$ cd CAD_tools/
sysad@sysad-HP-Elite-Tower-600-69-Desktop-PC:~/CAD_tools$ git clone https://github.com/steveicarus/iverilog.git
Cloning into 'iverilog'

remote: Enumerating objects: 100% (9052/9052), done.
remote: Counting objects: 100% (9052/9052), done.
remote: Counting objects: 100% (934/934), done.
remote: Total 71648 (delta 8206), reused 8426 (delta 8108), pack-reused 62596
Receiving objects: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB | 3.55 MIB/s, done.
Resolving doletas: 100% (71648/71648), 29.54 MIB/s, done.
Resolving doletas: 100% (71648/71648), 20.54 MIB/s, done.
Resolving doletas: 100% (71648/71648), 20.54 MI
```

Docker installation: First step, install and run docker. Also, verify using "Hello world" example as shown.

OpenLane installation: Now, download and install OpenLane using git repository.

```
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~$ git clone --depth 1 https://github.com/The-OpenROAD-Project/OpenLane.git Cloning into 'OpenLane'...
remote: Enumerating objects: 508, done.
remote: Counting objects: 100% (598/508), done.
remote: Compressing objects: 100% (430/430), done.
remote: Total 508 (delta 82), reused 295 (delta 33), pack-reused 0
Receiving objects: 100% (82/82), done.
Resolving deltas: 100% (82/82), done.
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~$ cd OpenLane/

sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~\OpenLane$ sudo make
make[1]: Entering directory '/home/sysad/CAD_tools/OpenLane'
9dbd8b5ea2bd891bed4dcc97df5c7439083f9368: Pulling from efabless/openlane
Digest: sha256:dd3a76f5bf9752b228dba569368d70d7c32c79da83aaf4e2a87lc88fc9be2481
Status: Image is up to date for efabless/openlane:9dbd8b5ea2bd891bed4dcc97df5c7439083f0368
docker.io/efabless/openlane:9dbd8b5ea2bd891bed4dcc97df5c7439083f0368
make[1]: Leaving directory '/home/sysad/CAD_tools/OpenLane'
PYTHONPATH= ./venv/bin/python3 -m pip install --upgrade --no-cache-dir 'volare>=0.12.3'
Collecting volare>=0.12.3

Downloading volare-0.16.0-py3-none-any.whl.metadata (7.7 kB)
Requirement already satisfied: click<9,>=8.0.0 in ./venv/lib/python3.10/site-packages (from volare>=0.12.3) (8.1.7)
```

Verify OpenLane installation by running "sudo make test". "Basic test passed" verifies right installation of the tool.

```
- 61.6/61.6 kB 147.8 MB/s eta 0:00:00
Downloading certifi-2024.2.2-py3-none-any.whl (163 kB)
                                                  - 163.8/163.8 kB 197.1 MB/s eta 0:00:00
Downloading exceptiongroup-1.2.0-py3-none-any.whl (16 kB)
Downloading typing_extensions-4.9.0-py3-none-any.whl (32 kB)
Installing collected packages: pcpp, zstandard, typing-extensions, sniffio, pygments, mdurl, idna, h11, exceptiongroup, certifi, markdown-it-Successfully installed anyio-4.2.0 certifi-2024.2.2 exceptiongroup-1.2.0 h11-0.14.0 httpcore-1.0.2 httpx-0.26.0 idna-3.6 markdown-it-py-3.0.6
h-13.7.0 sniffio-1.3.0 typing-extensions-4.9.0 volare-0.16.0 zstandard-0.22.0
./venv/bin/volare enable --pdk skyl30
Version cd1748bb197f9b7af62a54507de6624e30363943 enabled for the skyl30 PDK.
sysad@sysad-HP-Elite-Tower-600-G9-Desktop-PC:~/OpenLane$ sudo make test
Available under the Apache License, version 2.0. See the LICENSE file for more details.
[INFO]: Using configuration in 'designs/spm/config.json'...
[INFO]: PDK Root: /root/.volare
 [INFO]: Process Design Kit: sky130A
[INFO]: Standard Cell Library: skyl30_fd_sc_hd
[INFO]: Optimization Standard Cell Library: skyl30_fd_sc_hd
 [INFO]: Run Directory: /openlane/designs/spm/runs/openlane_test
[INFO]: Saving runtime environment...
[INFO]: Preparing LEF files for the nom corner...
[INFO]: Preparing LEF files for the min corner...
[INFO]: Preparing LEF files for the max corner...
.
INFO]: Running linter (Verilator) (log: designs/spm/runs/openlane_test/logs/synthesis/linter.log)...
[INFO]: 0 errors found by linter
 [INFO]: 0 warnings found by linter
 STEP 1]
 INFO]: Running Synthesis (log: designs/spm/runs/openlane_test/logs/synthesis/l-synthesis.log)...
 STEP 2]
[INFO]: Running Single-Corner Static Timing Analysis (log: designs/spm/runs/openlane_test/logs/synthesis/2-sta.log)...
 STEP 31
[INFO]: Running Initial Floorplanning (log: designs/spm/runs/openlane_test/logs/floorplan/3-initial_fp.log)...
```

Designing using iVerilog and openlane:

Verify design using iVerilog commands as given below:

iverilog -o counter counter_tb.v counter.v

vvp counter

Complied results are stored in "counter" file, which can used to verify the results. This is one of the ways of compiling.

OpenLane: Commands to add new design and run the flow script as shown below: