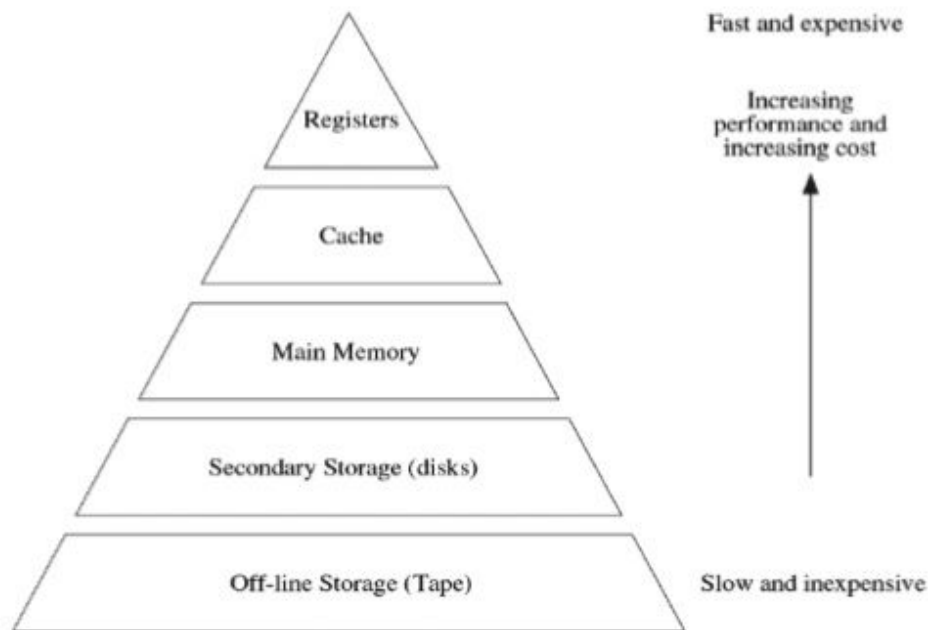


Assignment 5

EE15B025 | Ganga Meghanath

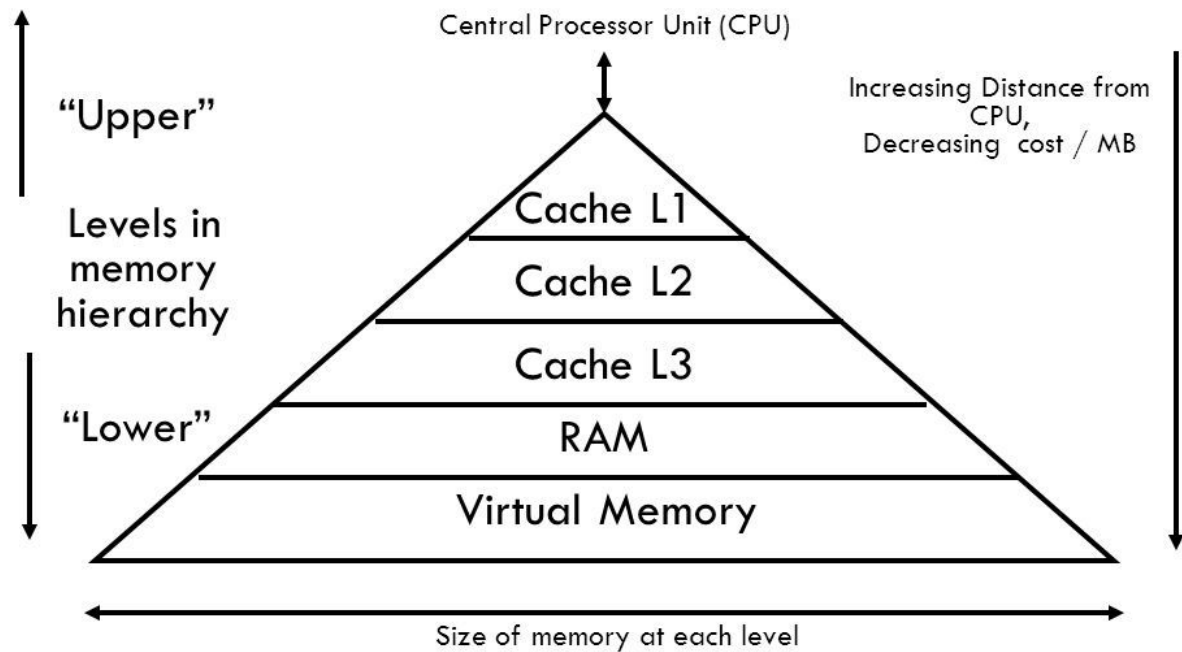
The Memory Hierarchy



Question

Plot the L1 cache miss rate against L1 block size(16,32,64,128 bytes) fixing L1 cache size as 32KB and associativity as 4 for the specified benchmark : fluidanimate (-n 2)

Memory Conclusion (2)



Answer

Block Size (kB)	Miss Rate in Core0 (%)	Miss Rate in Core1 (%)
16	4.81	0.39
32	4.76	0.39
64	4.74	0.39
128	4.81	0.39

Plots

