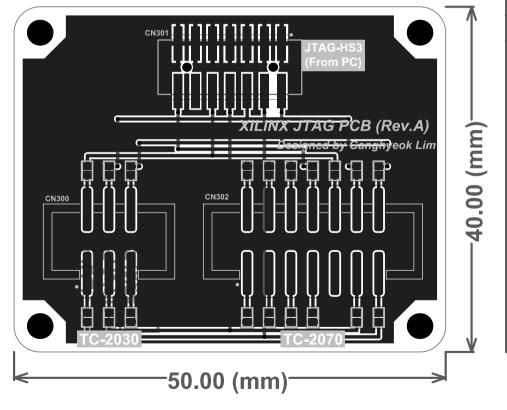


3 XILINX JTAG PCB (Rev.A) [3] Connector Target Device Part (TC-2030/2070) PC Part (JTAG-HS3) Cable: TC-2030 (Tag-Connect) Cable: JTAG-HS3 (Digilent) JTAG_TMS SB300 1 JTAG_TDI DGND P3.3VS JTAG_TDO JTAG_TCK SB302 3 4 SB303 JTAG TMS DGND DGND SB304 6 SB305 P3.3VS DGND JTAG_TCK JTAG_TDO DGND DGND JTAG_TDI DGND DGND 13 PS_NRST 0878321420 Cable: TC-2070 (Tag-Connect) DGND 2 SB307 DGND SB308 3 DGND SB310 5 DGND SB312 7 PS NRST 8 SB313 10 SB314 JTAG_TDI SB315 11 JTAG TCK SB317 13 JTAG_TMS 14 SB318 P3.3VS 3020-14-0300-00 **Mount Hole** XILINX JTAG PCB.PrjPcb * H300 H301 H302 NTH-3MM NTH-3MM NTH-3MM NTH-3MM 03_Connector.SchDoc Sheet #3 of 3 Author * Date 2024-05-19

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# Layer Information		
L1	Top (Signal / GND)	
L2	Bottom (Signal /GND)	



# PCB Specification (JLCPCB)			
No	Category	Selection	
1	Base Material	FR-4	
2	Layer #	2 - Layer	
3	Dimension	50.00(mm) * 40.00(mm)	
4	Thickness	1.6T	
5	Color	Black	
6	Material Type	Default	
7	Surface Finish	HASL(with Lead)	
8	Gold Thickness	No	
9	Outer Copper Weight	1 oz	
10	Inner Copper Weight	0.5 oz	
11	Impedance Control	No	
12	Layer Stack-up	Default Stack-up	
13	Via Covering	Tented	
14	Min. Via Hole Size / Diameter	Hole: 0.3mm / Diameter: 0.45mm	
15	Board Outline Tolerance	±0.2mm (Regular)	
16	Confirm Production File	No	
17	Remove Order Number	Yes	
18	Flying Probe Test	Fully Test	
19	Gold Fingers	No	
20	30° Finger Chamfered	No	
21	Castellated Holes	No	
22	Edge Plating	No	