

A

B

C

D

A

B

C

D

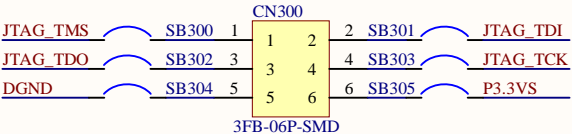
[2] Overview

Title		XILINX JTAG PCB.PrjPcb		Rev	*
Doc		02_Overview.SchDoc			
Sheet # 2 of 3		Author *			
Date		2024-05-19			
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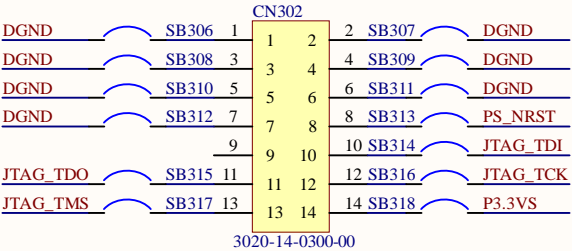
[3] Connector

Target Device Part (TC-2030/2070)

Cable : TC-2030 (Tag-Connect)

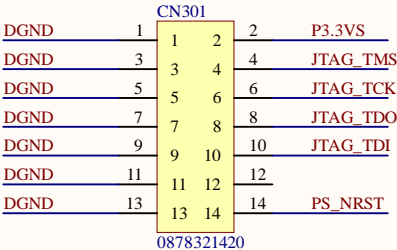


Cable : TC-2070 (Tag-Connect)

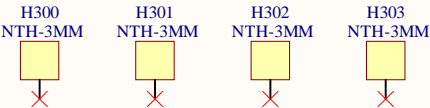


PC Part (JTAG-HS3)

Cable : JTAG-HS3 (Digilent)

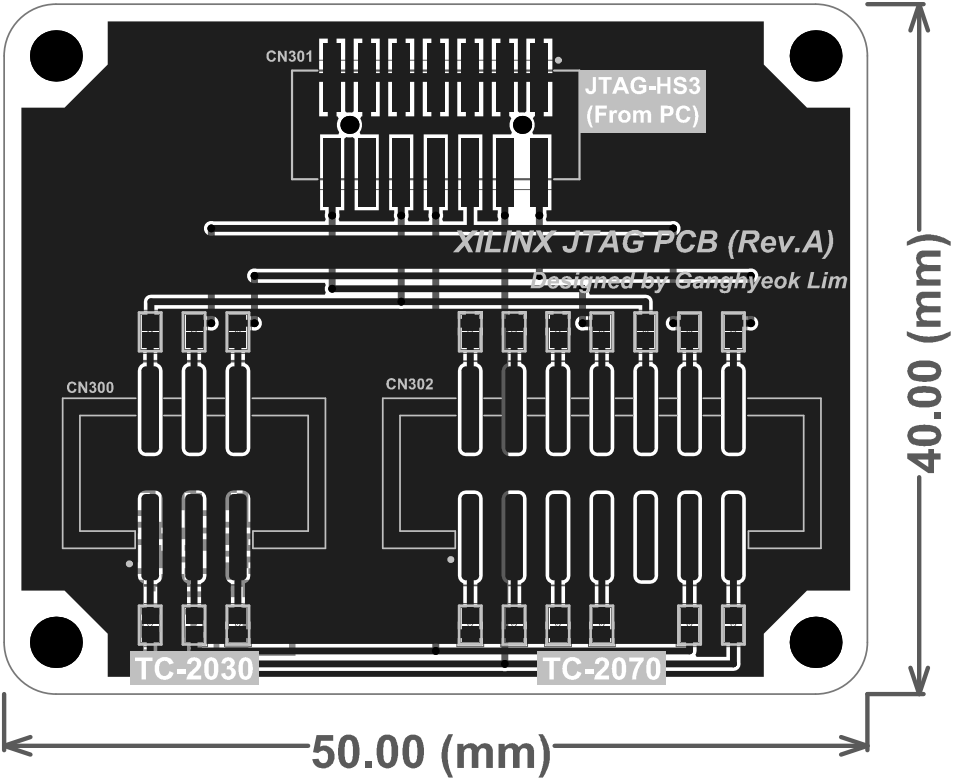


Mount Hole



Title		XILINX JTAG PCB.PrjPcb		Rev	*
Doc		03_Connector.SchDoc			
Sheet # 3 of 3		Author *			
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# Layer Information	
L1	Top (Signal / GND)
L2	Bottom (Signal /GND)



# PCB Specification (JLCPCB)		
No	Category	Selection
1	Base Material	FR-4
2	Layer #	2 - Layer
3	Dimension	50.00(mm) * 40.00(mm)
4	Thickness	1.6T
5	Color	Black
6	Material Type	Default
7	Surface Finish	HASL(with Lead)
8	Gold Thickness	No
9	Outer Copper Weight	1 oz
10	Inner Copper Weight	0.5 oz
11	Impedance Control	No
12	Layer Stack-up	Default Stack-up
13	Via Covering	Tented
14	Min. Via Hole Size / Diameter	Hole : 0.3mm / Diameter : 0.45mm
15	Board Outline Tolerance	±0.2mm (Regular)
16	Confirm Production File	No
17	Remove Order Number	Yes
18	Flying Probe Test	Fully Test
19	Gold Fingers	No
20	30° Finger Chamfered	No
21	Castellated Holes	No
22	Edge Plating	No