EEG Measurement B'd (Rev.A)

[1] Index

Rev	Date	Designer	Description
A	24.04.10	Ganghyeok Lim	Create design project

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[2] Overview

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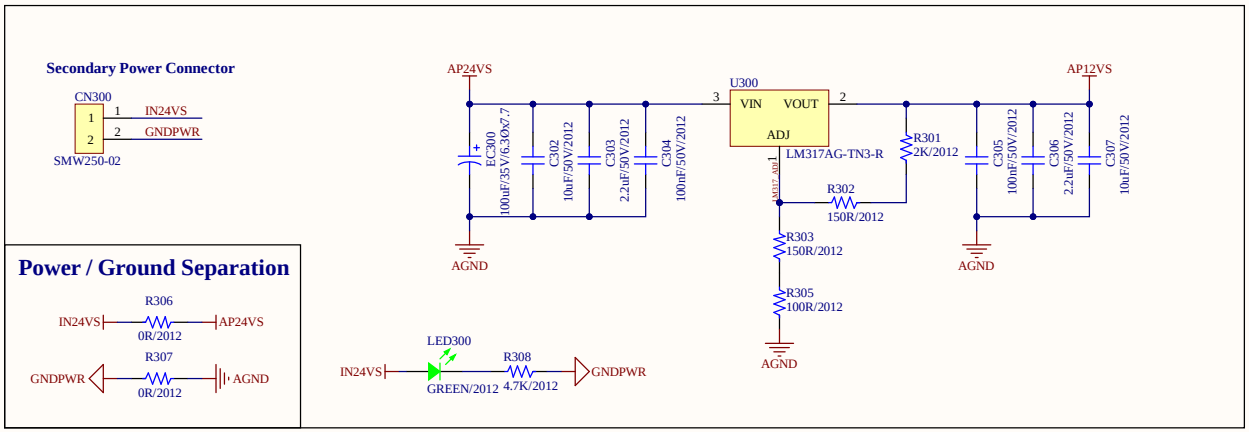
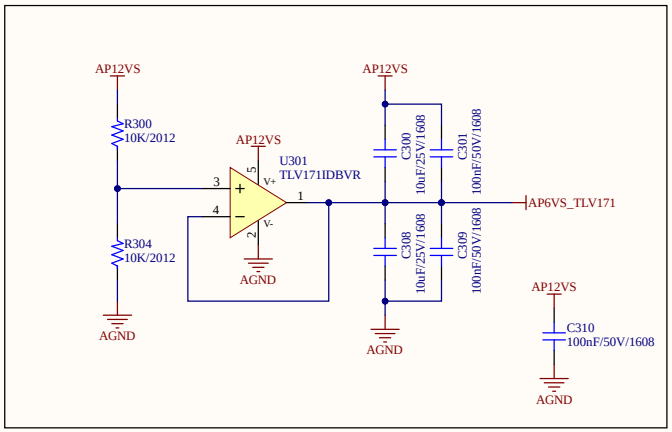
[3] Power

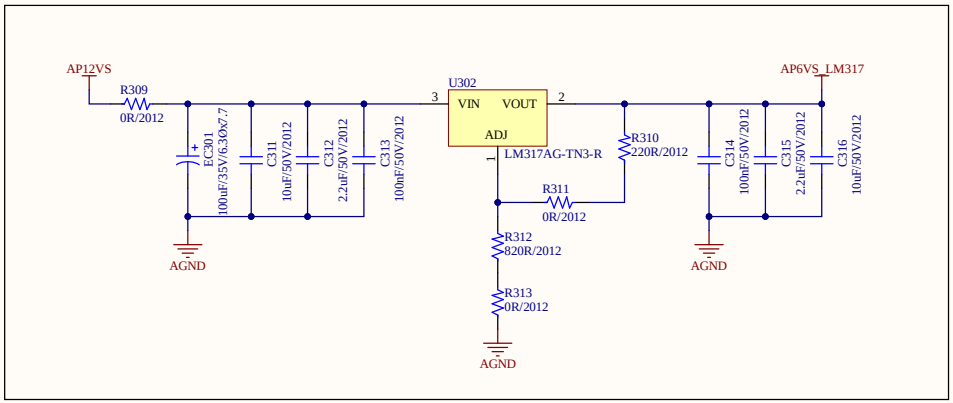
Power Input / AP12VS : 12V Linear Regulator



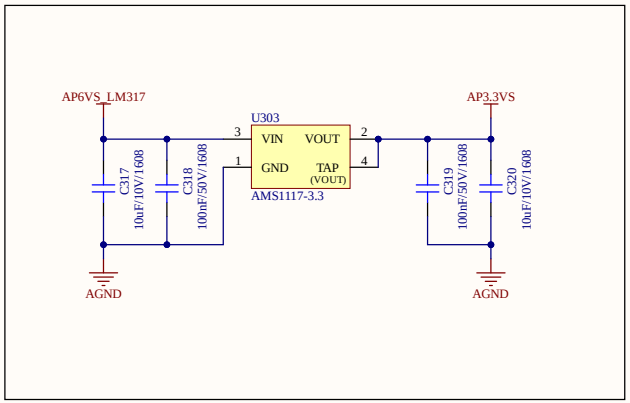
AP6VS_TLV171 : 6V Voltage Reference



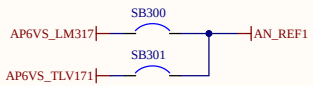
AP6VS_LM317 : 6V Linear Regulator



AP3.3VS : 3.3V LDO (3.3V / 1A)



Jumper Connection



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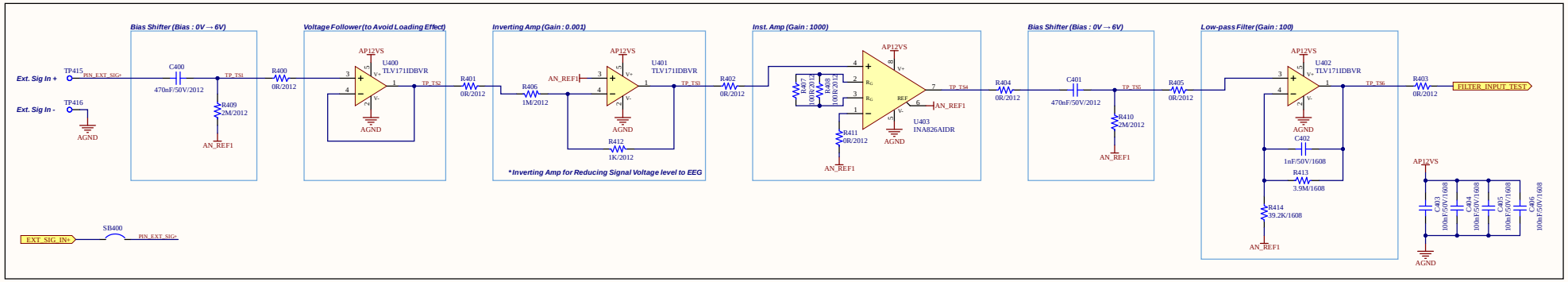
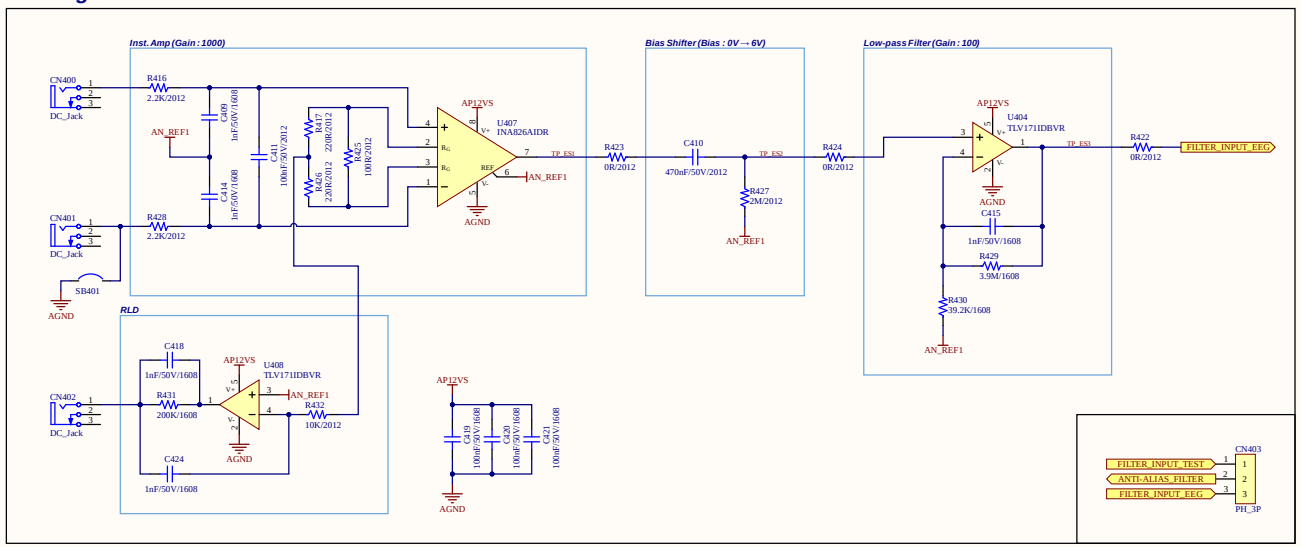
[4] Analog Part (Test & EEG Signal Amplifying / Filtering)

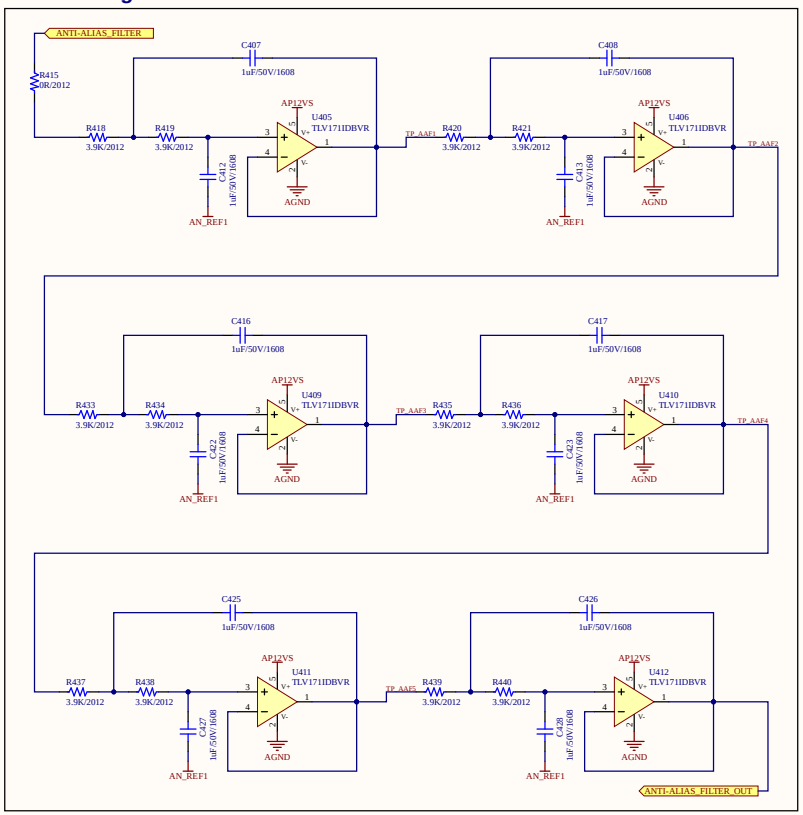
Test Signal



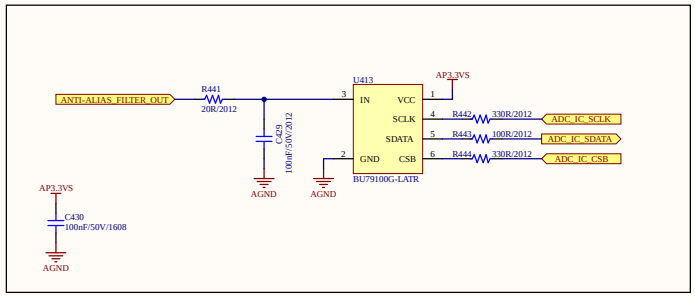
EEG Signal



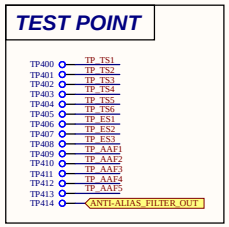
Anti-Aliasing Filter

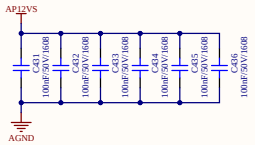


A/D Converter



TEST POINT





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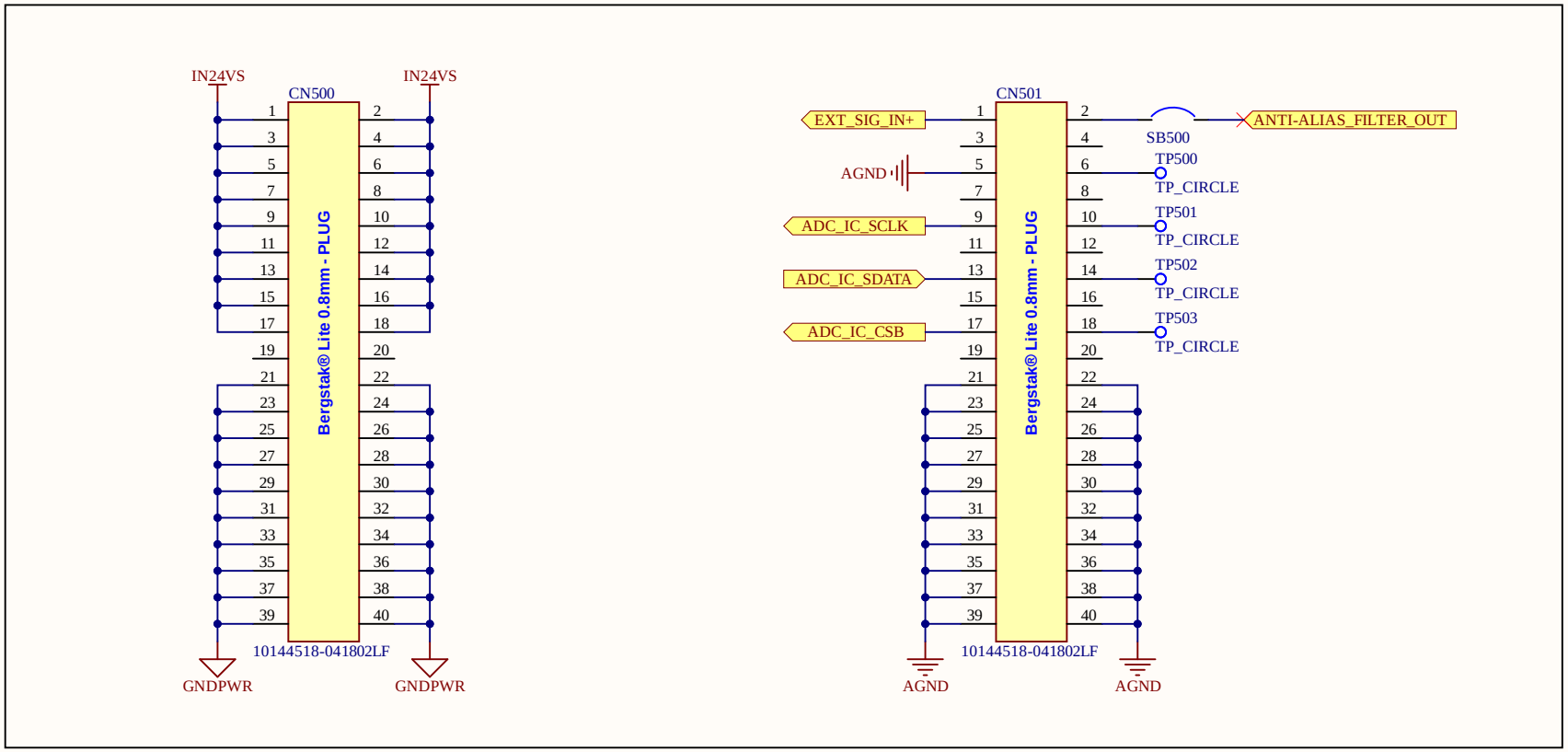
[5] Connector



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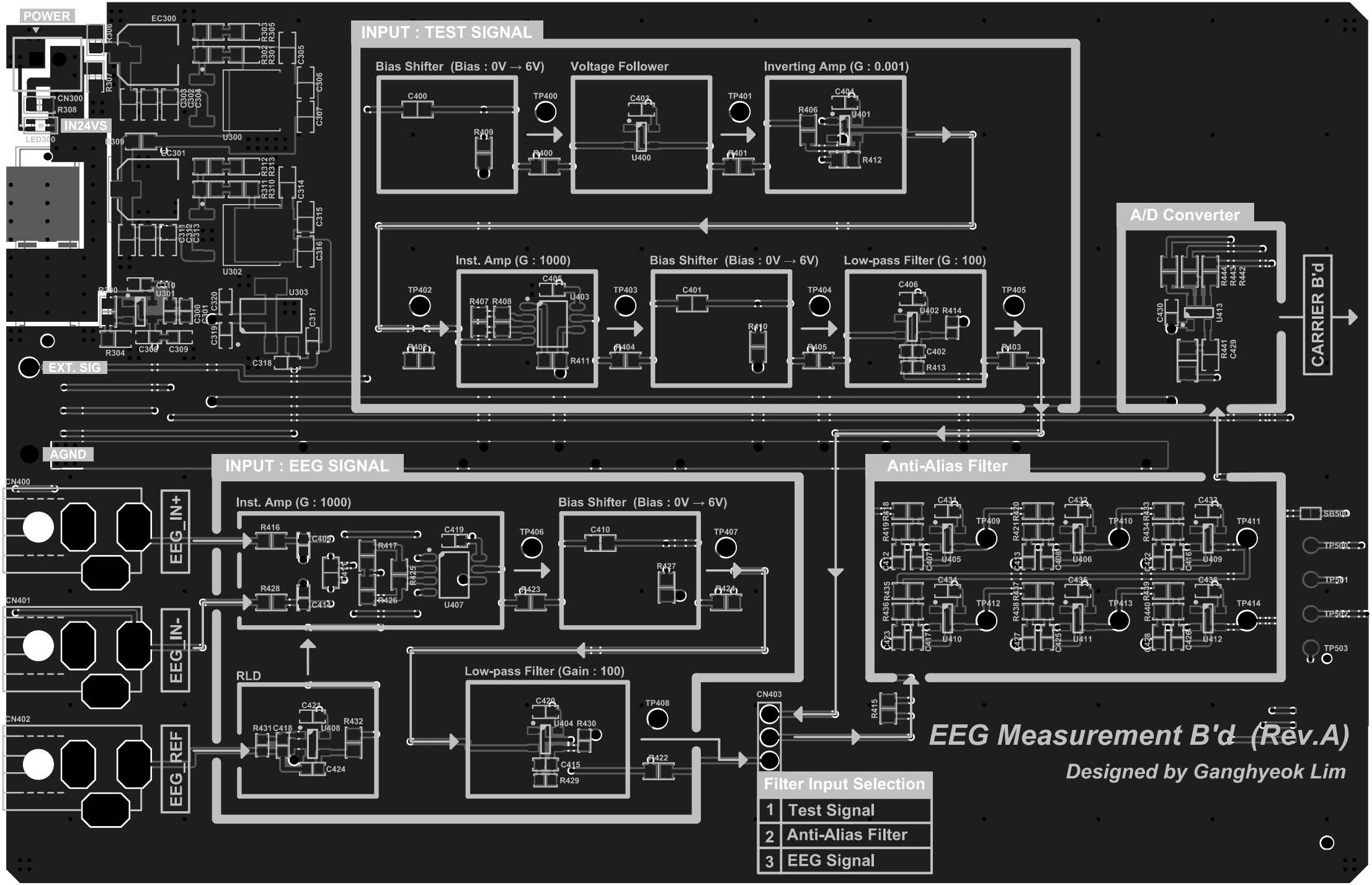
POWER
IN24VS
EXT. SIG
AGND
INPUT : TEST SIGNAL
Bias Shifter (Bias : 0V → 6V)
Voltage Follower
Inverting Amp (G : 0.001)
Inst. Amp (G : 1000)
Bias Shifter (Bias : 0V → 6V)
Low-pass Filter (G : 100)
A/D Converter
CARRIER B'd
INPUT : EEG SIGNAL
Inst. Amp (G : 1000)
Bias Shifter (Bias : 0V → 6V)
RLD
Low-pass Filter (Gain : 100)
Anti-Alias Filter
EEG_IN+
EEG_IN-
EEG_REF
CN400
CN401
CN402
CN403
Filter Input Selection
1 Test Signal
2 Anti-Alias Filter
3 EEG Signal
EEG Measurement B'd (Rev.A)
Designed by Ganghyeok Lim