

STM32H743XIH6 SOM B'd (Rev.A)

[1] Index

Rev	Date	Designer	Description
A	24.04.18	Ganghyeok Lim	Create design project

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Date				2024-04-17			
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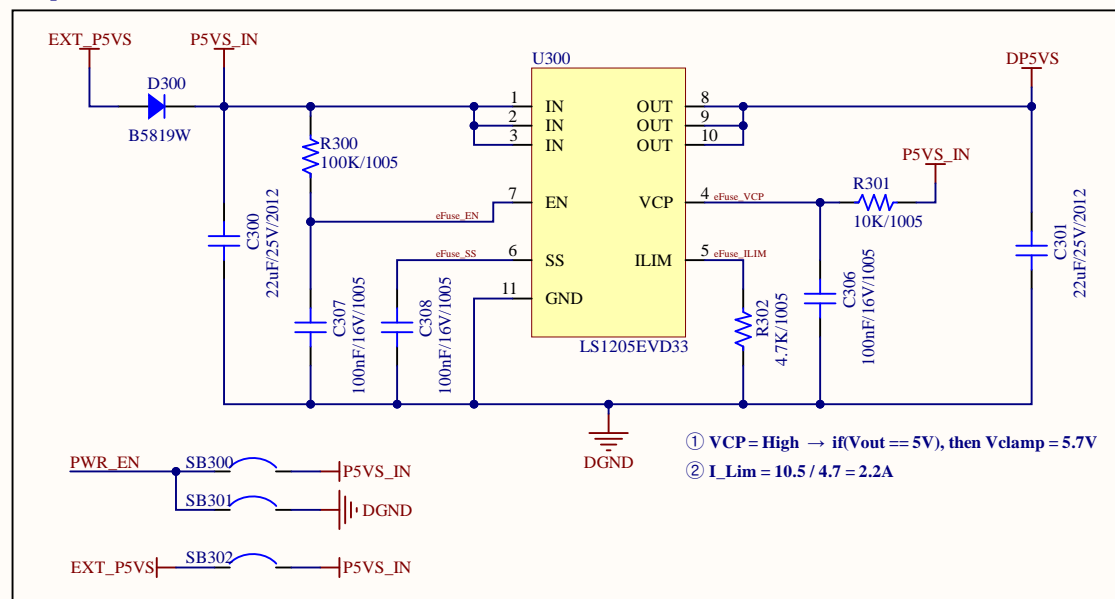
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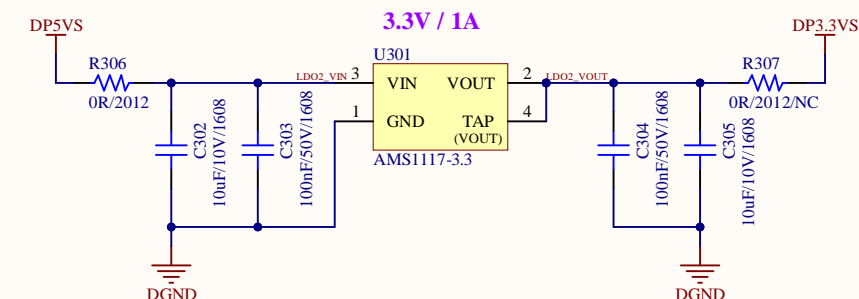
[3] Power

Input Power & eFuse

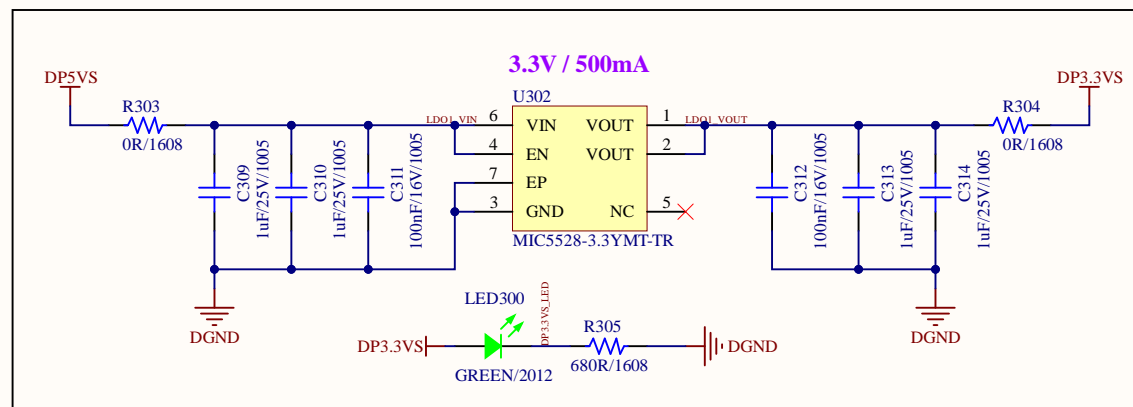


P3.3VS (Reserved)

Reserved Part



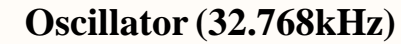
P3.3VS



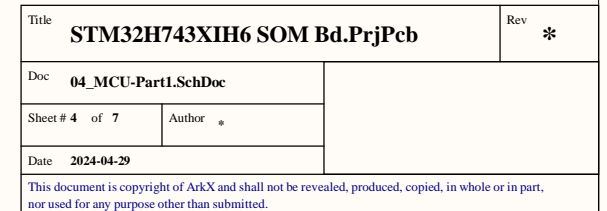
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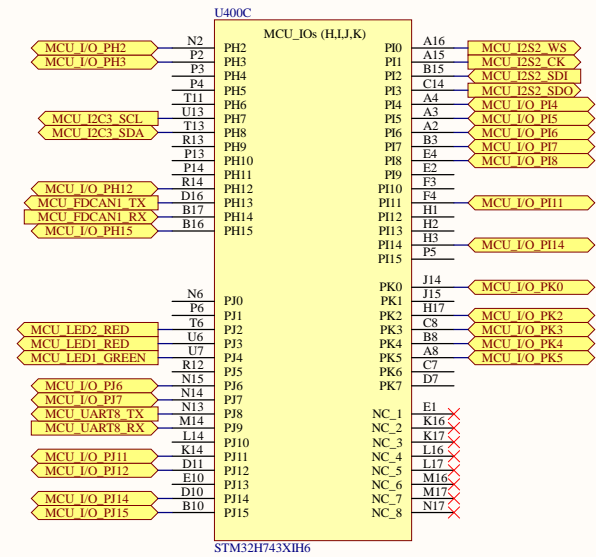
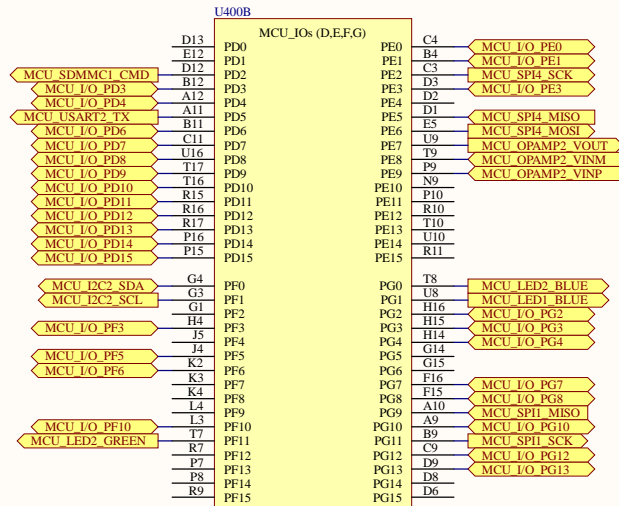
Oscillator (12MHz)



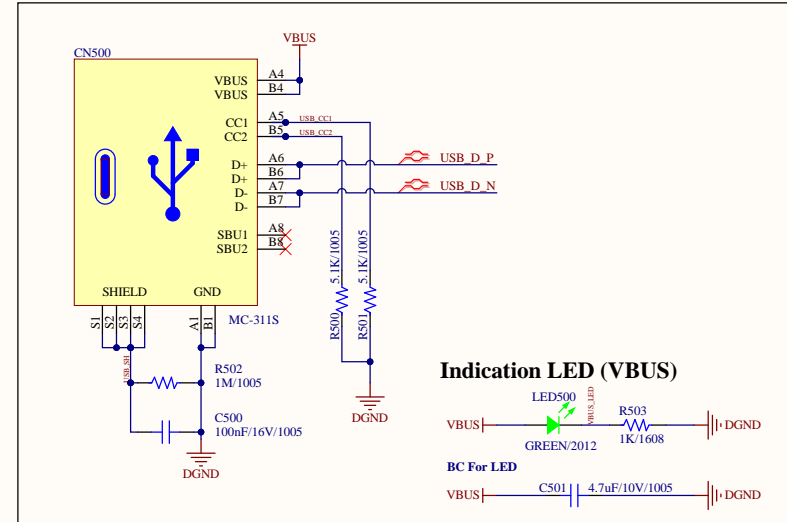
MCU RESET



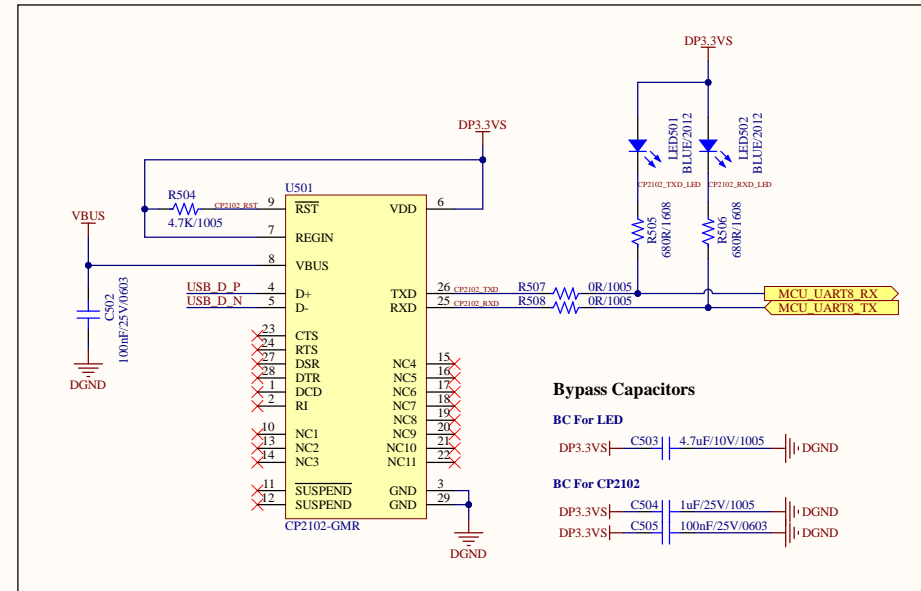
[5] MCU - Part2



USB Connector (Type-C)

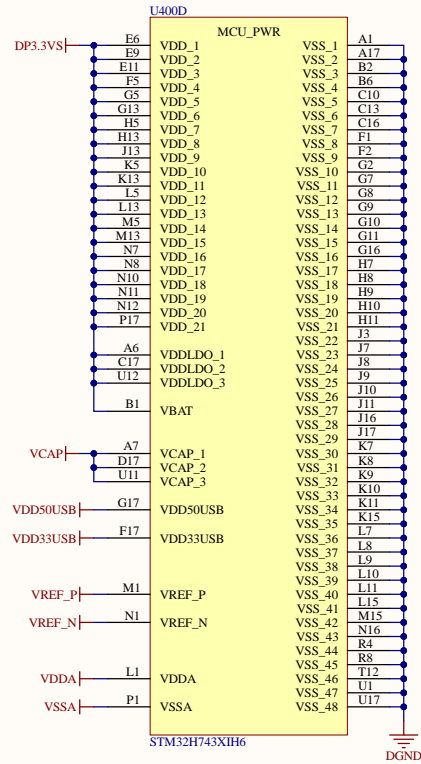


USB - UART Bridge

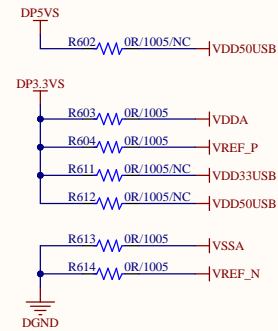


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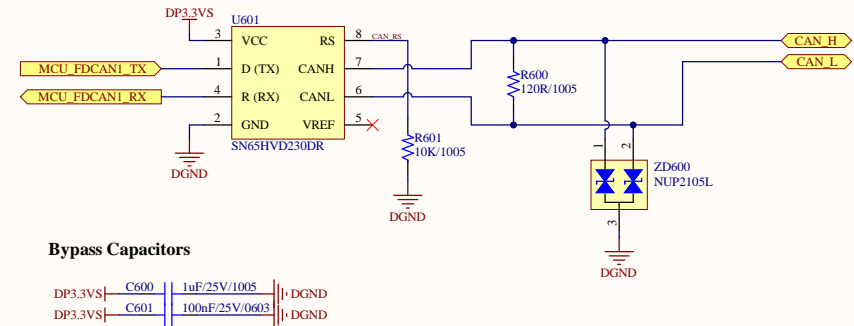
[6] MCU - Part3



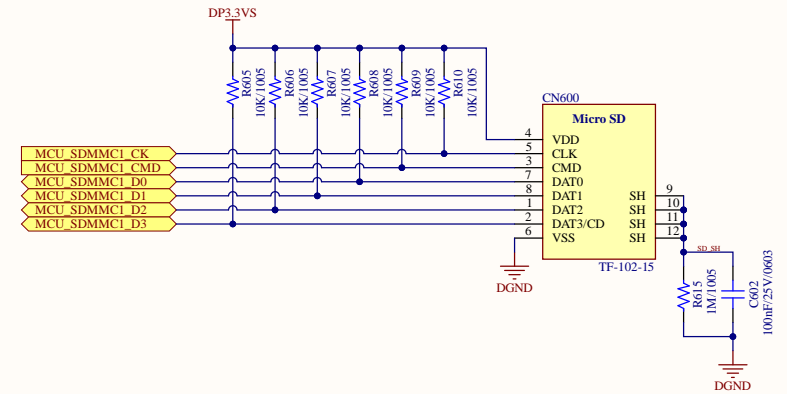
Pull-Up/Down Resistors



CAN Transceiver

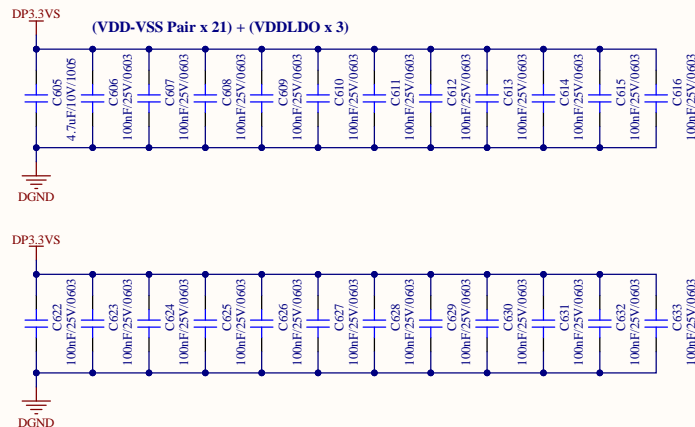


Micro-SD Card



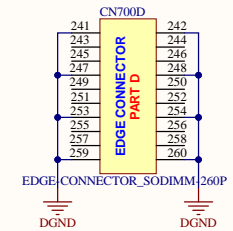
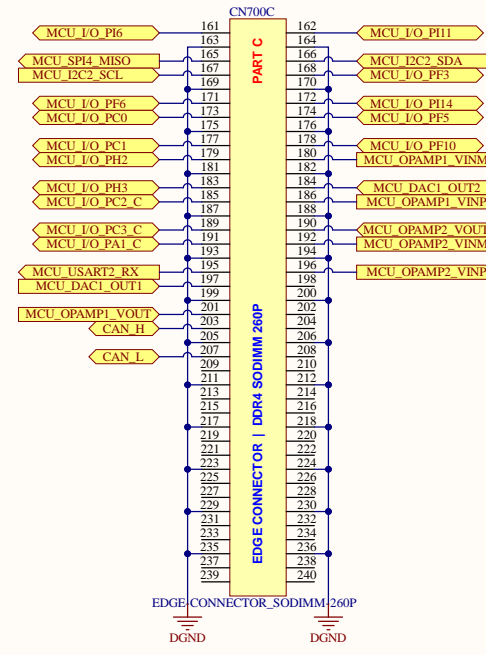
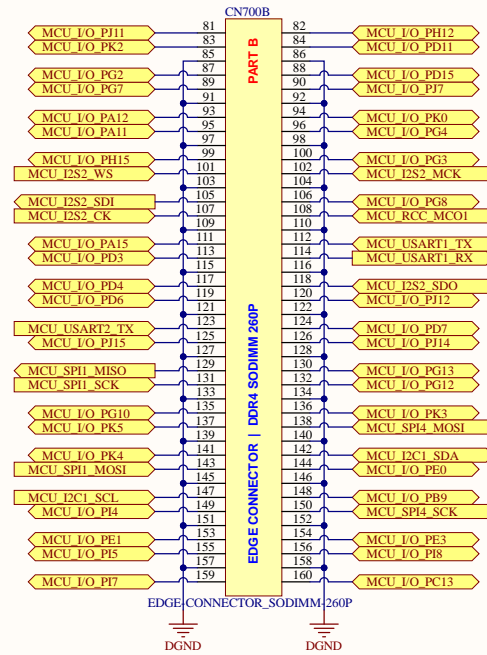
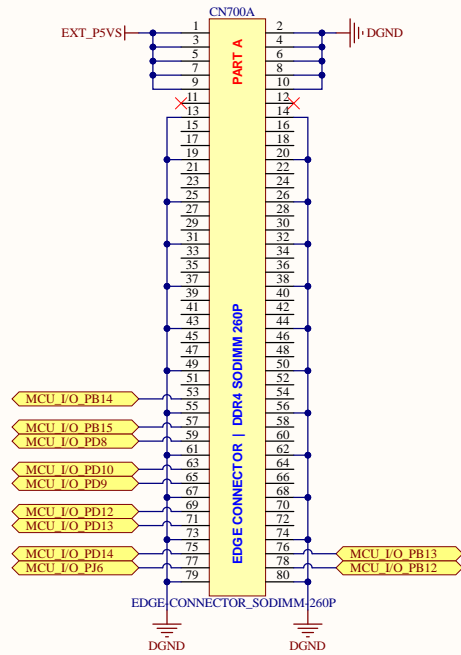
Decoupling Capacitors

DeCap for "VDD - VSS Pair"

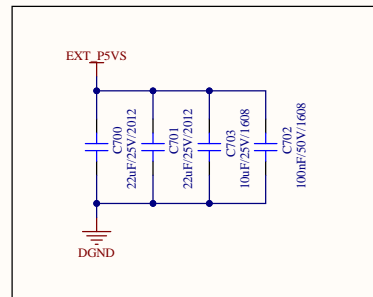


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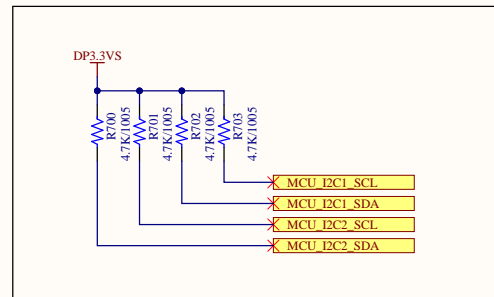
[7] Connector



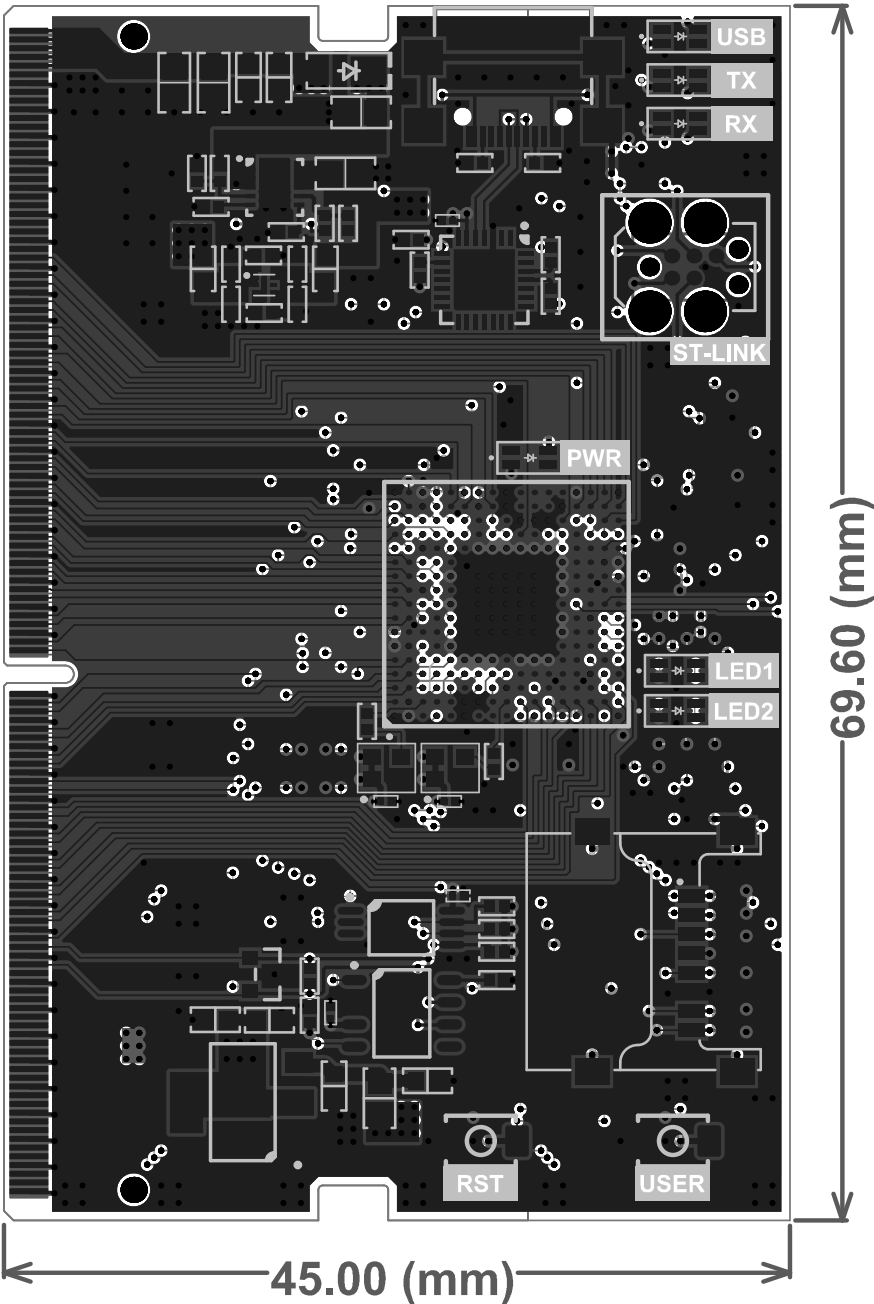
Decoupling Capacitors



Pull-up Resistors for Ext. I2C Peri



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# Layer Information	
L1	Top (Signal / GND)
L2	GND
L3	Power (5V / 3.3V)
L4	Signal / GND
L5	GND
L6	Signal / GND
L7	GND
L8	Bottom (Signal / GND)

# PCB Specification (JLCPCB)		
No	Category	Selection
1	Base Material	FR-4
2	Layer #	8 - Layer
3	Dimension	45.00(mm) * 69.60(mm)
4	Thickness	1.2T
5	Color	Black
6	Material Type	FR-4 TG155
7	Surface Finish	ENIG
8	Gold Thickness	2 U"
9	Outer Copper Weight	1 oz
10	Inner Copper Weight	0.5 oz
11	Impedance Control	No
12	Layer Stack-up	JLC08121H-3313 (Default Stack-up)
13	Via Covering	Epoxy Filled & Capped
14	Min. Via Hole Size / Diameter	Hole : 0.2mm / Diameter : 0.35mm
15	Board Outline Tolerance	±0.2mm (Regular)
16	Confirm Production File	No
17	Remove Order Number	Yes
18	Flying Probe Test	Fully Test
19	Gold Fingers	Yes
20	30° Finger Chamfered	No
21	Castellated Holes	No
22	Edge Plating	No