

TEST-001 B'd (Rev.A)

[1] Index

Rev	Date	Designer	Description
A	24.03.08	Ganghyeok Lim	Create design project

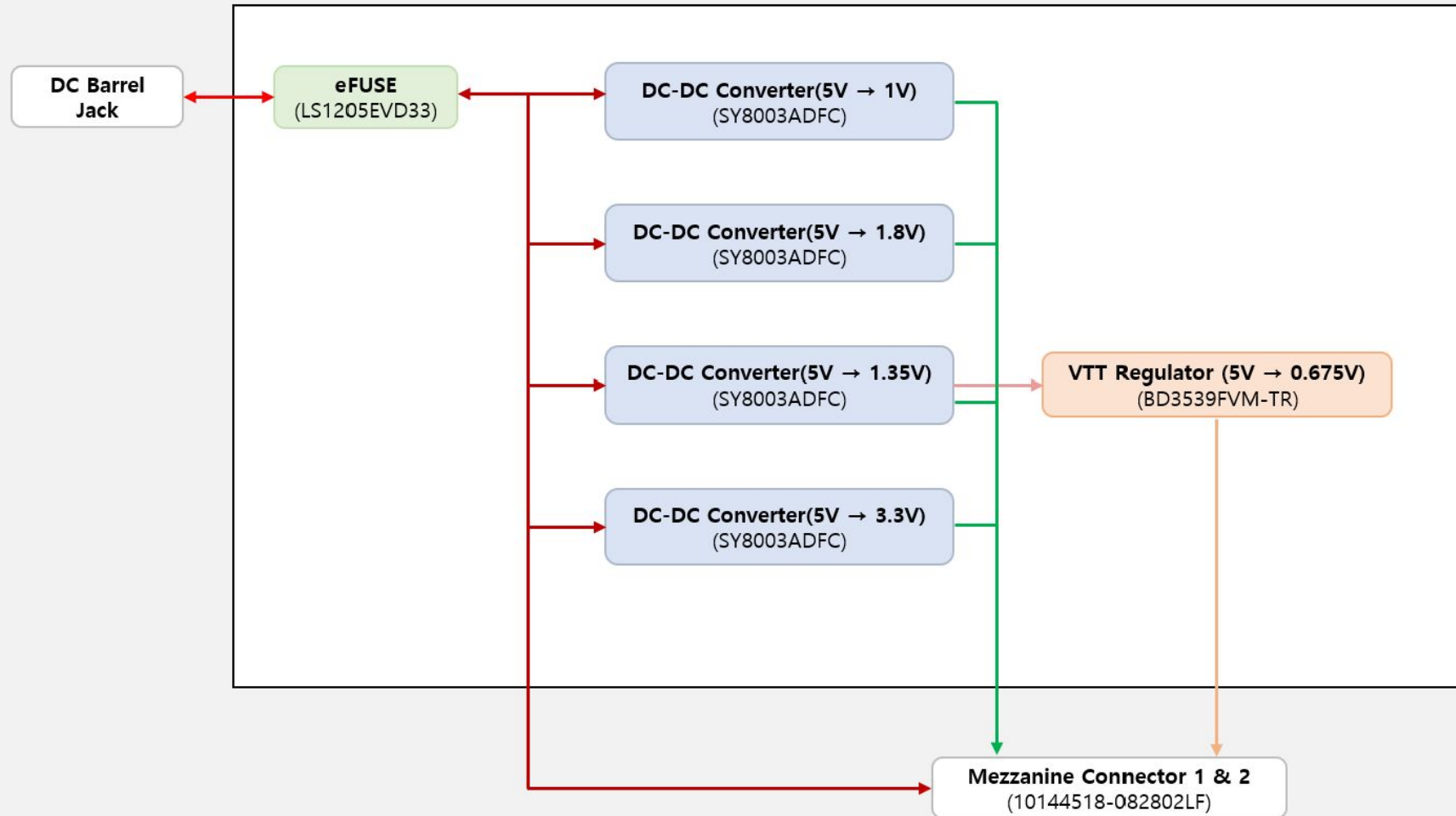
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Title Test_001_Bd.PrjPcb		Rev *
Doc 01_Index.SchDoc		
Sheet # 1 of 4	Author *	
Date 2024-03-11		
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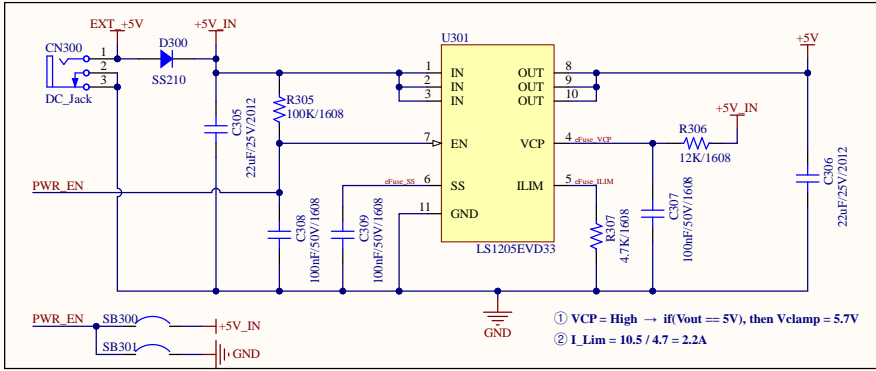
[2] Overview

TEST001 B'd (Rev.A)

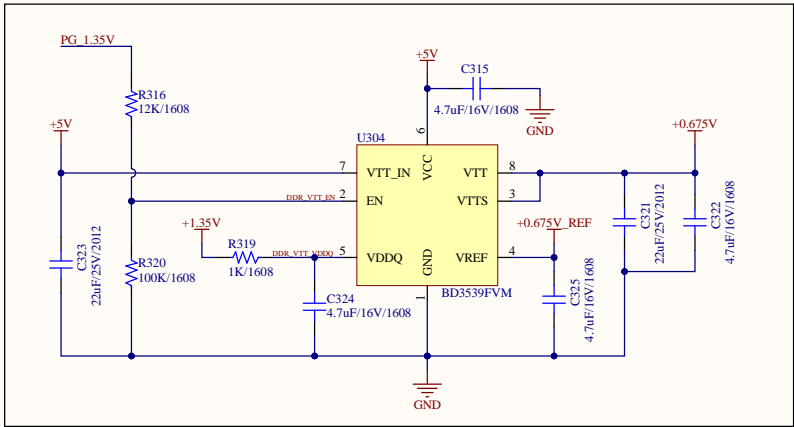


[3] DUT ICs

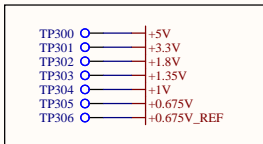
Input Power & eFuse



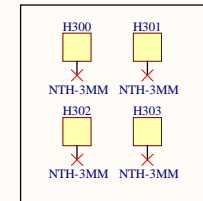
DDR3L VTT Regulator



Test Point

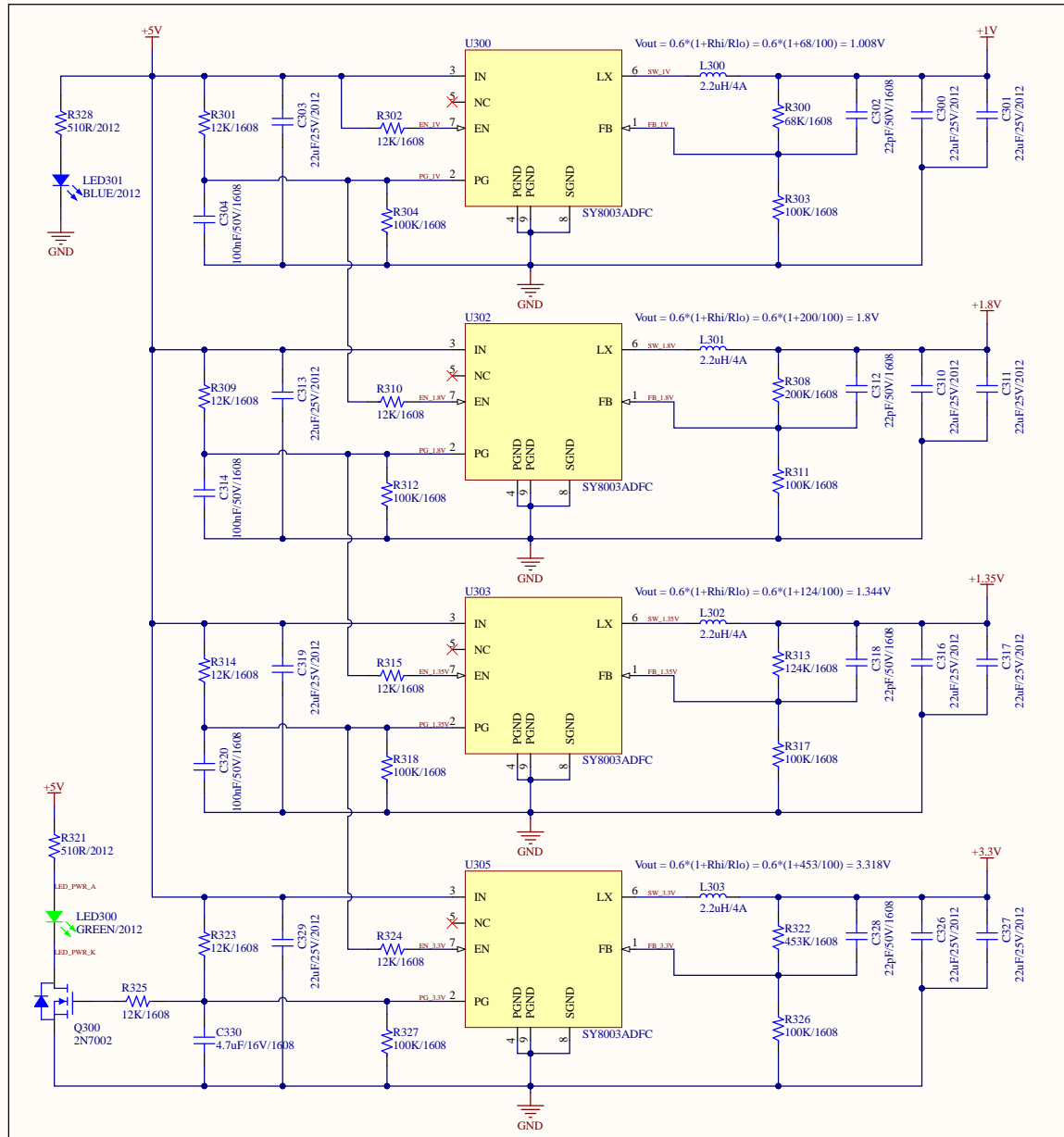


PCB Mount Hole



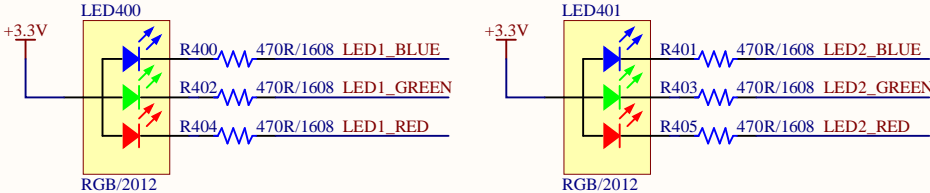
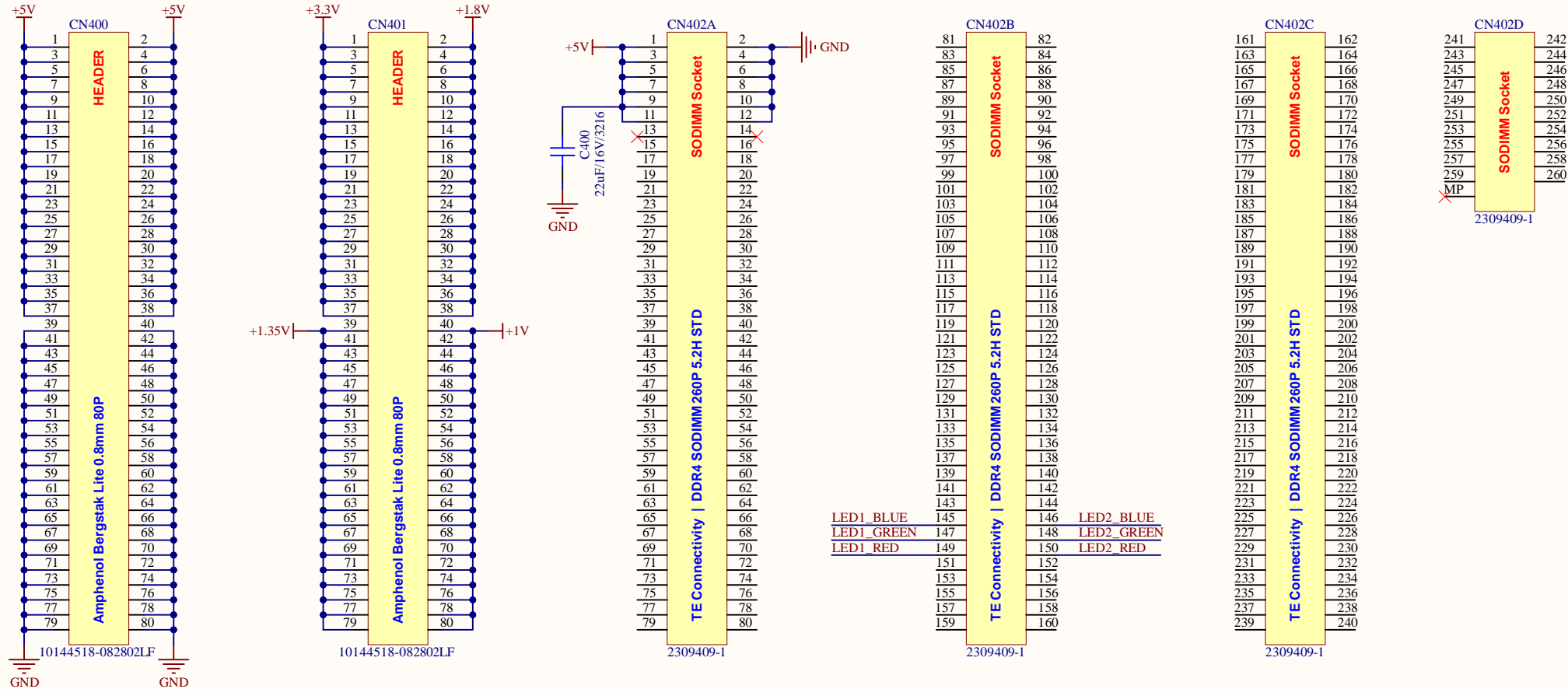
Buck Converters

Power Sequence : +1V → +1.8V → +1.35V → +3.3V



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[4] DUT Connectors



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