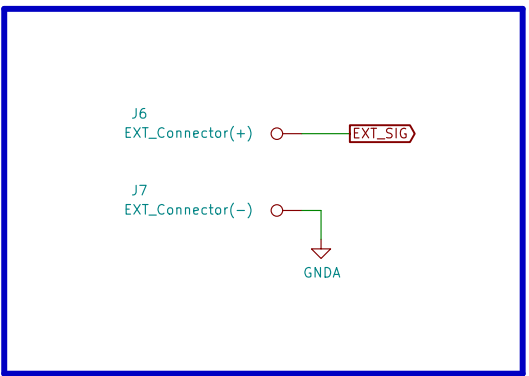
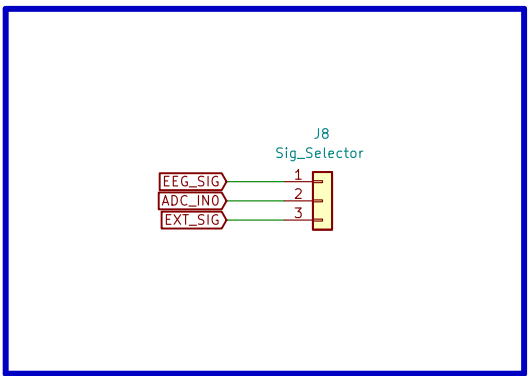


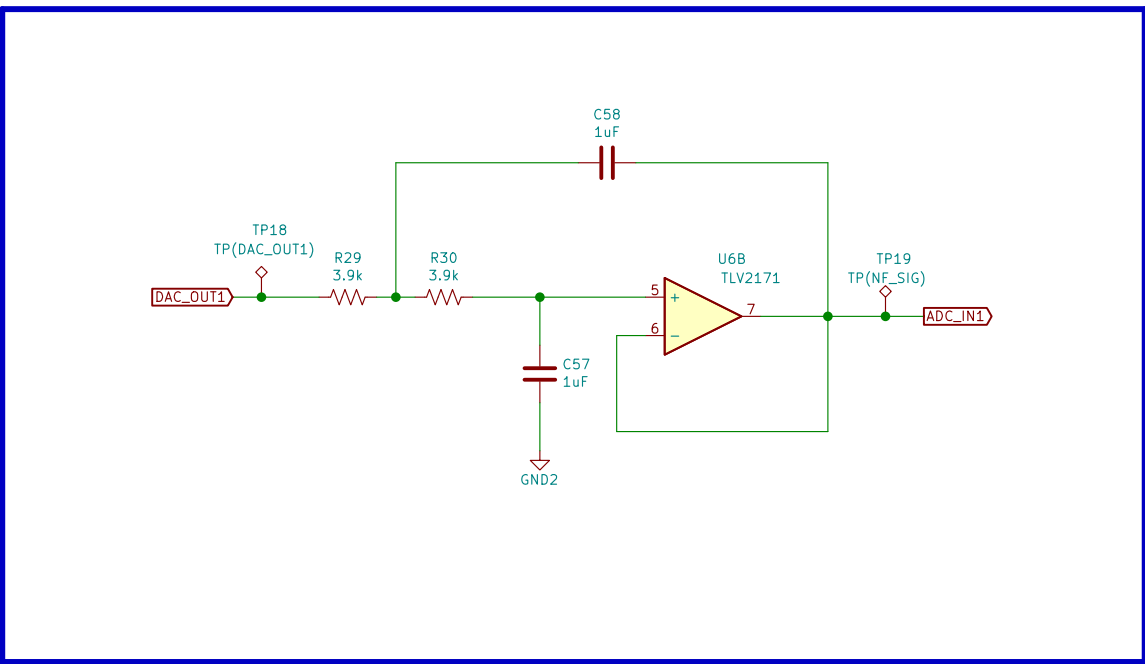
EEG MEASUREMENT CIRCUIT



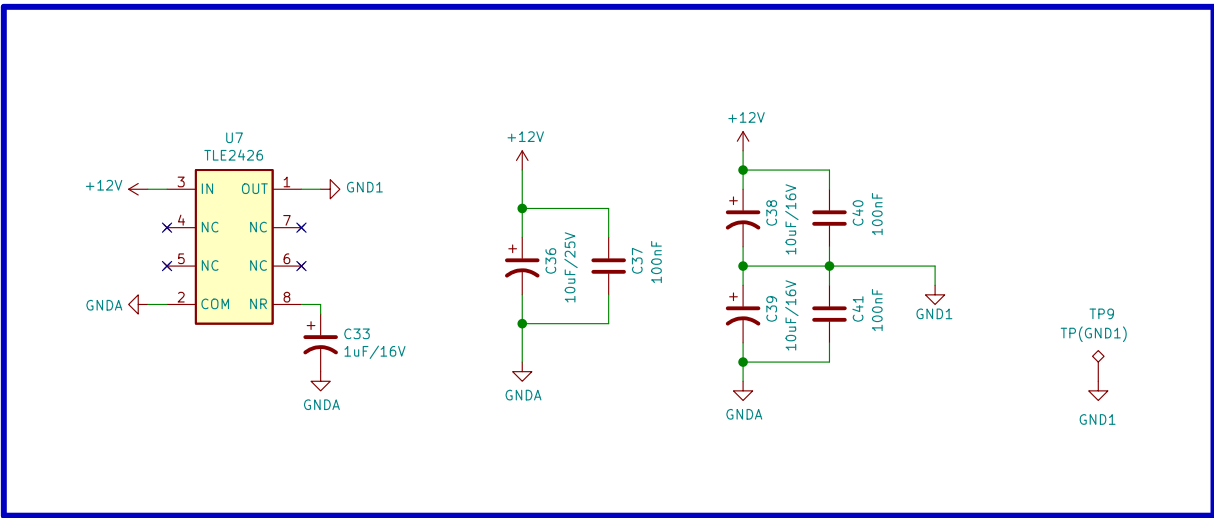
EXTERNAL SIGNAL



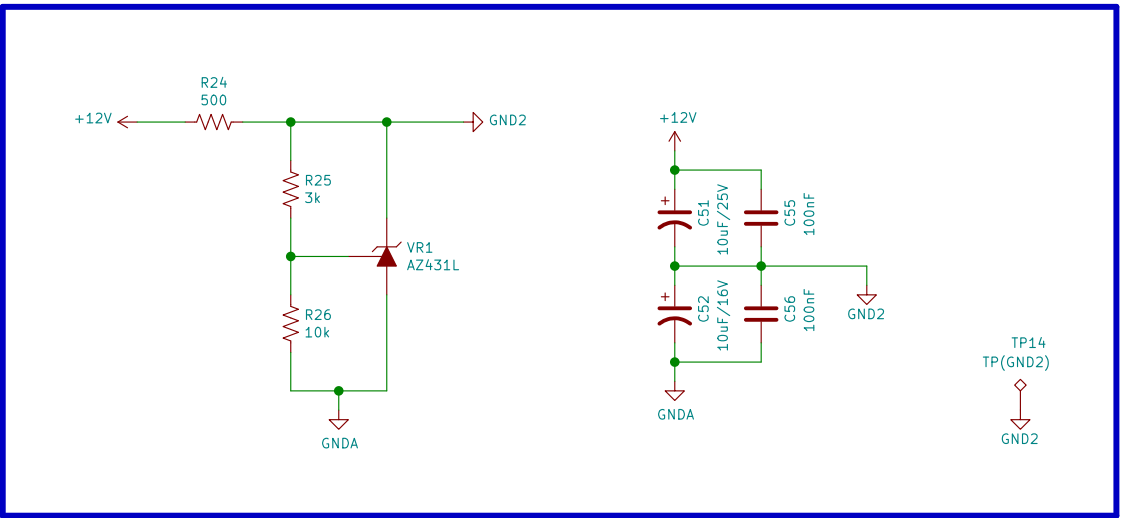
ADC_IN0 INPUT SELECTOR



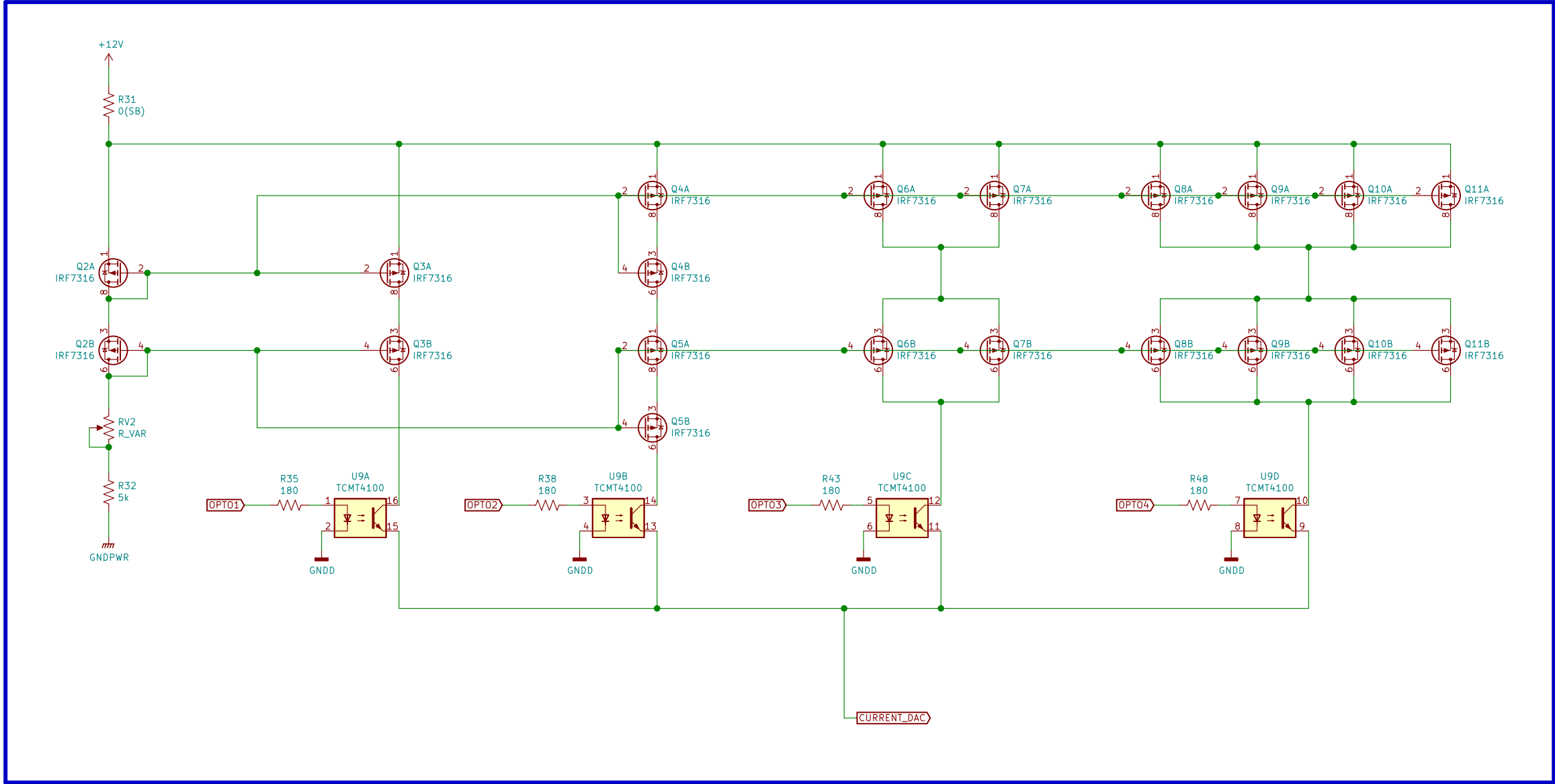
2th Order LPF with Unit DC Gain Post Filter ($f_c = 40\text{Hz}$)



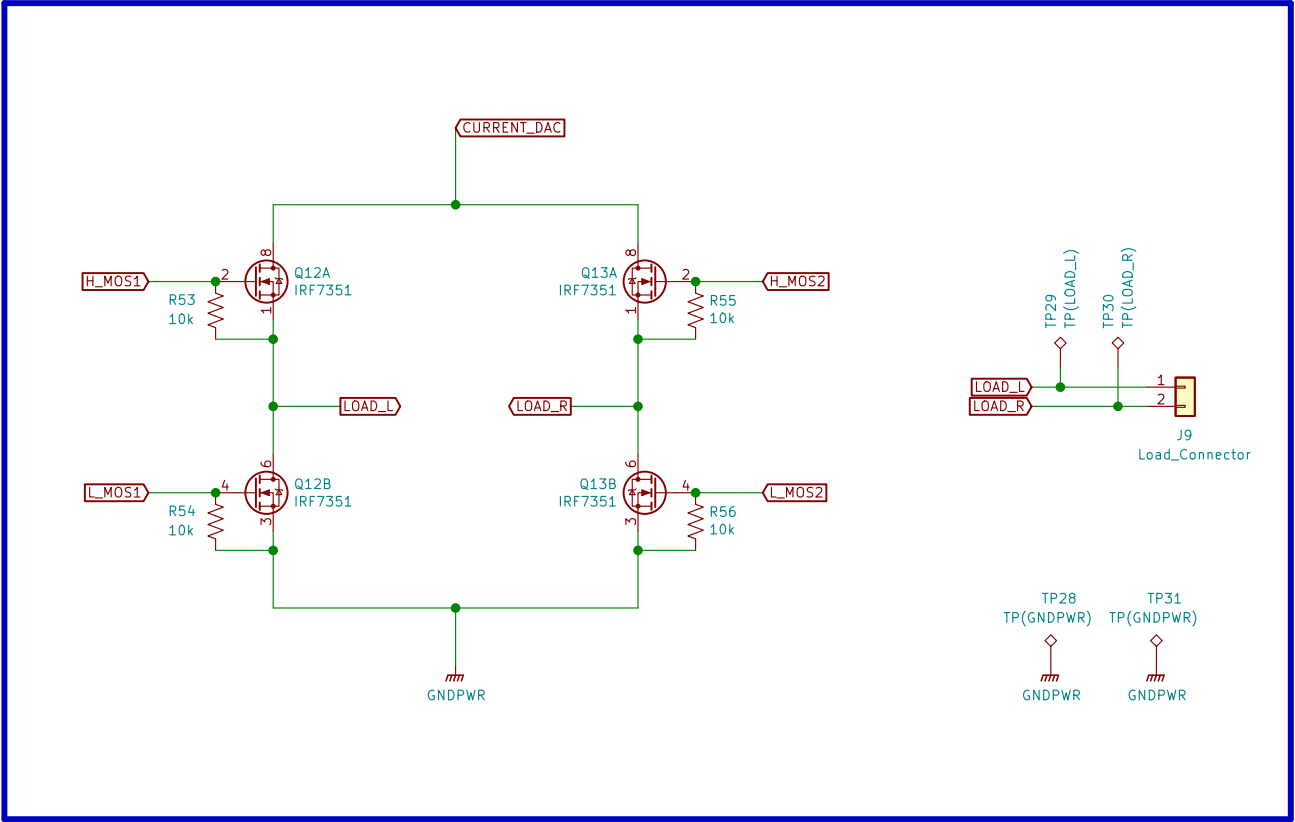
GND REFERENCE (GND1 : 6[V])



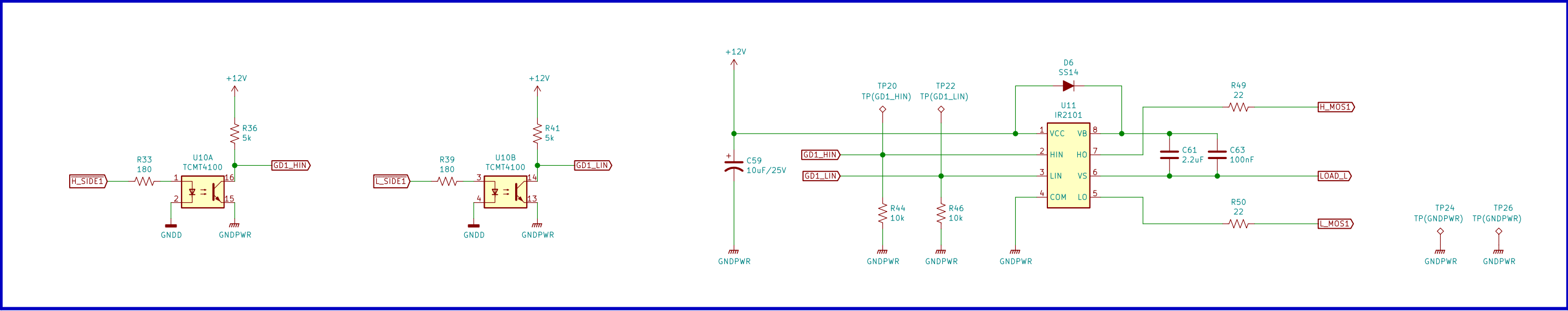
GND REFERENCE (GND2 : 1.65[V])



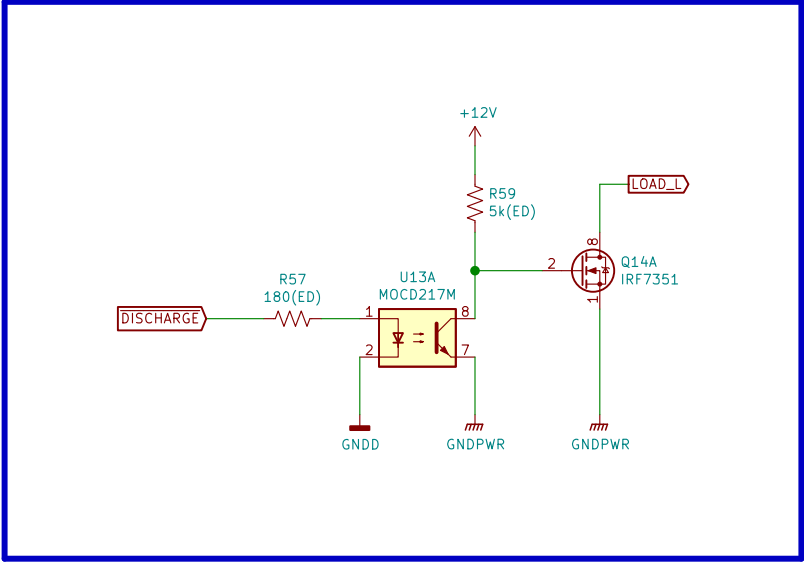
CURRENT STEERING DAC



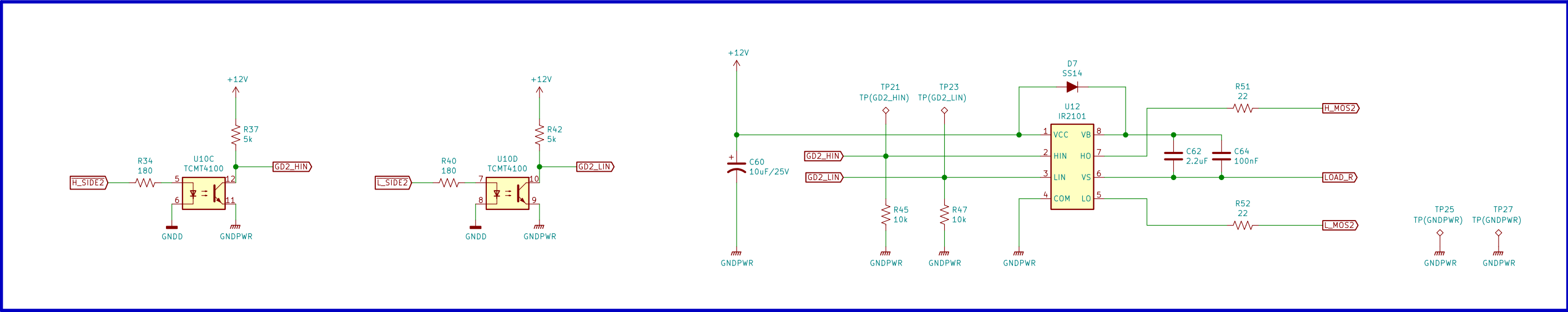
BI-POLAR CURRENT DRIVER



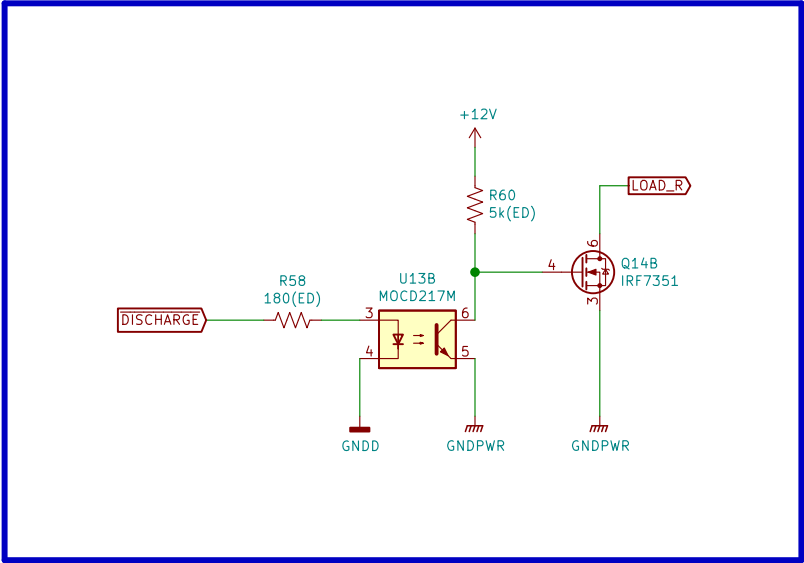
BI-PHASIC PULSE SHAPING GATE DRIVER (LEFT)



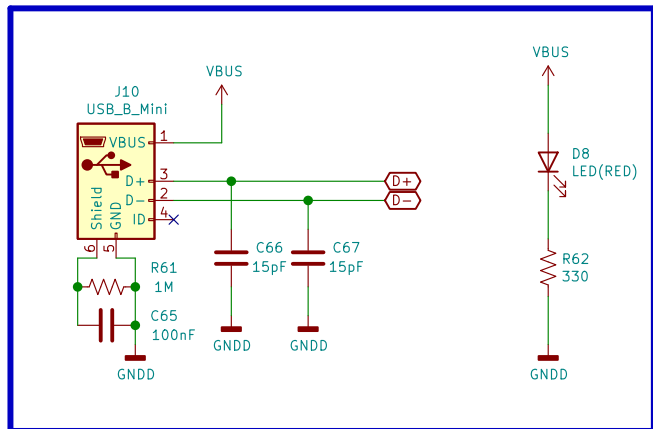
PASSIVE DISCHARGE (LEFT)



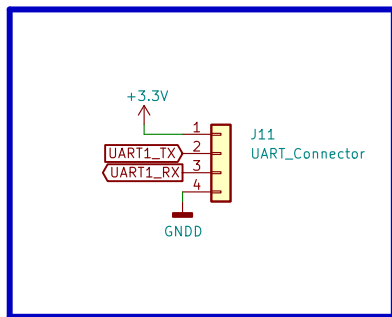
BI-PHASIC PULSE SHAPING GATE DRIVER (RIGHT)



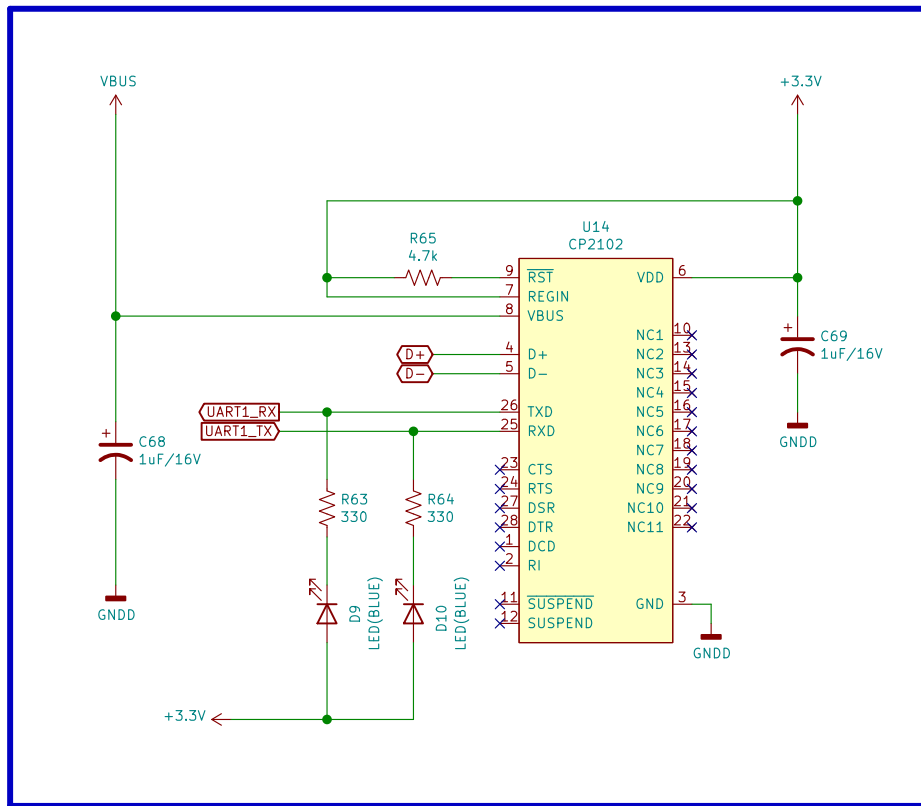
PASSIVE DISCHARGE (RIGHT)



USB-B MINI CONNECTOR



UART CONNECTOR



USB to UART BRIDGE

Designed by Ganghyeok Lim

Arkx

Sheet: /EEG Measurement & Stimulation System (4/4)/

File: file61D3EB20.sch

Title: EEG Measurement & Brain Stimulation System (4/4)

Size: A4 Date: 2021-04-09

Rev: Ver1.0A

KiCad E.D.A. eeschema (5.1.7)-1

Id: 4/4