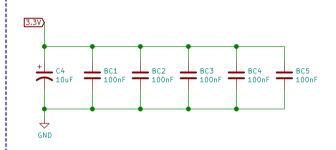
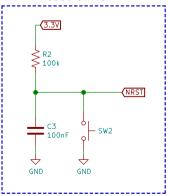
# N1 - Power Output (+3.3V) PWR\_FLAG U1 S514 LM1117-3.3 3.3V V0 2 10uF R7 150

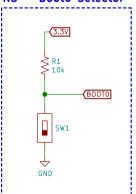




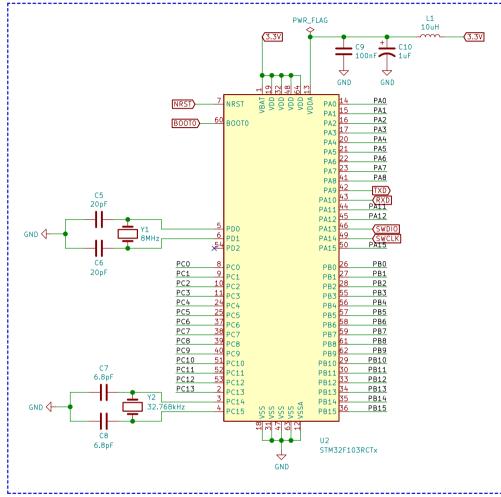
N4 - Reset Switch



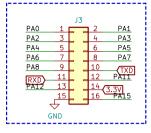
N5 - Boot0 Selector



N2 - STM32F103RCT6 Circuitry



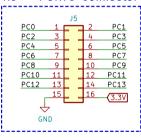
# N7 - PORTA Connector



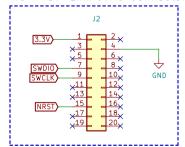
### N8 - PORTB Connector

PB0	1	J4	2	PB1
PB2	3		4	PB3
PB4	5		6	PB5
PB6	7		8	PB7
PB8	9		10	PB9
PB10	11		12	PB11
PB12	13		14	PB13
PB14	15		16	PB15

### N9 - PORTC Connector



# N6 - ST-Link JTAG Connector



Designed by Ganghyeok Lim

# ArkX

Sheet

File: STM32F103RCT6\_Dev\_Board\_Ver1.0A.sch

# Title: STM32F103RCT6 Dev Board

Size: User	Date: 2020-10-31	Rev: Ver 1.0A
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