HALF ADDER

PROGRAM:

module half\_adder  (

   i\_bit1,

   i\_bit2,

   o\_sum,

   o\_carry

   );

  input  i\_bit1;

  input  i\_bit2;

  output o\_sum;

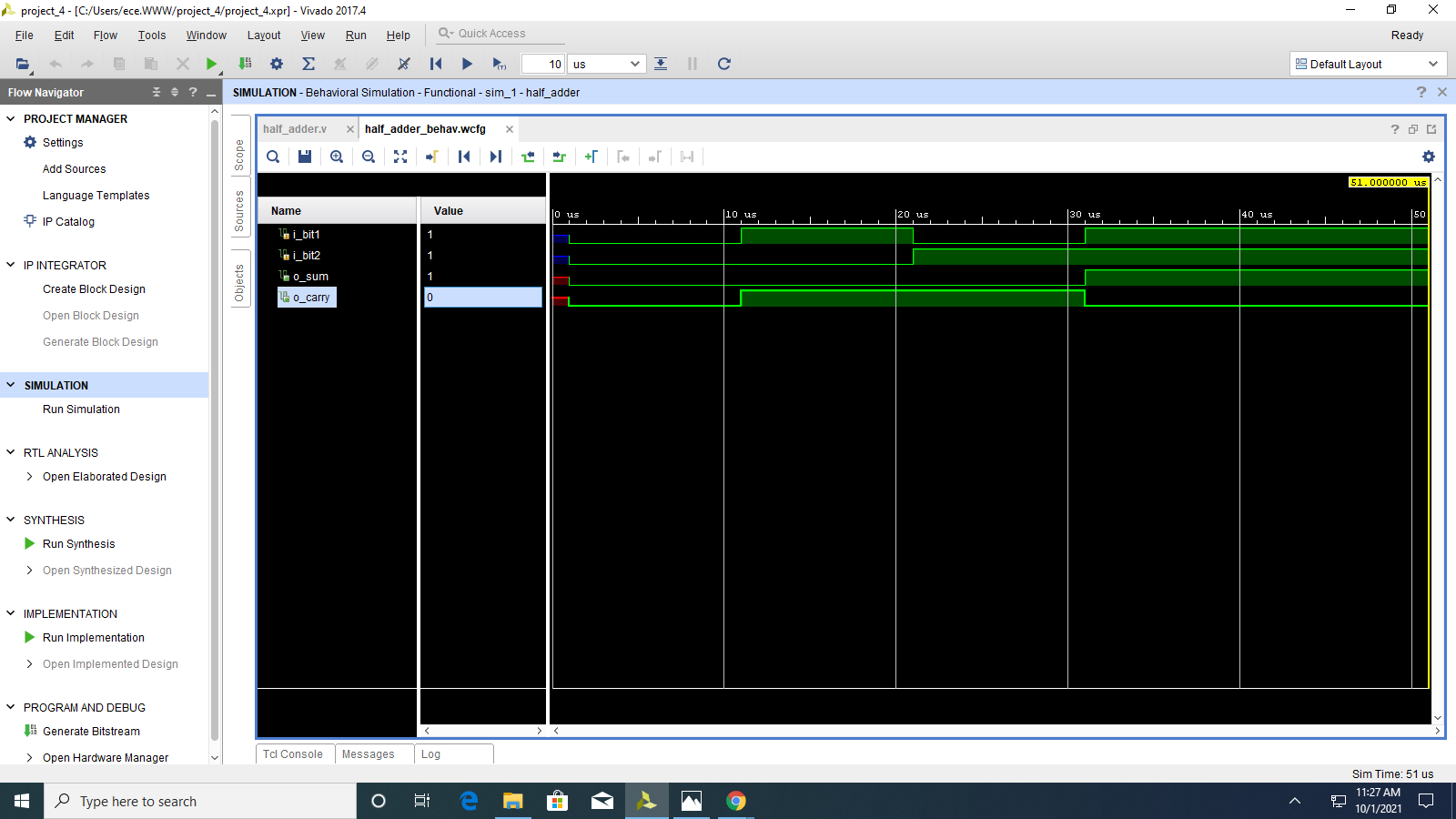
  output o\_carry;

  assign o\_sum   = i\_bit1 ^ i\_bit2;

  assign o\_carry = i\_bit1 & i\_bit2;

end module

**Waveforms:**



**RTL SIMULATION**:

