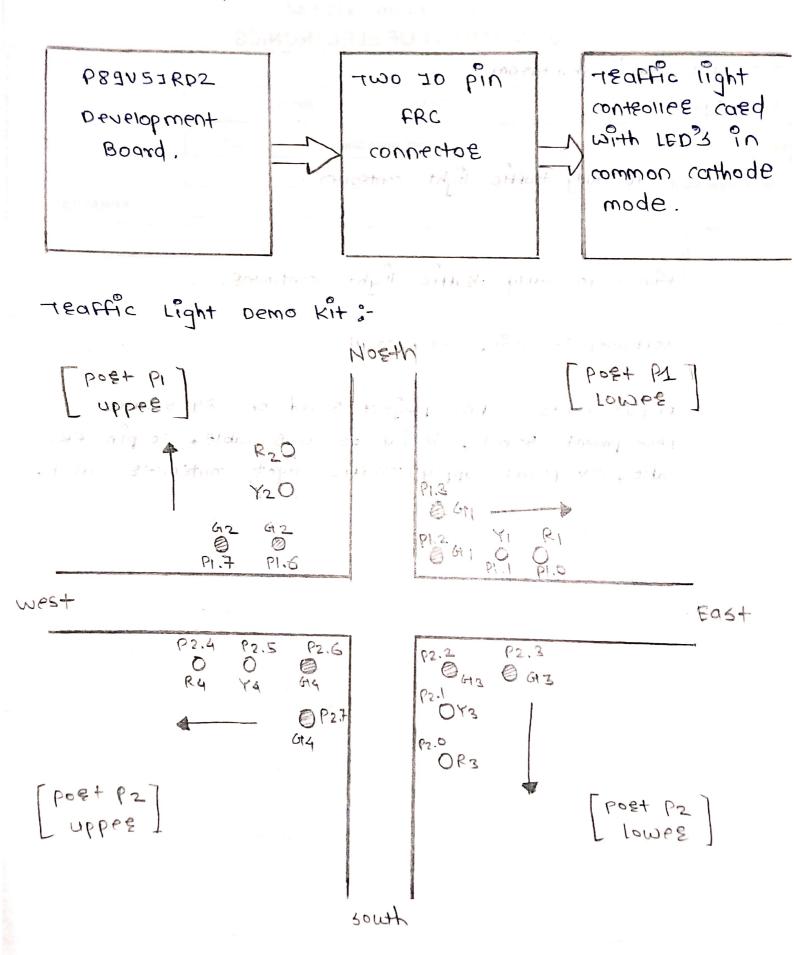
Dr. D. Y. Patil Unitech Soceity Dr. D. Y. Patil Science & Computer Science College, Akurdi, Pune - 411 044

DEPARTMENT OF ELECTRONICS

Name: Patil sugaj sha	ALCVIEY					
Class: Sybcs making	Roll No. :	Batch :	La	arada za d		
Experiment No. :	Performed Da	Performed Date : / /20				
itle of Experiment : To study	teaffic light control	168.		· breed		
olan.				REMARKS		
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4m :- 70	study teaffic light	conte	niiee			
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	power supply TEaH					
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clecut plageam :-



Theory: In teaffic light cord which is interfaced with 74125772 is composed of 18 LED, 7 to gisbland of all signals used in the actual teappic light system to acheive this we have used is 210 lines of IC 89F5IGRDZ (port 1 and 2). All LED'S are connected in common cathode mode (i.e all rathodes are grounded. The delay used in the program is very low as compared to actual system so as to have proper demonstration of controller card. calculations of the values to be teansfee on poets: thive High (1) logic for LED and Low(0) Logic for LED OFF. peoceduce: connect tizu supply to board. connect the USB cable of the miceo contenies kit to the PC. connect to pin FRC cables to the teathic light interfacing casd. switch on the power supply. open the software "sst" to establish the communication click the "option" of SST SOFTWARE and select the com post and boud some as good dick ok. Reset the device. click to phload/ Run userrode. now BEOWSE Hex file that is to be download then click ok.

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FRC	connectos	petalls	•
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```
peogeam :-
  #include < reg s1.h>
  msdelay (unsigned PM);
  main ()
  PJ = 0x00;
   P2 = 0 × 00;
  while (z)
   11 NOE+H
   PI = 0 x 01;
   P2 = 0×11;
   msdelay (500);
   PI = 0X21;
   msdelay (200);
   11 895+
   Pz= 0x 10;
   P2 = 0 × 11 's have radio har reason de
   msdelay (500);
   P1 = 0 x 12;
   wedflah (soo);
   11 south
   Py = 0 X11;
   P2 = 0 X 10;
   msdelay (500);
   P2 = 0 × 12;
   msdelay (200);
   11 0067+
  PI= OXII;
 P2 = 0x10;
```

		msdelay (200);
		7
		7
		msdelay (unsigned int itime)
		ş
		9nt igis
		68 (1:0;1<1time; 1++);
	and the second s	fog (j=0; j< 2000; j++);
		7
	1	
		Rebult !-
	1.	we have studied and exercised teathic light program
		and verify the output.
	2.	acrossing to the given disection ascen , reliow and
		zed lep's ase on in sequence.
	3.	FOR example, east disection 1st the given LED'S ON, FOR
	A note that a second	specific time at the same time, all the disection sed
		LED'S ON then yellow LED'S will be ON FOR Specific
		time then after that green LEP's for neat diagram.
	4.	In the eight mode all lep's of yellow colour are
		bijuking.
- Aug		
- Carlotte	-	conclusion !-
of the particular to	1.	LED can be interfaced to any one of the part.
- Andrews	2,	Here part I and part 3 for LED interfacing LED can be on when particular part bit is I.
	3.	LED can be on when pasticulas past bit is I.
The state of the s		
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CHARGO	- 11	