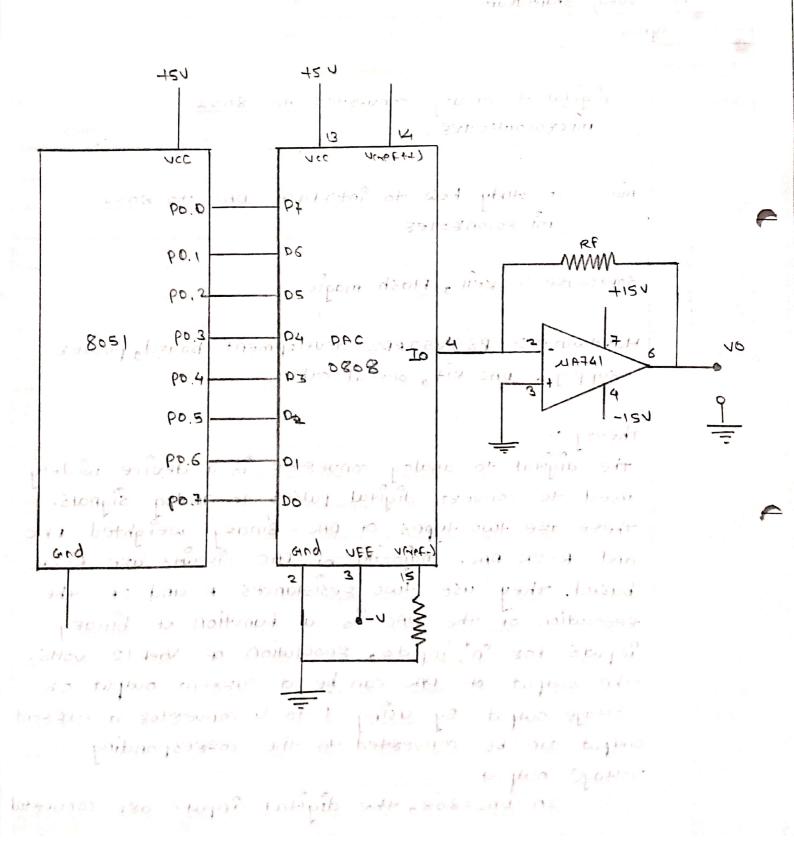
## Dr. D. Y. Patil Unitech Soceity Dr. D. Y. Patil Science & Computer Science College, Akurdi, Pune - 411 044

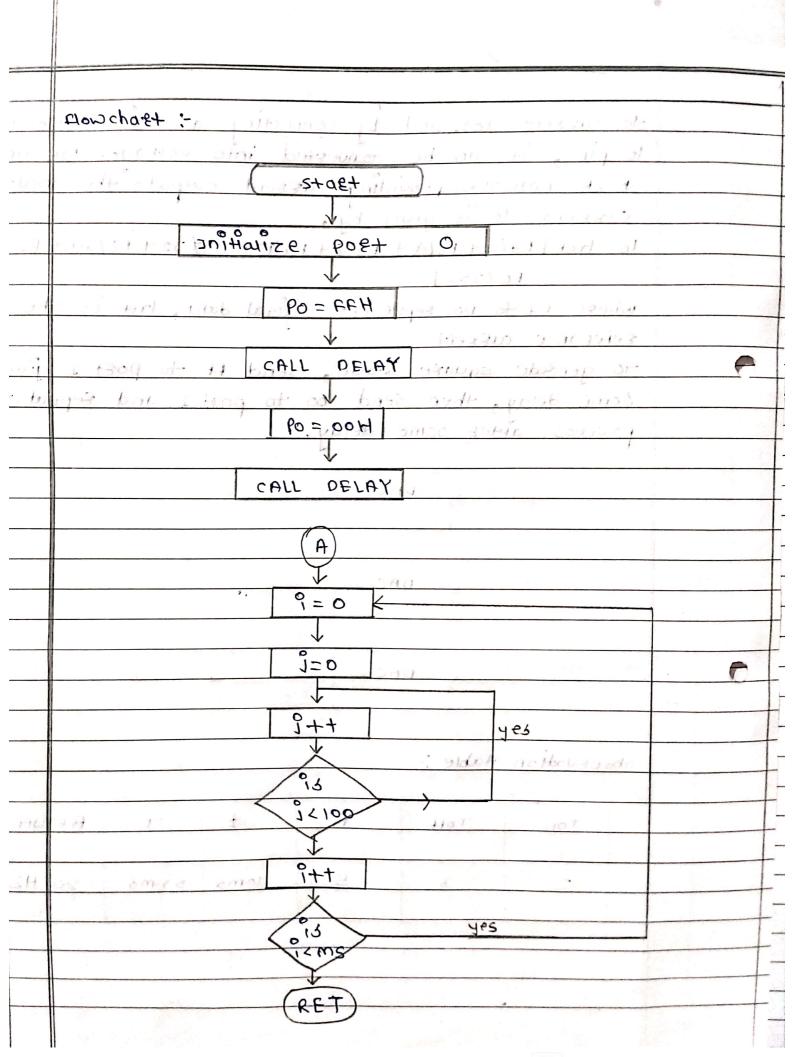
DEPARTMENT OF ELECTRONICS

Name: Patil	sueaj shantaeam.	, ' · · · ·
Class: 341		
Experiment No. :	in a second	
Title of Experime	nt: <u>oigital to analog</u> converter to 8051	REMARKS
	Am :- TO study how to interface DAC to	2 8051
	miceo conteoller.	
		<u>(#</u>
	software: Keil, Hash magic.	5
_		1
	Hardwore: PRGV51RD2 development boo	red, bonnes
	supply, DAC Kit, seeid cable.	<u> </u>
	Theory:	0 0 0 101
	The digital to analog converter is a d	eorge widery
	used to convert digital pulses to and	seighted on a
	There are two types of DAC - Binary w	LA nee 8-20
	and R-2R DAC. majorty of DAC cierustones R of based. They use two Epsistones R of	2000
	resolution of the DAC is a function	of binger
	inputs. for inputs, resolution of	_
	the output of pac can be a cuesent	
	voltage output. By using 1 to V conves	etoe a curent
	ontent can pe connected to the coeses	
	Noltage output	
	In DACO808, the digital input:	s are converte

## Block Diageam :-



Tild bedden	And the second s							
	to cuesent (10), and by connecting a resistor to the							
	lo pin, it can be converted into voltage. for an 8-bit							
	R-2R network providing rureent outputs the analog							
	rueeent lo is given by,							
	10 = lref [ D7/2 + D6/4 + D5/8 + D4/16 + D3/32 + D2/64 + P1/128+							
	00/256]							
	where pt to po represent digital data, lines is the							
	referance ruffent.							
	To gentate square wave, send of to port 1 give							
	some delay, then send on to post I and seperat the							
_	Process after some delay.							
_	PEOCESS GALL	30111	· sietaj.			# *	NAME OF TAXABLE PARTY.	
	ttH	. 0	ACHIAL .	Meo !	1 3 01P		- Landard Company	
		7	01		)			
1			A		, and			
1	Ноо	OOH DAC 1 1 OIP						
		-0/P						
	\$ . A		*	5A 7.73 (MARK)			PARTITION DESCRIPTION OF THE PARTITION O	
	FFH, delay, sou, del	04 0	AC				and the same of	
		7	01	ρ			atempresent	
	++2							
	observation table:							
	2 62							
	TON -	70FF	10126	, <del>†</del>	7	Requency		
		la a		and the second		7		
	2	3	51 1	loms	50 MS	20 HZ		
							-	
			6.	37			Market States of the	
	T. T							
11								



```
PAC peogeam :-
  #include zregsz. L>
void delay_ms(unsigned int);
void man (void)
      Po=0X FF;
       while (2)
        ; 47 XO =09
        delay-ms(10);
        PO = 0x00;
        delay - msc 10);
      rold delay-ws (unsigned int ms)
        foe(j=0; j<0×100; j++);
RESWY :
             7: 50 ms
                           0.02× 103
                            20 HZ
```

	conclusion:
[i	
2	BY Asing DAC. We can albegate gittesein types of
	waveform such as square, traingular and sine wave
उ	no cau nash the begind of Rednevice. Mans
	using control delay.
and in	
	; +1 < 7 - 1 +
	; 21 an eredi
	ioi = n - poid
	(n ti lengen en gest bro
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	gi î mi boni m
	Chtransi, o 1) as
	And the second of the second o
1	
1-1	COLOGICE CHARLES