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1. 2014.9-Present Chongqing University, study for Ph.D.’s degree, Major: Computer Science and Technology.
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Publications:

1, **Congming Gao**, Liang Shi, Mengying Zhao, Chun Jason Xue, Kaijie Wu, and Edwin Sha. "*Exploiting Parallelism in IO Scheduling for Access Conflict Minimization in Flash-based Solid State Drives*". (The 30th International Conference on Massive Storage Systems and Technology, MSST’14) –**Published**

2, **Congming Gao**, Liang Shi, Kaijie Wu, Chun Jason Xue, and Edwin H.-M. Sha. "*Exploit Asymmetric Error Rates of Cell States to Improve the Performance of Flash Memory Storage Systems*". (The 32nd IEEE International Conference on Computer Design, ICCD’14) –**Published**

3, Qiao Li, Liang Shi, **Congming Gao**, Kaijie Wu, Jason Chun Xue, Qingfeng Zhuge and H.-M. Edwin Sha. "*Maximizing IO Performance via Conflict Reduction for Flash Memory Storage Systems*". (The 2015 Design, Automation & Test in Europe Conference, DATE’15) –**Published**

4, Yejia Di, Liang Shi, **Congming Gao**, Kaijie Wu, Chun Jason Xue and Edwin H.-M. Sha. "*Minimizing cell-to-cell interference by exploiting differential bit impact characteristics of scaled MLC NAND flash memories*". (Non-Volatile Memory Systems and Applications Symposium, NVMSA’16) –**Published**

5, Edwin H.-M. Sha, **Congming Gao**, Liang Shi, Kaijie Wu, Mengying Zhao and Chun Jason Xue. "*Asymmetric Error Rates of Cell States Exploration for Performance Improvement on Flash Memory based Storage Systems*". (IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2016) –**Published**

6, **Congming Gao**, Liang Shi, Cheng Ji, Yejia Di, Kaijie Wu, Chun Jason Xue and Edwin H.-M. Sha. "*Exploiting Parallelism for Access Conflict Minimization in Flash-based Solid State Drives*". (IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017) –**Published**

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14, **Congming Gao**, Yejia Di, Aosong Deng, Duo Liu, Cheng Ji, Chun Jason Xue and Liang Shi. “*Flash Friendly File System Aware Mapping Cache Design on Solid State Drives*”. (The 7th IEEE Non-Volatile Memory Systems and Applications Symposium, NVMSA’18) –**Published**

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