

FPGA Speaks

LEVEL-1

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1 Introduction

Basic logic gates AND,OR,NOT,XOR,NAND,NOT,XNOR,are designed

2 Tools/Software used

Xilinx Vivado

3 Description

AND Gate: It takes two inputs and produce one output.Output is high only when both inputs are high

OR Gate: It takes two inputs and produce one output.Output is high only when one of inputs is high

NAND Gate: It takes two inputs and produce one output.Output is high only when one of inputs is low

NOR Gate: It takes two inputs and produce one output.Output is low only when both inputs are high

XOR Gate: It takes two inputs and produce one output.Output is high only when number of high inputs are odd

NOT Gate: It takes one input and produce one output.Output is high only when input is low and low when input is high

XNOR Gate: It takes two inputs and produce one output.Output is high only when number of higher inputs are even

4 What I have done at this level?

coding of Basic logic gates is done using xilinx Vivado

5 Link

Github