
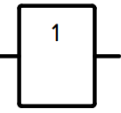
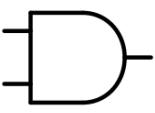
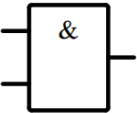
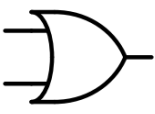
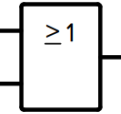
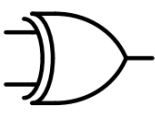
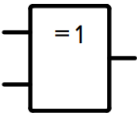

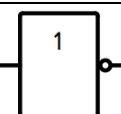
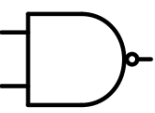
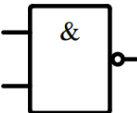
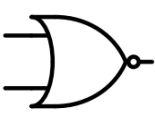
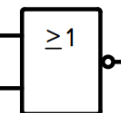
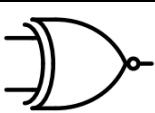
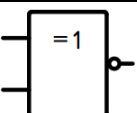


Tabela WR de Portas Lógicas			
Saída Normal			
Pergunta	Resposta	Lógica	Símbolo
O que é preciso para a saída ser VERDADEIRA ?	A entrada precisa ser verdadeira.	BUFFER	  ANSI IEC
	TODAS as entradas precisam ser verdadeiras.	AND	  ANSI IEC
	UMA ou MAIS entradas precisam ser verdadeiras.	OR	  ANSI IEC
	O número de entradas verdadeiras precisa ser ÍMPAR.	XOR	  ANSI IEC
Saída Complementada			
Pergunta	Resposta	Lógica	Símbolo
O que é preciso para a saída ser FALSA ?	A entrada precisa ser verdadeira.	NOT	  ANSI IEC
	TODAS as entradas precisam ser verdadeiras.	NAND	  ANSI IEC
	UMA ou MAIS entradas precisam ser verdadeiras.	NOR	  ANSI IEC
	O número de entradas verdadeiras precisa ser ÍMPAR.	XNOR	  ANSI IEC
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