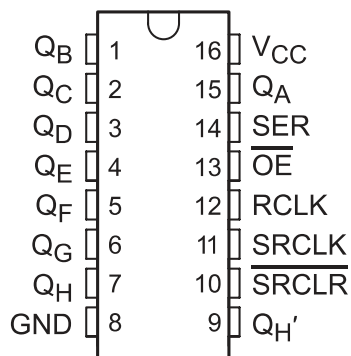
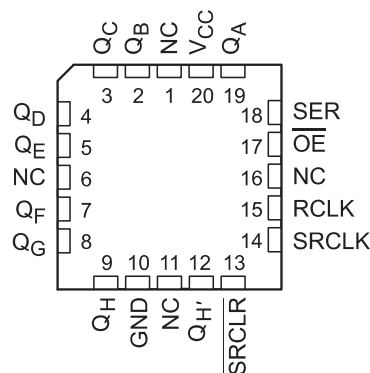


## 5 Pin Configuration and Functions



**D, N, NS, J, DB, or PW Package**  
**16-Pin SOIC, PDIP, SO, CDIP, SSOP, or TSSOP**  
**Top View**



**FK Package**  
**20-Pin LCCC**  
**Top View**

**Table 5-1. Pin Functions**

PIN			I/O <sup>(1)</sup>	DESCRIPTION
NAME	SOIC, PDIP, SO, CDIP, SSOP, or TSSOP	LCCC		
GND	8	10	—	Ground Pin
OE	13	17	I	Output Enable
QA	15	19	O	QA Output
QB	1	2	O	QB Output
QC	2	3	O	QC Output
QD	3	4	O	QD Output
QE	4	5	O	QE Output
QF	5	7	O	QF Output
QG	6	8	O	QG Output
QH	7	9	O	QH Output
QH'	9	12	O	QH' Output
RCLK	12	14	I	RCLK Input
SER	14	18	I	SER Input
SRCLK	11	14	I	SRCLK Input
SRCLR	10	13	I	SRCLR Input
NC	—	1	—	No Connection
		16		
		11		
		16		
VCC	—	20	—	Power Pin

(1) Signal Types: I = Input, O = Output, I/O = Input or Output.