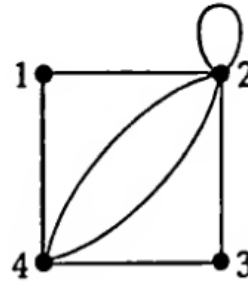


Graph & its Matrix Representation

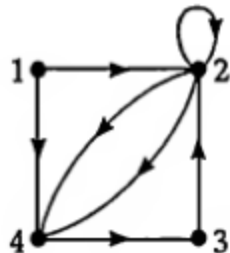
Matrix Representation

A graph is completely determined by either its **adjacencies** or its **incidences** (conveniently stated in matrix form)



	col 1	col 2	col 3	col 4
	↓	↓	↓	↓
row 1 →	0	1	0	1
row 2 →	1	1	1	2
row 3 →	0	1	0	1
row 4 →	1	2	1	0

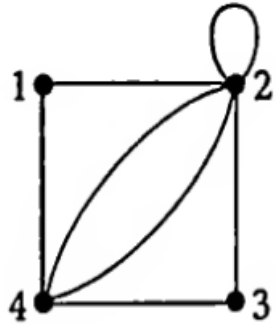
Adjacency matrix of G



	col 1	col 2	col 3	col 4
	↓	↓	↓	↓
row 1 →	0	1	0	1
row 2 →	0	1	0	2
row 3 →	0	1	0	0
row 4 →	0	0	1	0

Adjacency matrix of directed G

Matrix Representation



	col 1	col 2	col 3	col 4
	↓	↓	↓	↓
row 1 →	0	1	0	1
row 2 →	1	1	1	2
row 3 →	0	1	0	1
row 4 →	1	2	1	0

- The adjacency matrix of a graph is symmetrical about the main diagonal (top-left to bottom-right)
- Also, for a graph without loops, each entry on the main diagonal is 0
- The sum of the entries in any row or column is the degree of the vertex corresponding to that row or column

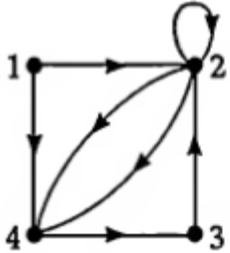
A labeled graph and its adjacency matrix

Definition

Let G be a graph with n vertices labeled $1, 2, 3, \dots, n$.

The adjacency matrix $A(G)$ of G is the $n \times n$ matrix in which the entry in row i and column j is the **number of edges** joining the vertices i and j .

Matrix Representation



	col 1	col 2	col 3	col 4
	↓	↓	↓	↓
row 1 →	0	1	0	1
row 2 →	0	1	0	2
row 3 →	0	1	0	0
row 4 →	0	0	1	0

Adjacency matrix of a **labeled digraph G**

- Not usually symmetrical about the main diagonal
- If the digraph has no loops, then each entry on the main diagonal is 0
- The sum of the entries in any row is the **out-degree** of the vertex corresponding to that row
- The sum of the numbers in any column is the **in-degree** of the vertex corresponding to that column

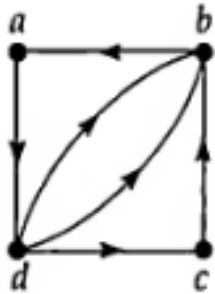
Definition

Let D be a digraph with n vertices labelled $1, 2, 3, \dots, n$.

The adjacency matrix $A(D)$ of D is the $n \times n$ matrix in which the entry in row i and column j is **the number of arcs** from vertex i to vertex j .

Matrix Representation

Adjacency matrix of a labeled digraph G



	a	b	c	d
a	0	0	0	1
b	1	0	0	0
c	0	1	0	0
d	0	2	1	0

Walks of lengths 2:

For example, there are two different walks of length 2 from a to b, because there is one arc from a to d and two arcs from d to b.

Walks of lengths 3:

Similarly, there are two different walks of length 3 from d to d, since there are two arcs from d to b, and one walk of length 2 from b to d, namely, bad.

Walks of lengths 1:

the number of walks of length 1 from a to c is 0, so 0 appears in row 1 column 3;
 the number of walks of length 1 from b to a is 1, so 1 appears in row 2 column 1;
 the number of walks of length 1 from d to b is 2, so 2 appears in row 4 column 2.

	a	b	c	d
a		2		
b			1	
c				
d				

numbers of walks of length 2

	a	b	c	d
a				
b				
c				
d				2

numbers of walks of length 3

- Complete the matrices...



	a	b	c	d
a	0	0	0	1
b	1	0	0	0
c	0	1	0	0
d	0	2	1	0

	a	b	c	d
a		2		
b				1
c				
d				

numbers of walks of length 2

	a	b	c	d
a				
b				
c				
d				2

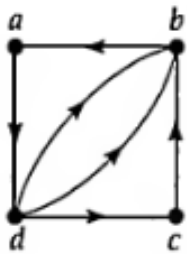
numbers of walks of length 3

- Complete the matrices...

$$A = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \end{bmatrix}, \quad A^2 = \begin{bmatrix} 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 2 & 1 & 0 & 0 \end{bmatrix}, \quad A^3 = \begin{bmatrix} 2 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 2 \end{bmatrix}.$$

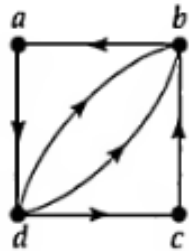
Let G be a digraph with n vertices labeled $1, 2, \dots, n$, let A be its adjacency matrix with respect to this listing of the vertices, and let k be any positive integer. Then the number of walks of length k from vertex i to vertex j is equal to the entry in row i and column j of the matrix A^k (the k^{th} power of the matrix A)

Matrix Representation



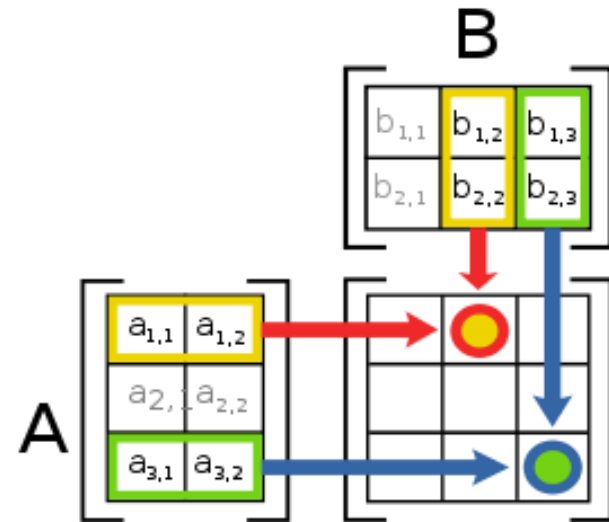
	a	b	c	d
a	0	0	0	1
b	1	0	0	0
c	0	1	0	0
d	0	2	1	0

$$\begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \end{bmatrix}$$



	a	b	c	d
a	0	0	0	1
b	1	0	0	0
c	0	1	0	0
d	0	2	1	0

0	0	0	1
1	0	0	0
0	1	0	0
0	2	1	0

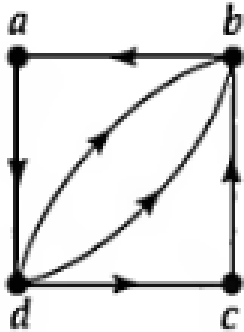


$$\begin{aligned} x_{1,2} &= (a_{1,1}, a_{1,2}) \cdot (b_{1,2}, b_{2,2}) \\ &= a_{1,1}b_{1,2} + a_{1,2}b_{2,2} \end{aligned}$$

$$\begin{aligned} x_{3,3} &= (a_{3,1}, a_{3,2}) \cdot (b_{1,3}, b_{2,3}) \\ &= a_{3,1}b_{1,3} + a_{3,2}b_{2,3}. \end{aligned}$$

Matrix Representation

A digraph is strongly connected if there is a path from vertex i to vertex j , for each pair of distinct vertices i and j , and that a path is a walk in which all the vertices are different.

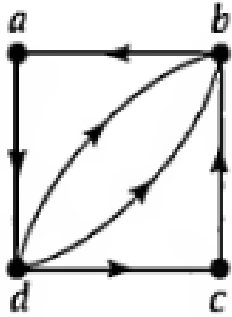


There are four vertices, so a path has length 1, 2 or 3.

The numbers of walks (including the paths) of lengths 1, 2 and 3 between pairs of distinct vertices are given by the non-diagonal entries in the matrices in A , A^2 , and A^3

$$A = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \end{bmatrix}, \quad A^2 = \begin{bmatrix} 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 2 & 1 & 0 & 0 \end{bmatrix}, \quad A^3 = \begin{bmatrix} 2 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 2 \end{bmatrix}.$$

Matrix Representation



$$A = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \end{bmatrix}, \quad A^2 = \begin{bmatrix} 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 2 & 1 & 0 & 0 \end{bmatrix}, \quad A^3 = \begin{bmatrix} 2 & 1 & 0 & 0 \\ 0 & 2 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 2 \end{bmatrix}.$$

$$B = A + A^2 + A^3 = \begin{bmatrix} 2 & 3 & 1 & 1 \\ 1 & 2 & 1 & 1 \\ 1 & 1 & 0 & 1 \\ 3 & 3 & 1 & 2 \end{bmatrix}.$$

Let b_{ij} denote the entry in row i and column j in the matrix B . Then each entry b_{ij} is the total number of walks of lengths 1, 2 and 3 from vertex i to vertex j .

Since all the **non-diagonal** entries are positive, each pair of distinct vertices is connected by a path, so the digraph is strongly connected.

Theorem: Let D be a digraph with n vertices labeled $1, 2, \dots, n$, let A be its adjacency matrix with respect to this listing of the vertices, and let B be the matrix

$$B = A + A^2 + \dots + A^{n-1}$$

Then D is strongly connected iff each non-diagonal entry in B is positive

- that is, $B_{ij} > 0$ whenever $i \neq j$

Pros and Cons of Adjacency Matrices

- Pros:
 - Simple to implement
 - Easy and fast to inform if a pair (i,j) is an edge: simply check if $A[i][j]$ is 1 or 0
- Cons:
 - No matter how few edges the graph has, the matrix takes $O(n^2)$ in memory

Adjacency Lists Representation

- A graph of n nodes is represented by a one-dimensional array L of linked lists, where
 - $L[i]$ is the linked list containing all the nodes adjacent from node i
 - The nodes in the list $L[i]$ are in no particular order

Example of Linked Representation

L[0]: empty

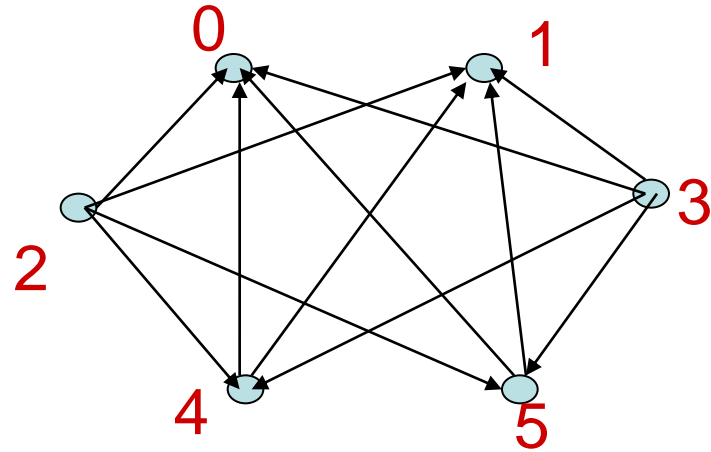
L[1]: empty

L[2]: 0, 1, 4, 5

L[3]: 0, 1, 4, 5

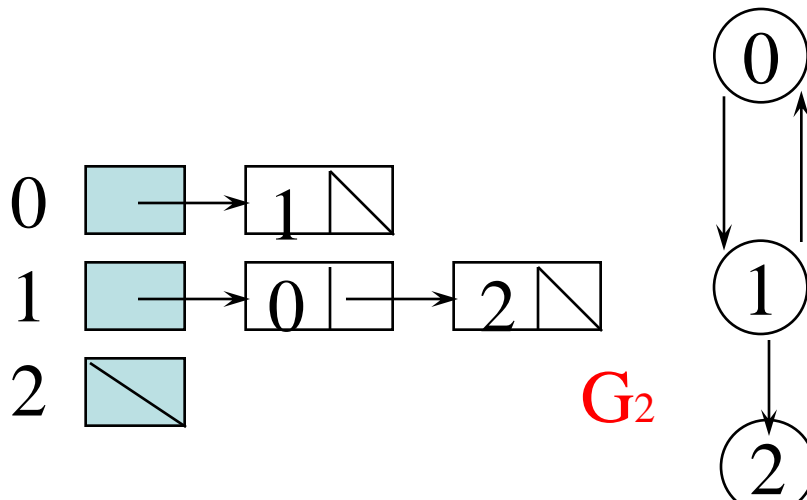
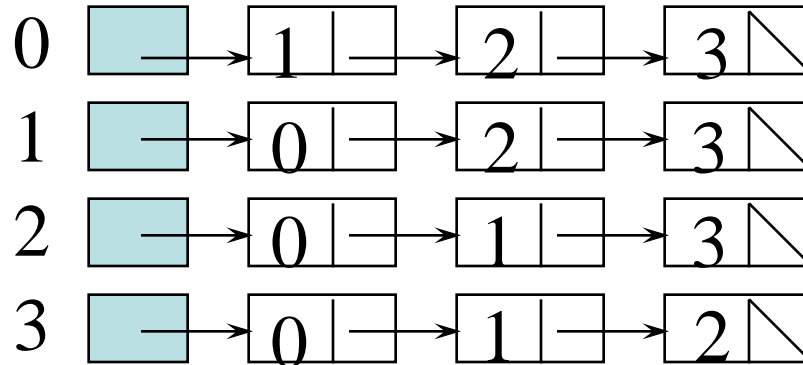
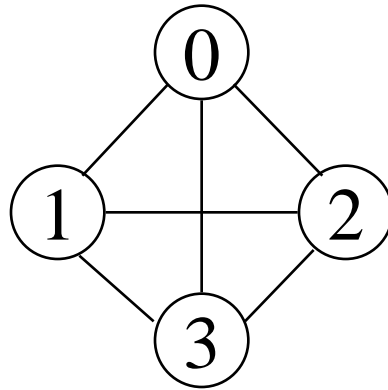
L[4]: 0, 1

L[5]: 0, 1

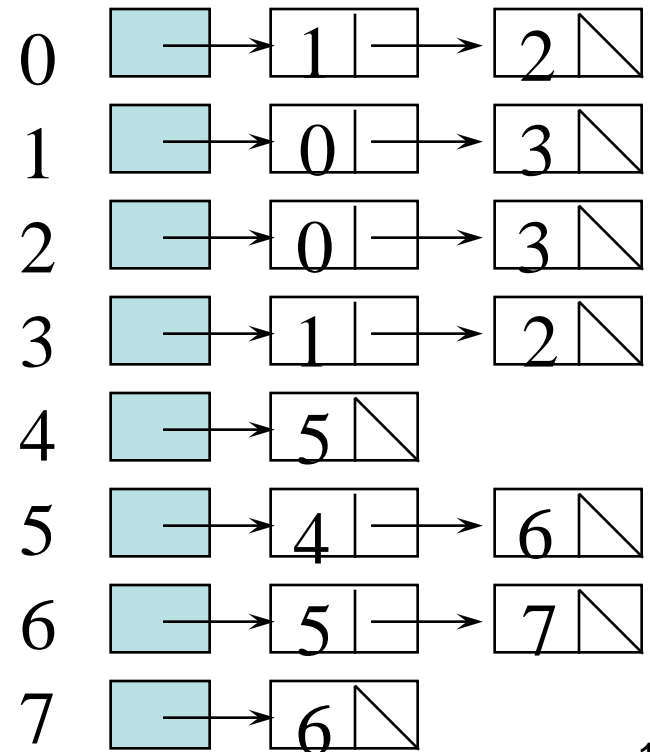
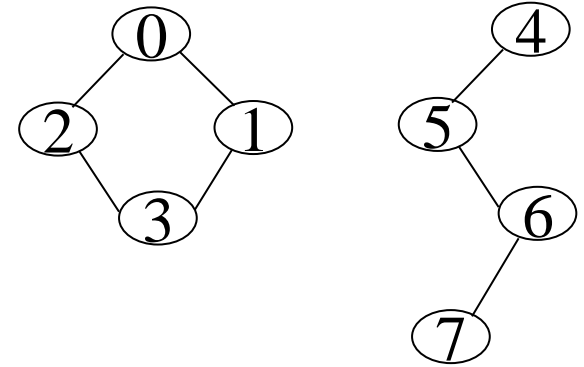


Examples of Adjacency Lists

G_1



G_3



Pros and Cons of Adjacency Lists

- Pros:
 - Saves on space (memory): the representation takes as many memory words as there are nodes and edges
- Cons:
 - It can take up to $O(n)$ time to determine if a pair of nodes (i,j) is an edge: one would have to search the linked list $L[i]$, which takes time proportional to the length of $L[i]$.

The Graph Class

```
class Graph {  
    public:  
        typedef int datatype;  
        typedef datatype * datatypeptr;  
        Graph( int n=0); // creates a graph of n nodes and no edges  
        bool isEdge( int i, int j);  
        void setEdge( int i, int j, datatype x);  
        int getNumberOfNodes(){return numberOfNodes;};  
    private:  
        datatypeptr *p; //a 2-D array, i.e.an adjacency matrix  
        int numberOfNodes;  
};
```

Graph Class Implementation

```
Graph::Graph( int n){
    assert(n>=0);
    numberOfNodes=n;
    if (n==0) p=NULL;
    else{
        p = new datatypeptr[n];
        for (int i=0;i<n;i++){
            p[i] = new datatype[n];
            for (int j=0;j<n;j++)
                p[i][j]=0;
        }
    }
};
```

```
bool Graph::isEdge(int i, int j){
    assert(i>=0 && j>=0);
    return p[i][j] != 0;
};
```

```
void Graph::setEdge(int i,
                    int j, datatype x){
    assert(i>=0 && j>=0);
    p[i][j]=x;
};
```


Theorem:

Let D be a digraph with n vertices labeled $1, 2, \dots, n$, let A be its adjacency matrix with respect to this listing of the vertices, and let k be any positive integer.

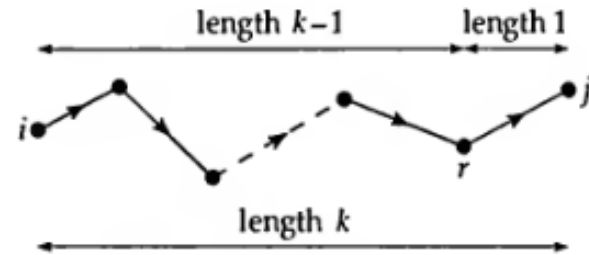
Then the number of walks of length k from vertex i to vertex j is equal to the entry in row i and column j of the matrix A^k (the k^{th} power of the matrix A).

Proof:

The proof is by mathematical induction on k , the length of the walk.

Step 1 The statement is true when $k = 1$, since the number of walks of length 1 from vertex i to vertex j is the number of arcs from vertex i to vertex j , and this is equal to a_{ij} the entry in row i and column j of the adjacency matrix A .

Step 2 We assume that $k > 1$, and that the statement is true for all positive integers less than k . We wish to prove that the statement is true for the positive integer k .



Consider any walk of length k from vertex i to vertex j . Such a walk consists of a walk of length $k - 1$ from vertex i to some vertex r adjacent to vertex j , followed by a walk of length 1 from vertex r to vertex j .

By our assumption, the number of walks of length $k-1$ from vertex i to vertex r is the entry in row i and column r of the matrix A^{k-1} , which we denote by $a_{ir}^{(k-1)}$.

Since the number of walks of length 1 from vertex r to vertex j is a_{rj} it follows that

the number of walks of length k from vertex i to vertex j via vertex r (at the

previous step) = $a_{ir}^{k-1} \times a_{rj}$ 17

So, the total number of walks of length k from vertex i to vertex j

=

**the number of such walks via vertex 1 (at the previous step)
+ the number of such walks via vertex 2 (at the previous step)
.....**

**+ the number of such walks via vertex r (at the previous step)
+ the number of such walks via vertex n (at the previous step),**

$$a_{i1}^{(k-1)} a_{1j} + a_{i2}^{(k-1)} a_{2j} + \dots + a_{ir}^{(k-1)} a_{rj} + \dots + a_{in}^{(k-1)} a_{nj}$$

By the rules for matrix multiplication, this is just the entry in row i and column j of the matrix $A^{k-1} A = A^k$, as required.

$$\begin{array}{c}
 \text{row } i \\
 \left[\begin{array}{cccc} a_{i1}^{(k-1)} & a_{i2}^{(k-1)} & \dots & a_{in}^{(k-1)} \end{array} \right] \\
 A^{k-1}
 \end{array}
 \begin{array}{c}
 \text{column } j \\
 \left[\begin{array}{c} a_{1j} \\ a_{2j} \\ \vdots \\ a_{rj} \\ \vdots \\ a_{nj} \end{array} \right] \\
 A
 \end{array}
 =
 \begin{array}{c}
 \text{column } j \\
 \left[\begin{array}{c} \vdots \\ a_{ij}^{(k)} \\ \vdots \end{array} \right] \\
 A^k
 \end{array}
 \begin{array}{c}
 \text{row } i
 \end{array}$$

Thus, if the statement is true for all positive integers less than k , then it is true for the integer k . This completes Step 2.

Therefore, by the principle of mathematical induction, the statement is true for all positive integers k .

Theorem

Let D be a digraph with n vertices labeled $1, 2, \dots, n$, let A be its adjacency matrix with respect to this listing of the vertices, and let B be the matrix

$$B = A + A^2 + \dots + A^{n-1}.$$

Then D is strongly connected if and only if each non-diagonal entry in B is positive - that is, $b_{ij} > 0$ whenever $i \neq j$.

Proof

There are two statements to prove.

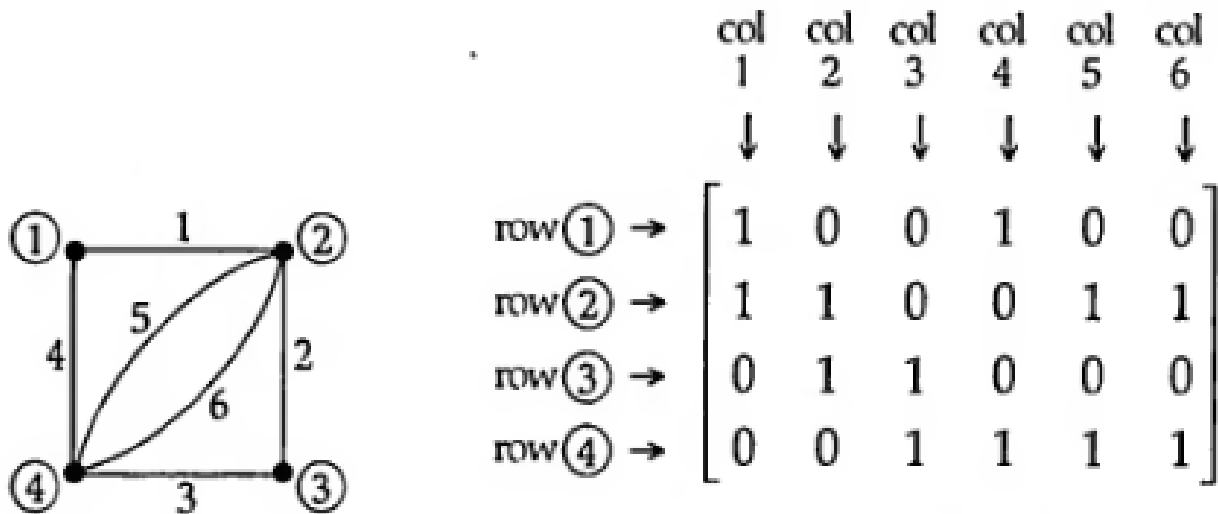
(a) If each non-diagonal entry in B is positive, then D is strongly connected.

Let D be a digraph that satisfies the given conditions, and suppose that each non-diagonal entry in B is positive - that is, $b_{ij} > 0$ whenever $i \neq j$ - then $A_{ij}^{(k)} > 0$ for some $k \leq n - 1$. Therefore there is a walk of length at most $n - 1$ from vertex i to vertex j whenever $i \neq j$, so the digraph D is strongly connected.

(b) If the digraph D is strongly connected, then each non-diagonal entry in B is positive.

Let D be a strongly connected digraph that satisfies the given conditions; then there is a path from any vertex to any other. Since D has n vertices, such a path has length at most $n - 1$. It follows that $a_{ij}^{(k)} > 0$ for at least one value of $k \leq n - 1$, and hence that the entry in row i and column j of B is positive; that is, $b_{ij} > 0$ whenever $i \neq j$.

Incidence Matrix

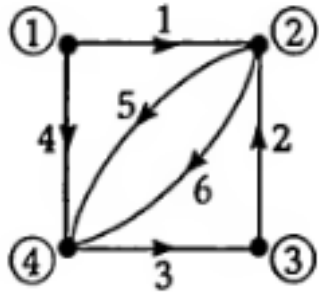


Definition

Let G be a graph without loops, with n vertices labeled u_1, u_2, \dots, u_n , and m edges labeled e_1, e_2, \dots, e_m .

The incidence matrix $I(G)$ of G is the $n \times m$ matrix in which the entry in row i and column j is

1 if the vertex i is incident with the edge j ,
0 otherwise.



	col 1	col 2	col 3	col 4	col 5	col 6
row ① →	1	0	0	1	0	0
row ② →	-1	-1	0	0	1	1
row ③ →	0	1	-1	0	0	0
row ④ →	0	0	1	-1	-1	-1

In the incidence matrix of a digraph without loops, each column has exactly one 1 and one -1, since each arc is incident from one vertex and incident to one vertex;

Definition

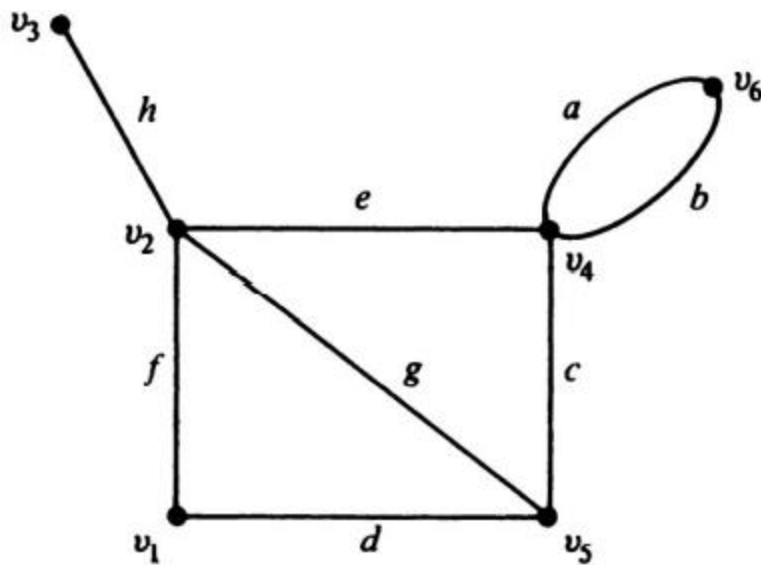
Let D be a digraph without loops, with n vertices labeled u_1, u_2, \dots, u_n and m arcs labeled e_1, e_2, \dots, e_m .

The incidence matrix $I(D)$ of D is the $n \times m$ matrix in which the entry in row i and column j is

- 1 if arc j is incident from vertex i ;
- 1 if arc j is incident to vertex i ;
- 0 otherwise.

the number of 1's in any row is the **out-degree** of the vertex corresponding to that row, and

the number of -1's in any row is the **in-degree** of the vertex corresponding to that row.



Find Incidence Matrix of the Graph...

- Parallel edges in a graph produce identical columns in its incidence matrix, e.g. columns 1&2
- Permutation of any two rows or columns in an incidence matrix simply corresponds to relabeling the vertices and edges of the same graph

	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>	<i>e</i>	<i>f</i>	<i>g</i>	<i>h</i>
v_1	0	0	0	1	0	1	0	0
v_2	0	0	0	0	1	1	1	1
v_3	0	0	0	0	0	0	0	1
v_4	1	1	1	0	1	0	0	0
v_5	0	0	1	1	0	0	1	0
v_6	1	1	0	0	0	0	0	0

→ Two graphs G_1 and G_2 are isomorphic iff their incidence matrices differ only by permutations of rows and columns

- If a graph G is **disconnected** and consists of two components g_1 and g_2 , the incidence matrix $A(G)$ of graph G can be written in a block-diagonal form as:

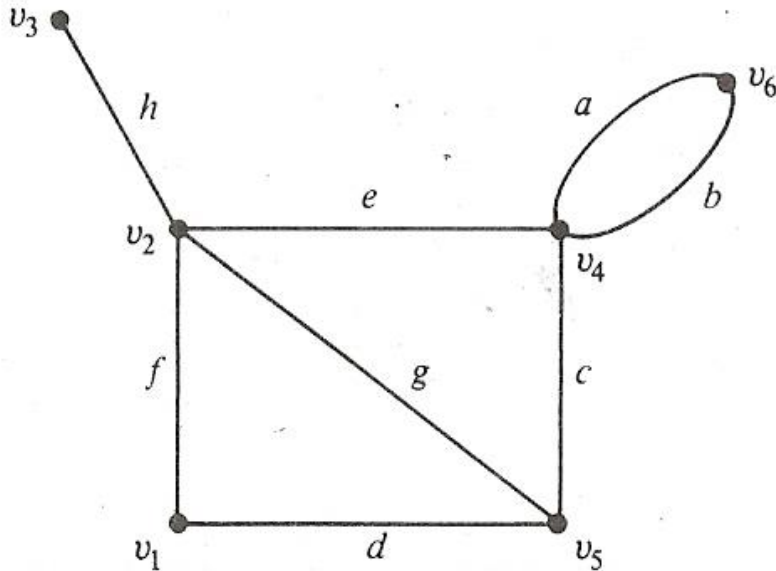
$$A(G) = \begin{bmatrix} A(g_1) & 0 \\ 0 & A(g_2) \end{bmatrix}$$

Where, $A(g_1)$ and $A(g_2)$ are the incidence matrices of components g_1 and g_2

→ for a disconnected graph with any number of components;
no edge in g_1 is incident on vertices of g_2 , *and vice a versa*
 → square submatrix of $A(G)$ is **nonsingular** iff the corresponding subgraph is a tree. The tree in this case is a **spanning tree**, because it contains $n - 1$ edges of the n -vertex graph

*// A **non-singular matrix** is a square one whose determinant is not zero //*

Circuit Matrix



If Graph G contains q different circuits and e edges then circuit matrix B is of size $q \times e$

$b_{ij} = 1$ if i^{th} circuit includes j^{th} edge
 $= 0$ otherwise

• Let $B(G)$ has 4 different circuits.

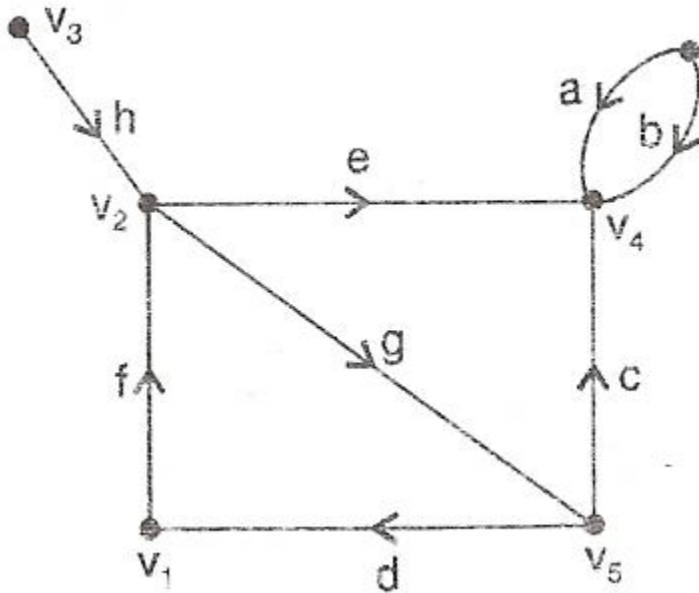
1: {a, b} 2: {c, e, g}

3: {d, f, g} 4: {c, d, f, e}

• Two graphs G_1 & G_2 have same circuit matrices iff G_1 & G_2 are 2-isomorphic (obtained after splitting & separation)

$$B(G) = \begin{matrix} & a & b & c & d & e & f & g & h \\ \begin{matrix} 1 \\ 2 \\ 3 \\ 4 \end{matrix} & \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 0 \end{bmatrix} \end{matrix}.$$

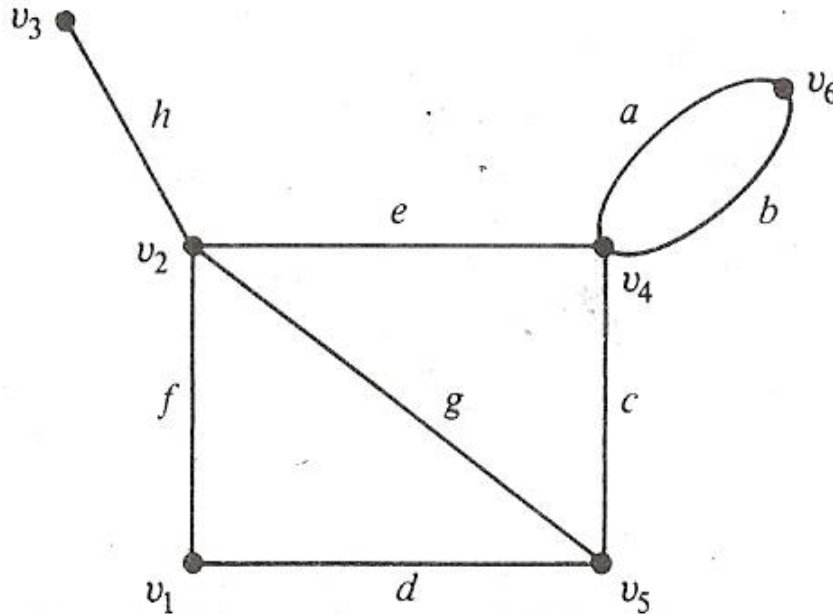
Circuit Matrix of a Digraph



$b_{ij} = 1$ if i^{th} circuit includes j^{th} edge & orientation of the edge and circuit coincide
 $= -1$ if i^{th} circuit includes j^{th} edge but the orientations of the two are opposite
 $= 0$ otherwise
 (circuit does not include edge)

$$\begin{matrix} & a & b & c & & d & e & & f & g & h \\ \begin{bmatrix} 0 & 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & -1 & 0 & 1 & 0 \\ 0 & 0 & 1 & -1 & -1 & -1 & 0 & 0 \\ -1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}
 \end{matrix}$$

Path Matrix



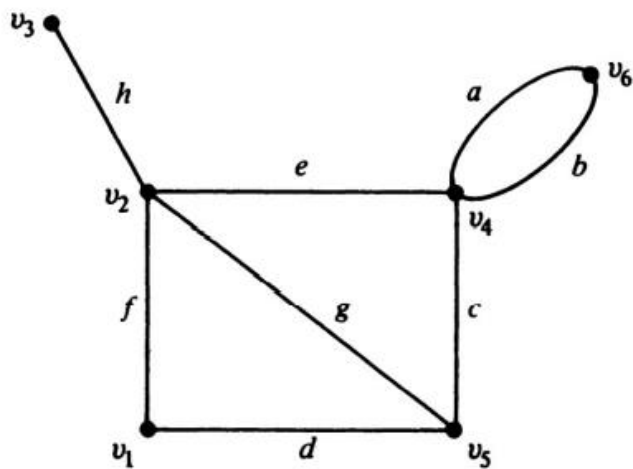
- A path matrix $P(x,y)$ indicates path between pair of vertices x & y
- The rows in $P(x,y)$ corresponds to different paths in vertex x & y
- The columns correspond to the edges in G & $P(x,y) = [p_{i,j}]$
- $p_{i,j} = 1$ if j th edge lies in the i th path
 $= 0$ otherwise

• For $P(v_3, v_4)$ 3 different path exist

• Let path 1: $\{h, e\}$ 2: $\{h, g, c\}$
 & 3: $\{h, f, d, c\}$

• The ring sum of any two rows in $P(x,y)$ corresponds to a circuit or an edge-disjoint union of circuits.

$$P(v_3, v_4) = \begin{matrix} & a & b & c & d & e & f & g & h \\ \begin{matrix} 1 \\ 2 \\ 3 \end{matrix} & \begin{bmatrix} 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 1 & 0 & 1 & 0 & 1 \end{bmatrix} \end{matrix}$$



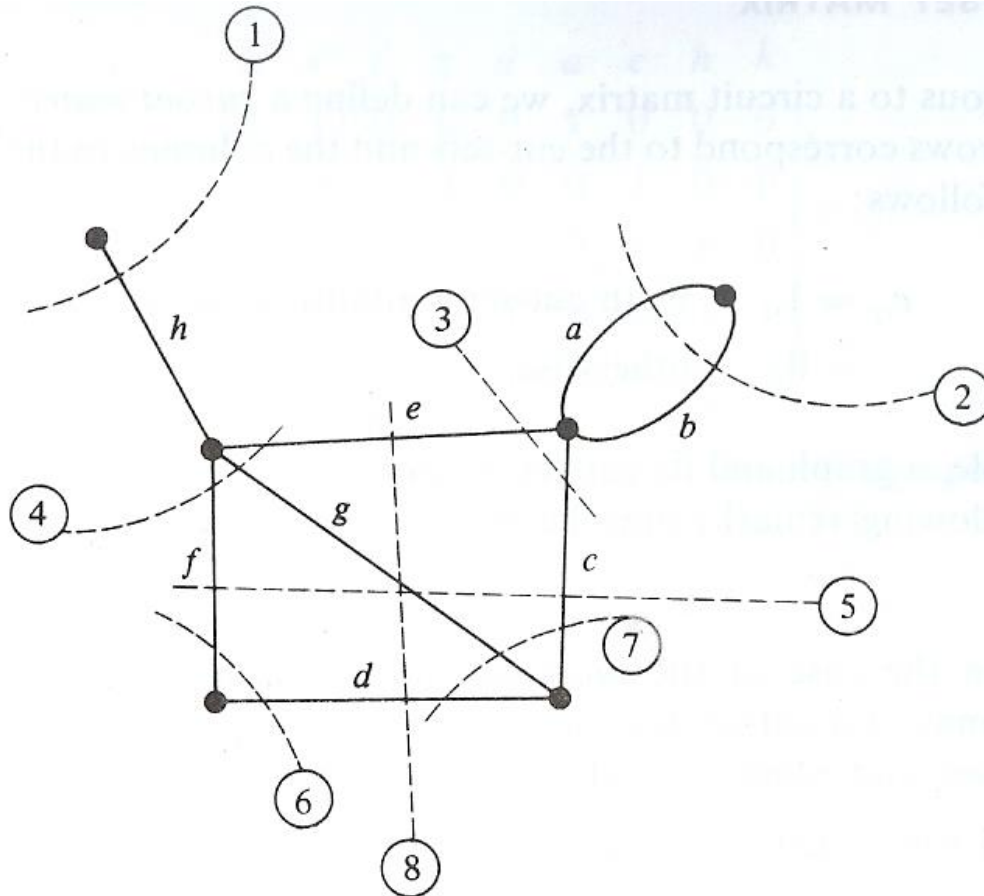
- Let B and A be, respectively, the circuit matrix and the incidence matrix (of a self-loop-free graph) whose columns are arranged using the same order of edges. Then every row of B is **orthogonal** to every row A ; that is,

$$A \cdot B^T = B \cdot A^T = 0 \pmod{2}$$

$$\begin{aligned}
 A \cdot B^T &= \begin{bmatrix} 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 1 & 1 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} 1 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 1 & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix} \\
 &= \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix} \pmod{2}.
 \end{aligned}$$

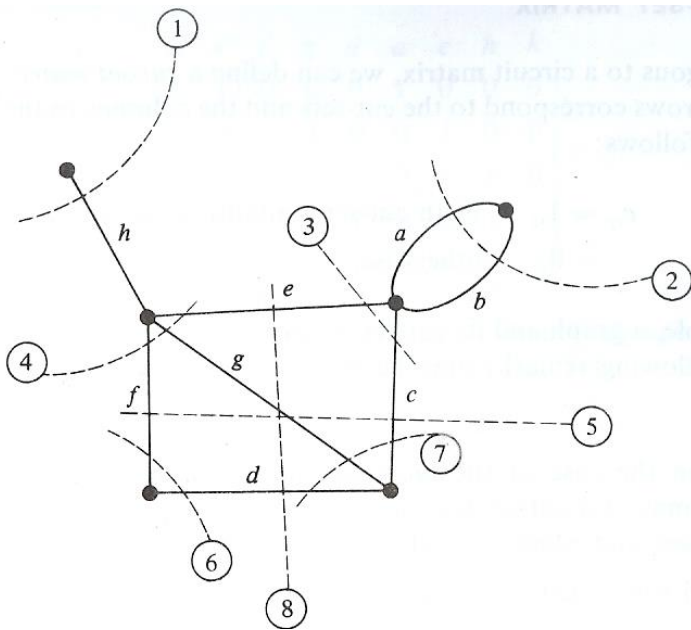
Multiply the incidence matrix and transposed circuit of the graph making sure that the edges are in the same order in both

Cut-set Matrix



- $C=[c_{i,j}]$; Rows correspond to cut-sets; columns to the edges
- $c_{i,j} = 1$ if i th cut-set contains j^{th} edge
= 0 otherwise

Cut-set Matrix



$$C = \begin{matrix} & \begin{matrix} a & b & c & d & e & f & g & h \end{matrix} \\ \begin{matrix} 1 \\ 2 \\ 3 \\ 4 \\ 5 \\ 6 \\ 7 \\ 8 \end{matrix} & \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 0 & 1 & 0 \end{bmatrix} \end{matrix}$$

Significance

Why Graphs in Matrix Representations?

- **Handling Large computational** problems
- Practically impossible for hand computation
- Programming attempts to handle problems like PERT, flow problems, transportation n/w, electrical n/w, circuit n/w etc.

e.g. Travelling Salesman Problem

Travelling Salesman Problem_Analysis

- Problem of finding lowest-weight Hamiltonian Circuit : weighted complete graph with n vertices
- There are $(\frac{1}{2} * (n-1)!)$ different circuits so as solutions.

- Brute Force analysis for 10 vertex graph:

$$\text{Total circuits} = (\frac{1}{2} * (n-1)!) = (\frac{1}{2} * (10-1)!) = 181,440$$

- For 20 vertex graph:

$$\text{Total circuits} = (\frac{1}{2} * (n-1)!) = (\frac{1}{2} * (20-1)!) \approx 6 * 10^{16}$$

- Even with nano instructional operations time required \approx **2 years**

Analysis

- Manipulation & analysis of graphs is **essentially non_numerical**
- Graph Theoretic algorithms & programs use **decision making ability** of the computer rather than ability to perform arithmetic operations
- Features of Algorithm:
 - Finiteness
 - Definiteness
 - i/p
 - o/p
 - effectiveness

Efficiency of Algorithms

- Efficiency in terms of memory & computation time
- Function of the size of the input
- Input is graph
- Size: $f(n, e)$
- Evaluation with “Worst case” execution time

INPUT:

Computer Representation of a Graph

A) Adjacency Matrix:

- Most popular form
- $n \times n$ binary matrix during i/p, process & o/p
- Hence require n^2 bits
- Each row of matrix with $\lceil n/w \rceil$ m/c words
(w is the word length)
- Total number of words = $n \times \lceil n/w \rceil$
- for symmetric matrix bits for storage = $n(n-1)/2$ but
increases computation time

INPUT:

Computer Representation of a Graph

B) Incidence Matrix:

- Uses $n \cdot e$ bits of storage
- Normally $e > n$: hence $n \cdot e > n^2$
- Favored in electrical & switching n/w

INPUT:

Computer Representation of a Graph

C) Edge Listing: (e.g. diagraph)

- Arbitrary order can be allowed
- Parallel edges & loops can be represented unlike adjacency matrix
- The number of bits required to label each vertex is b :
where $(2^{b-1} < n \leq 2^b)$
- Each edge requires storing two vertices; hence total storage
= $2e.b$ bits
- More economical than adjacency matrix if : $2e.b < n^2$
- If adjacency matrix of a graph is *sparse*; edge listing is more efficient approach (A mat. with many 0 elements is called as sparse mat. A` sparse adjacency mat. Implies a small e/n ratio)
- However, edge listing input style cause difficulty (extensive search techniques) in storage, retrieval & manipulation of the graph

INPUT:

Computer Representation of a Graph

D) Two Linear Arrays:

- Slight variation in edge listing
- Graph with two linear arrays:

$$F=(f_1, f_2, \dots, f_e) \text{ \& } H=(h_1, h_2, \dots, h_e)$$

- *e.g.*
- The i^{th} edge e_i is from vertex f_i to vertex h_i
- Convenient sorting in **weighted graphs**
- Storage requirement is same as edge listing

INPUT:

Computer Representation of a Graph

E) Successor Listing:

- Efficient method in graphs in which **ratio e/n is not large** is by means of n linear arrays
- vertices can be arranged in any order $1, 2, \dots, n$
- Each vertex k by linear array, whose first element is k & remaining are the successors of k
- Let **d_{av} be the average out degree** of the graph; if one word per vertex;
total storage for n vertex graph is: **$n(1 + d_{av})$** words
- Successor listing is more efficient than adjacency mat. If :
 $(d_{av} < (\lceil n/w \rceil) - 1)$
- This form of the i/p is extremely convenient for path finding algorithms & for **depth first search of a graph**.

Comment....forward..

- Graph representation forms are not entirely different; but conveys same information
- Efficiency of algorithm is dependant on the **form of the i/p**
- Proper choice of the data structure is important
- The o/p varies with problem (giving o/p in the form of sub_graph, yes/no, distance... etc...)