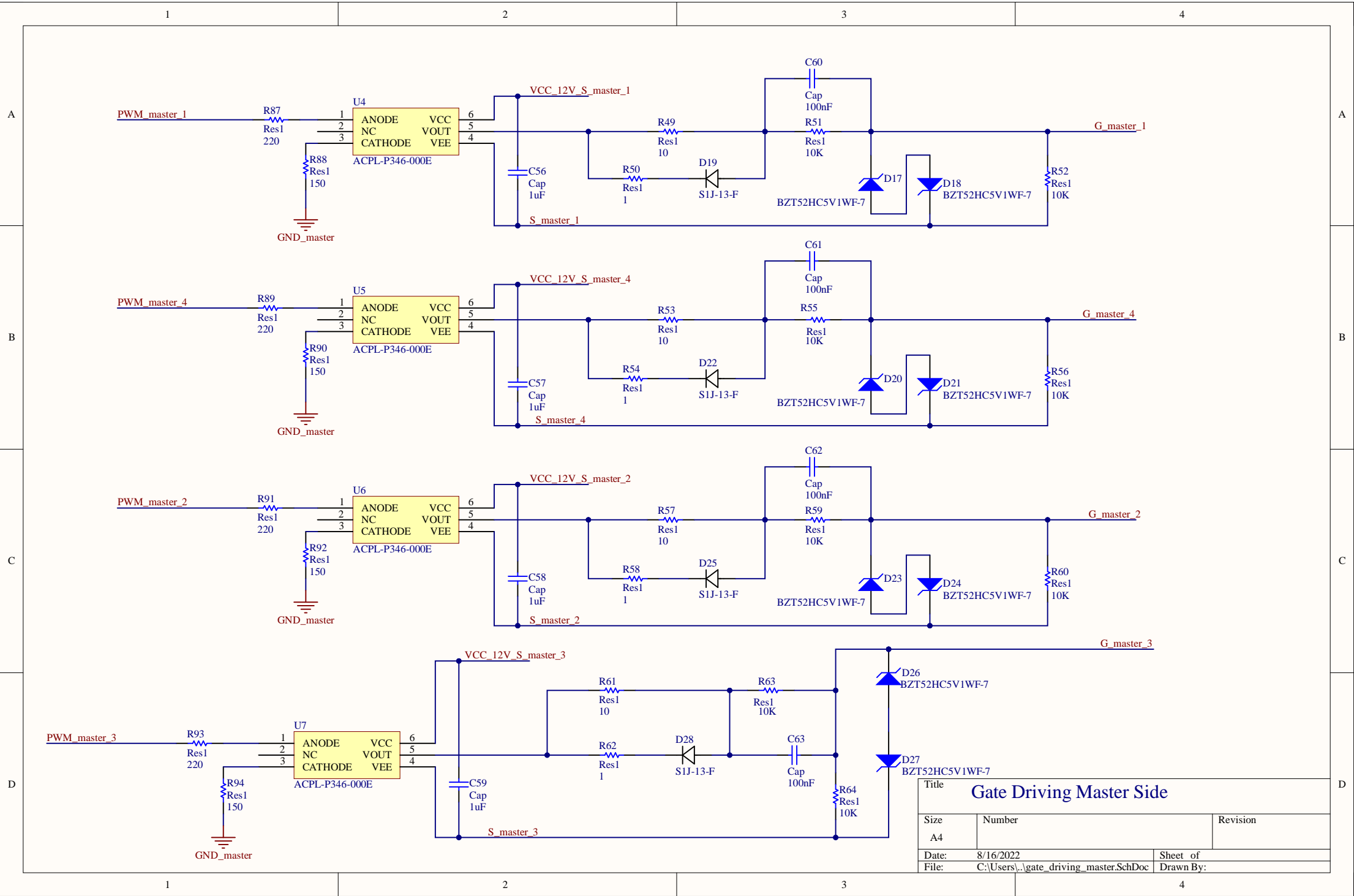
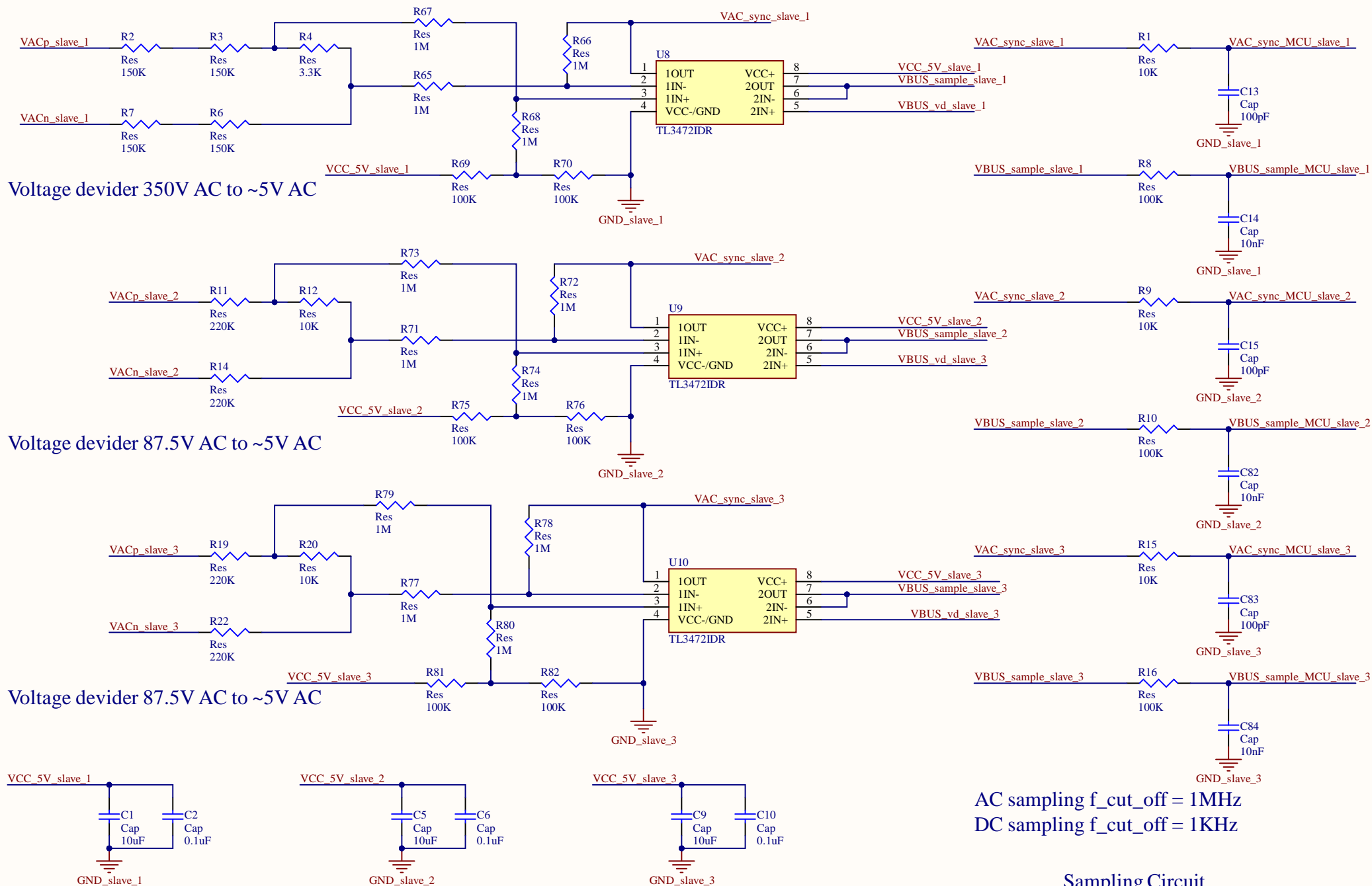
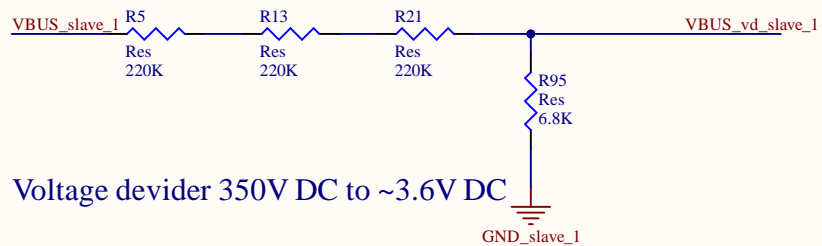


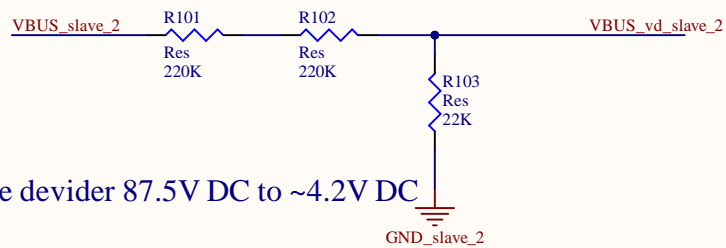
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Size	Number		Revision	
A4				
Date:	8/16/2022		Sheet of	
File:	C:\Users\...\main_power_circuit.SchDoc		Drawn By:	



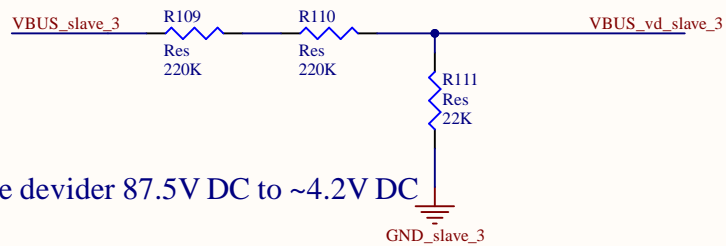




Voltage divider 350V DC to ~3.6V DC



Voltage divider 87.5V DC to ~4.2V DC



Voltage divider 87.5V DC to ~4.2V DC

Title		
DC Voltage Divider		
Size	Number	Revision
A4		
Date:	8/16/2022	Sheet of
File:	C:\Users\...\voltage_sampling.SchDoc	Drawn By:

A

B

C

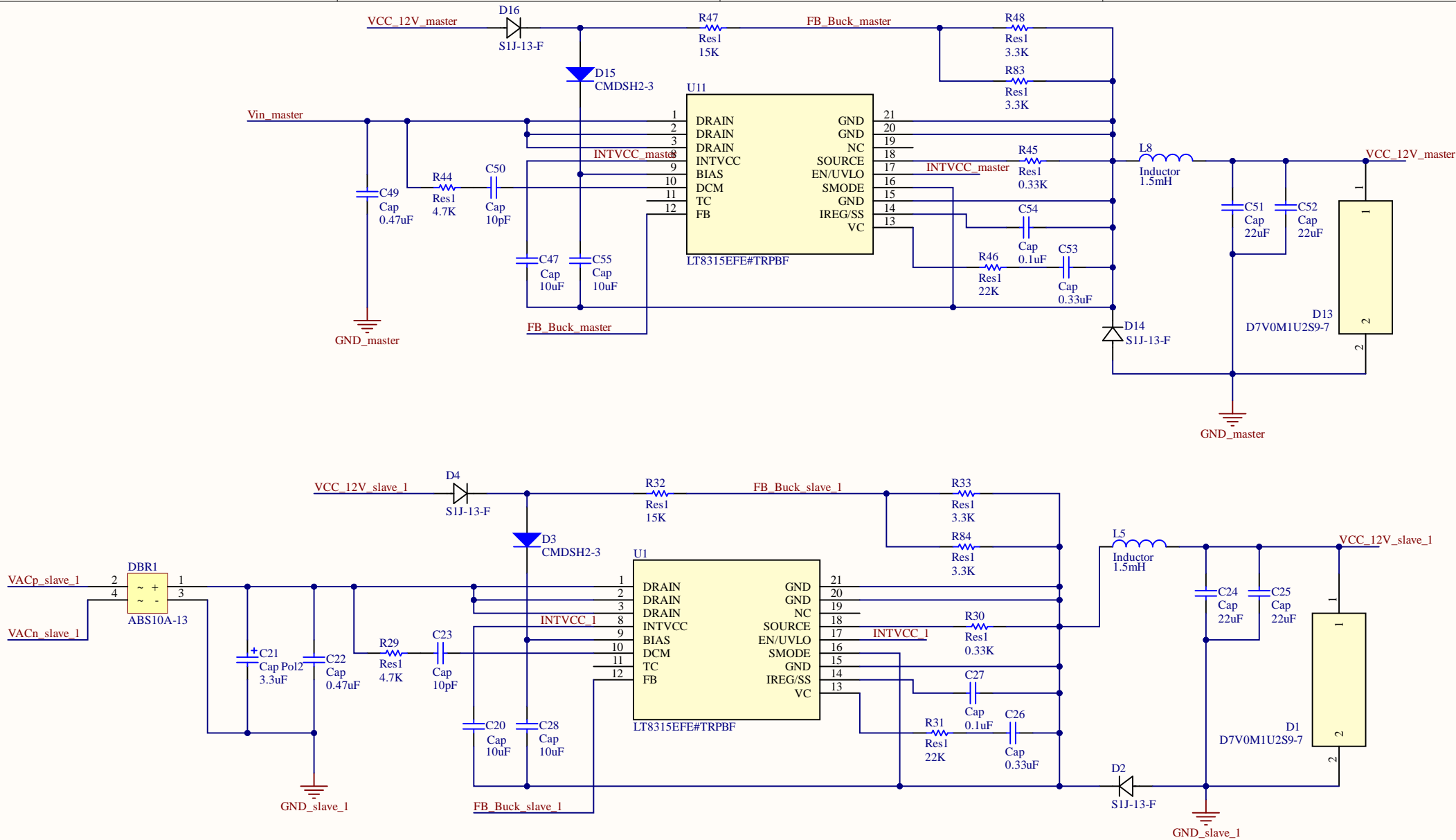
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A

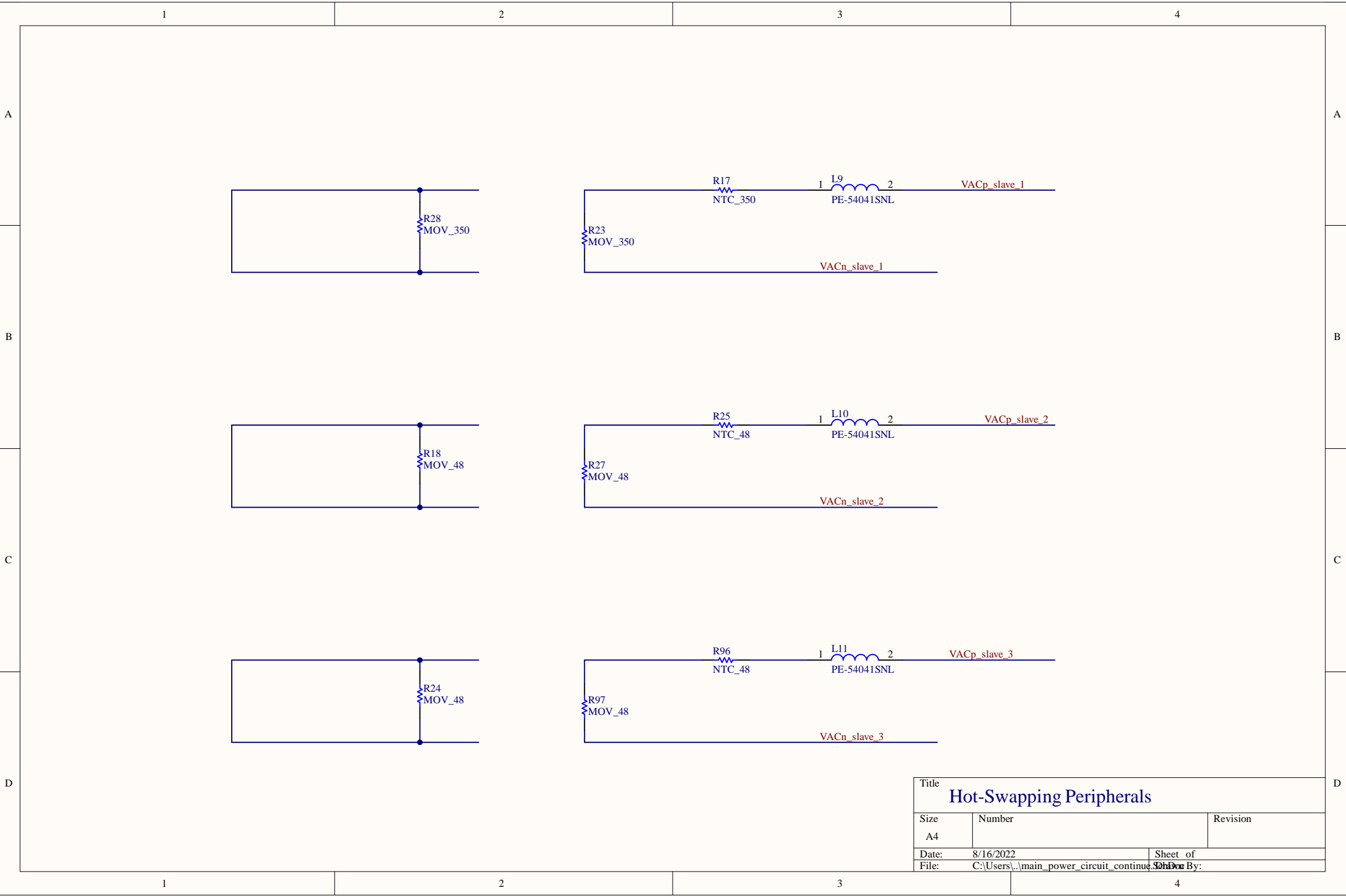
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C

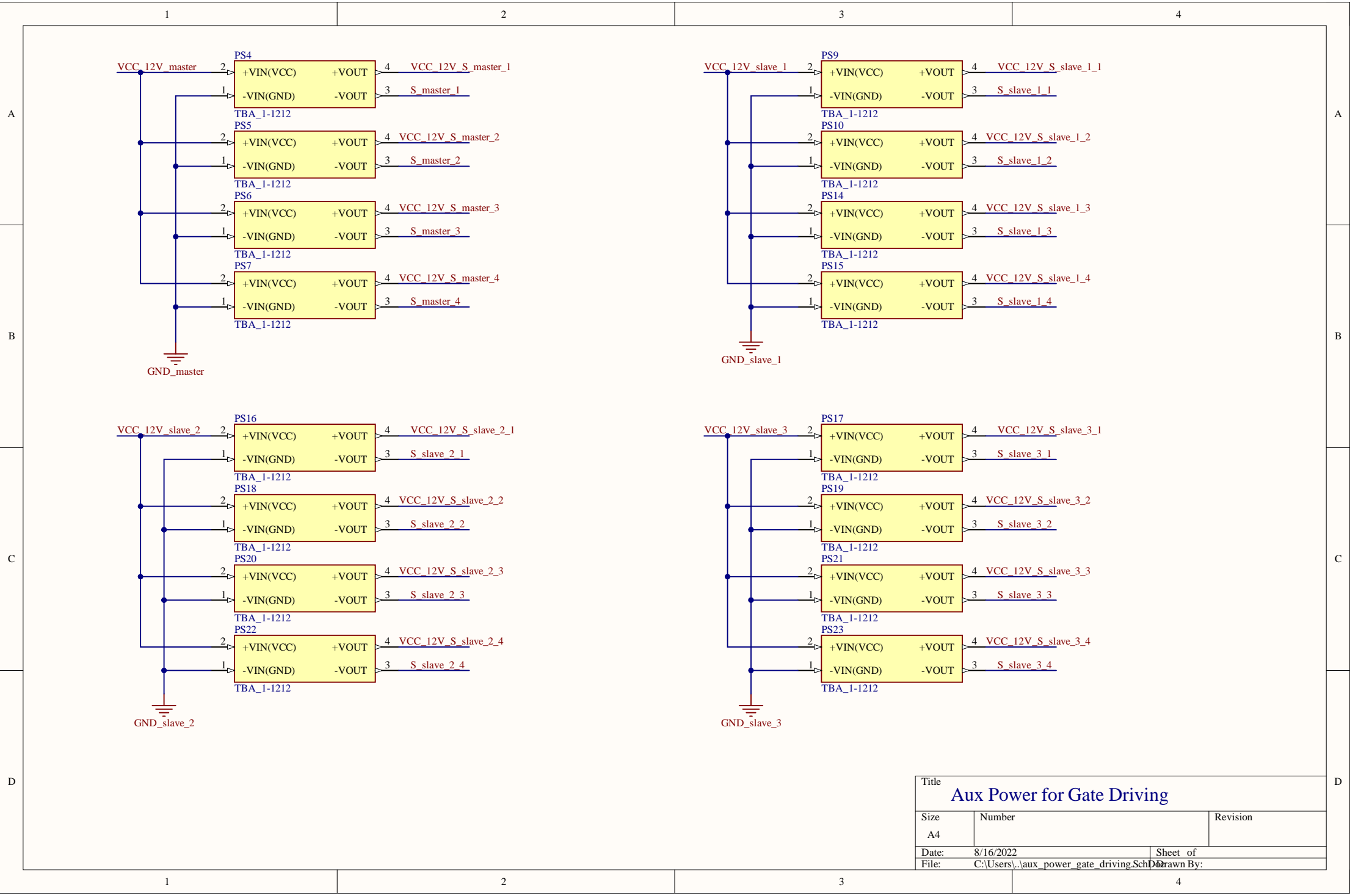
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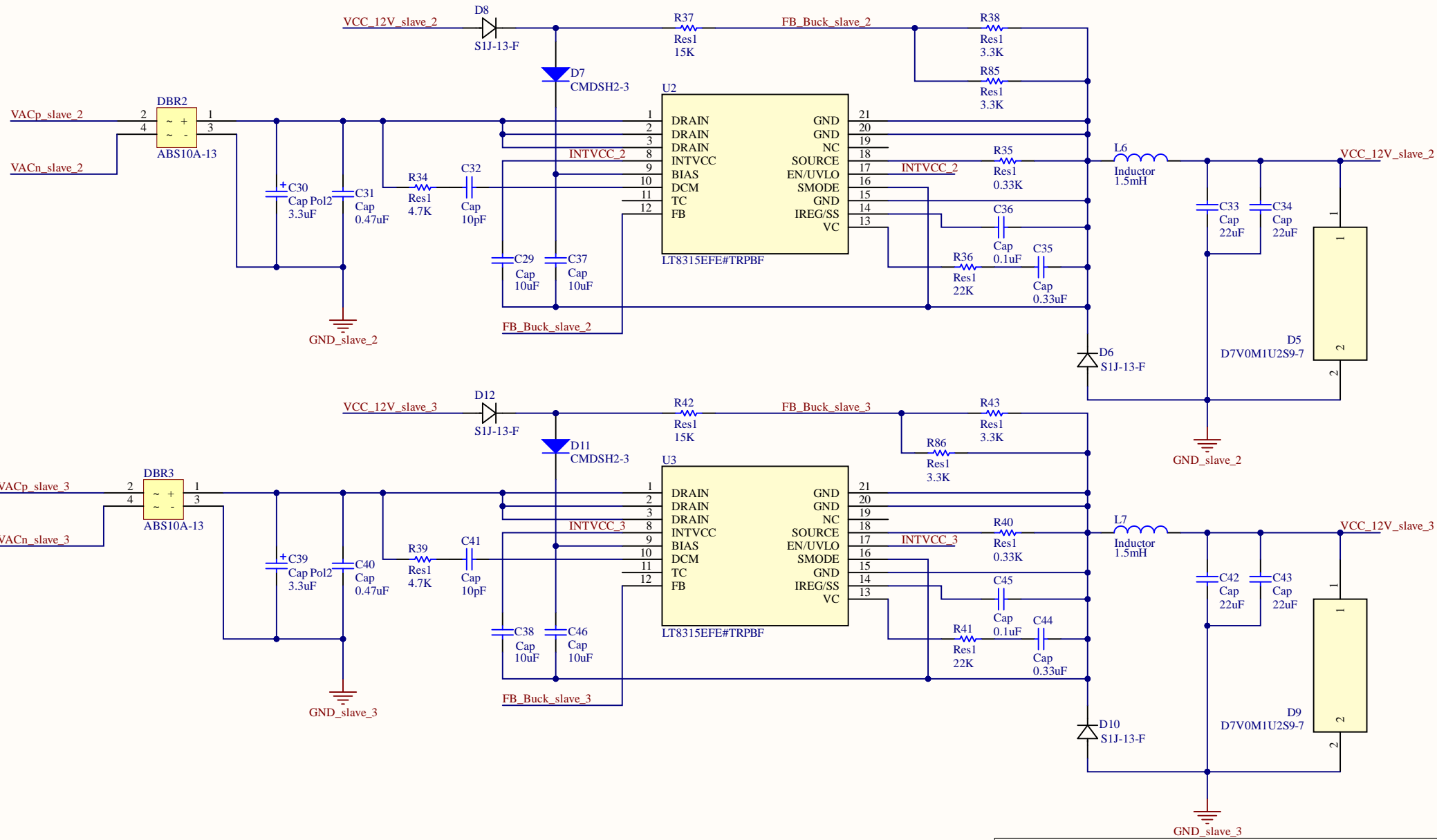
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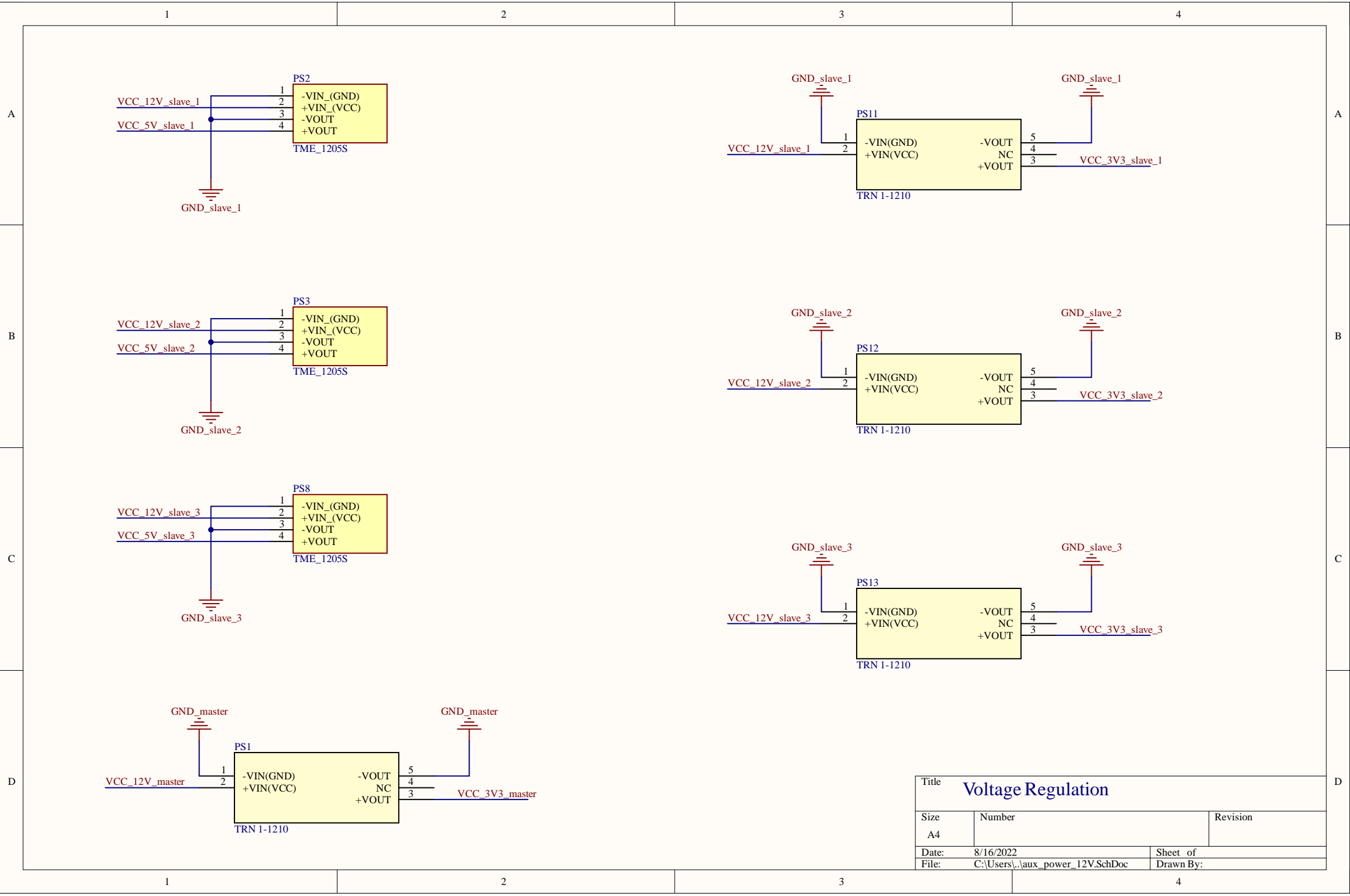
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Size	Number	Revision
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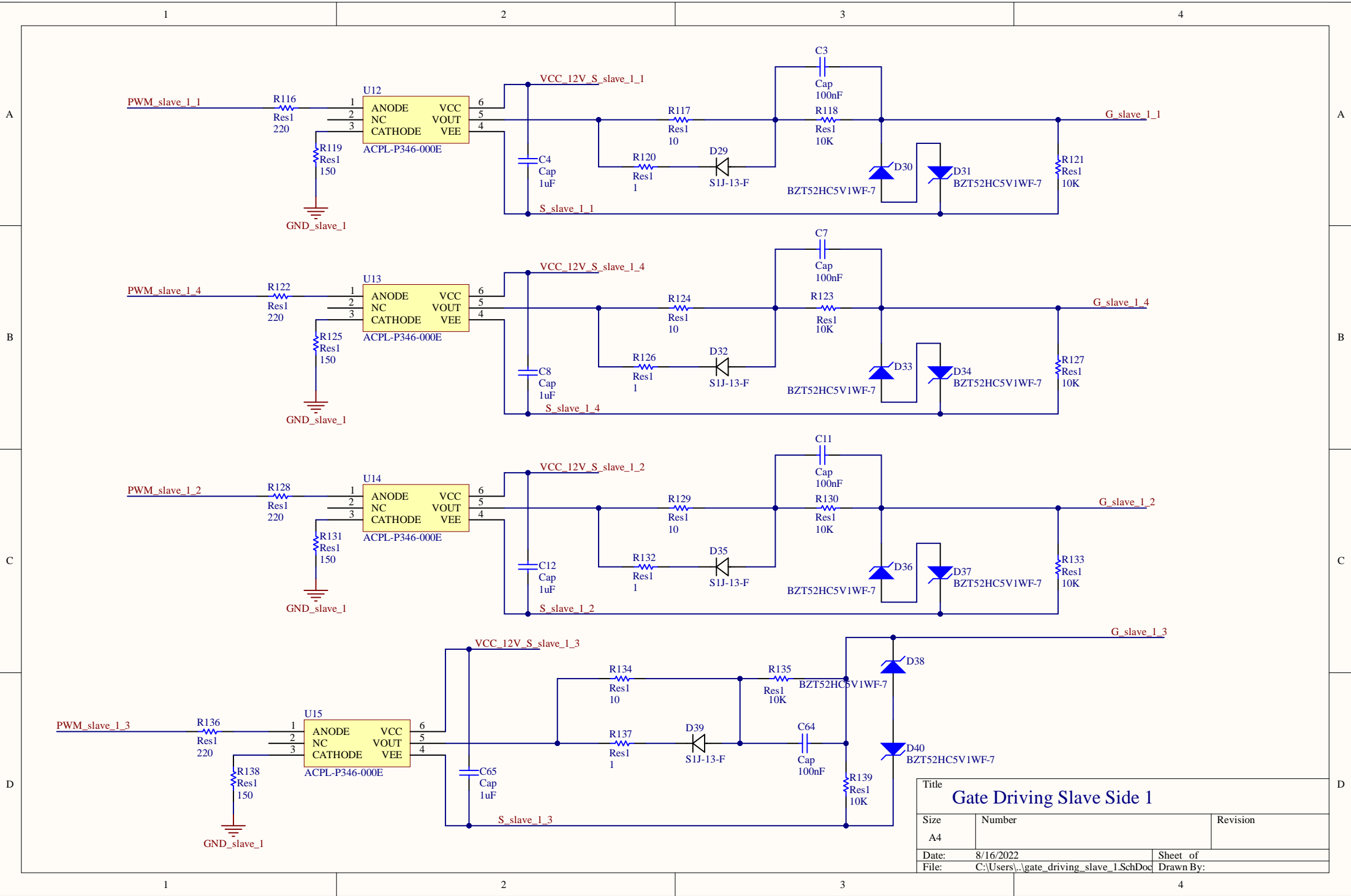


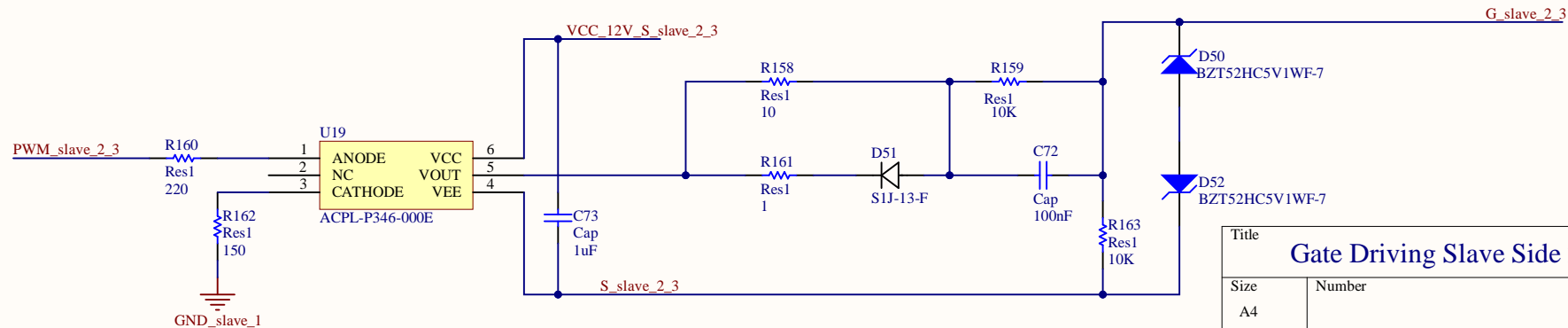
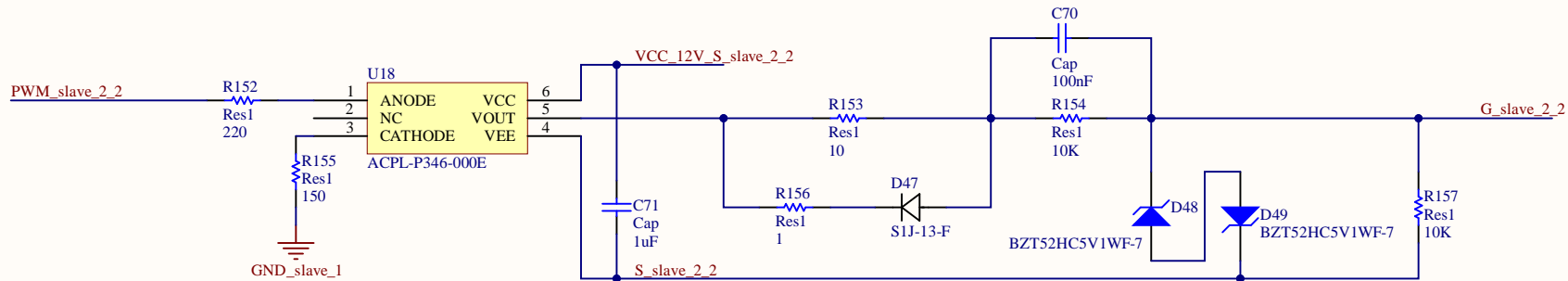
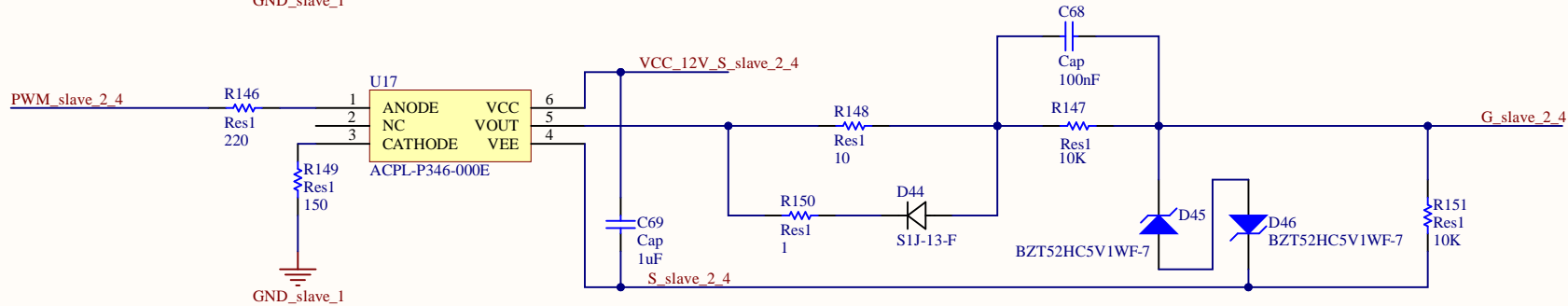
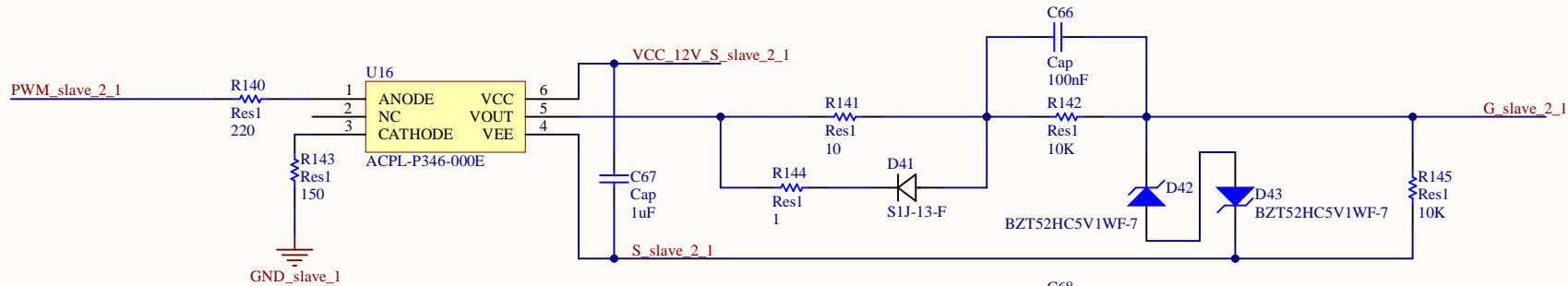
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Size	Number	Revision
A4		
Date:	8/16/2022	Sheet of
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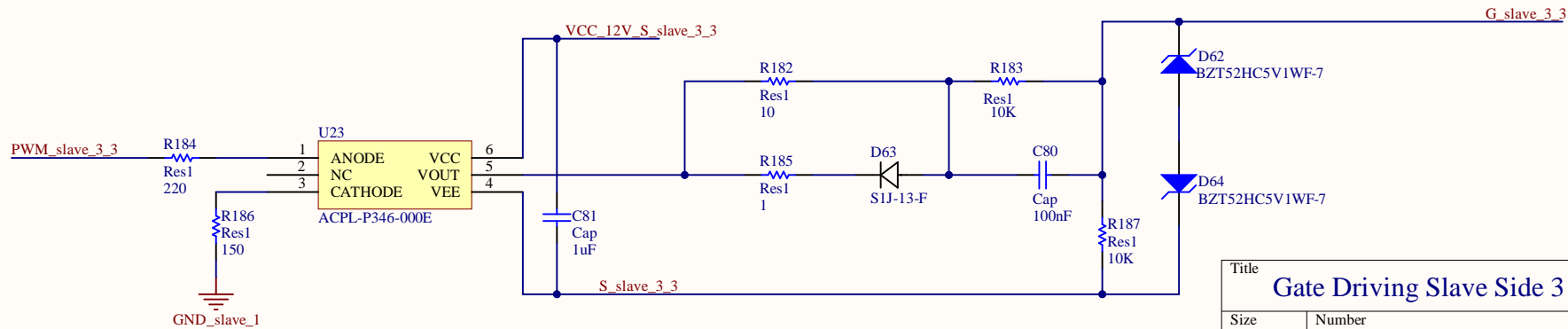
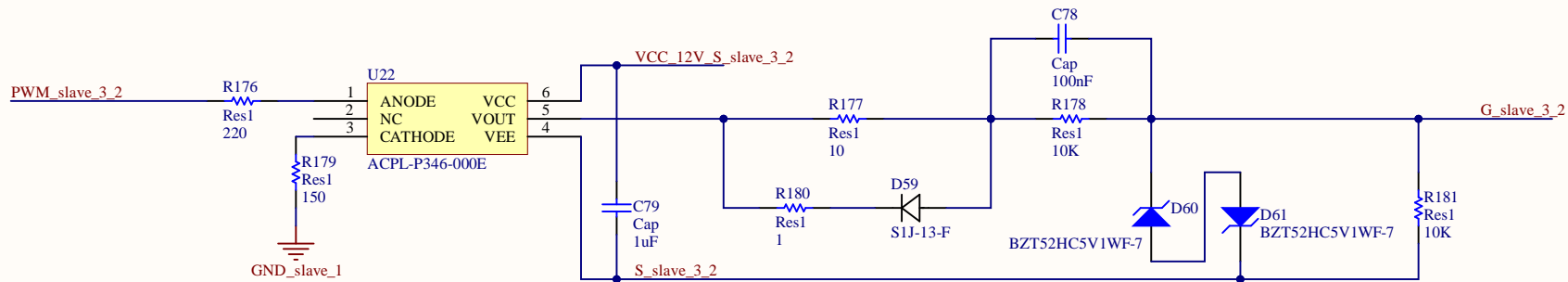
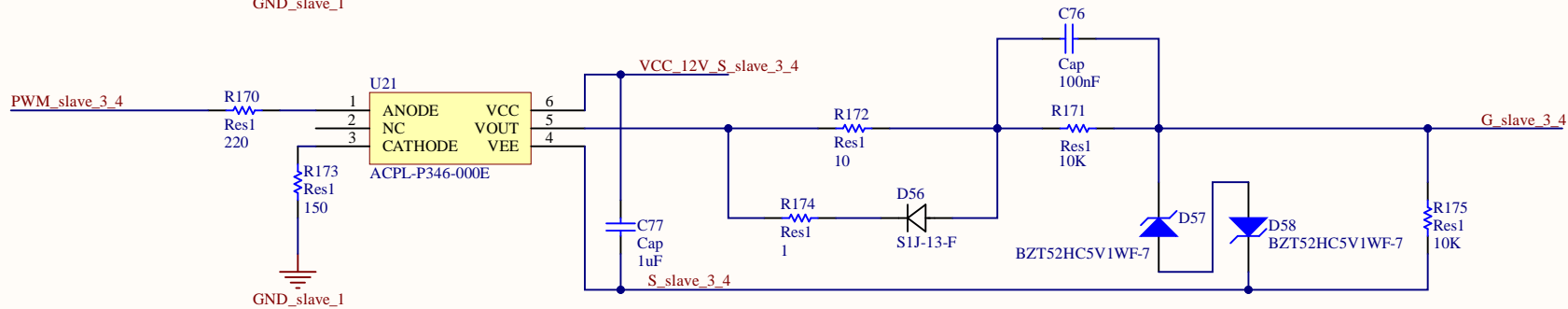
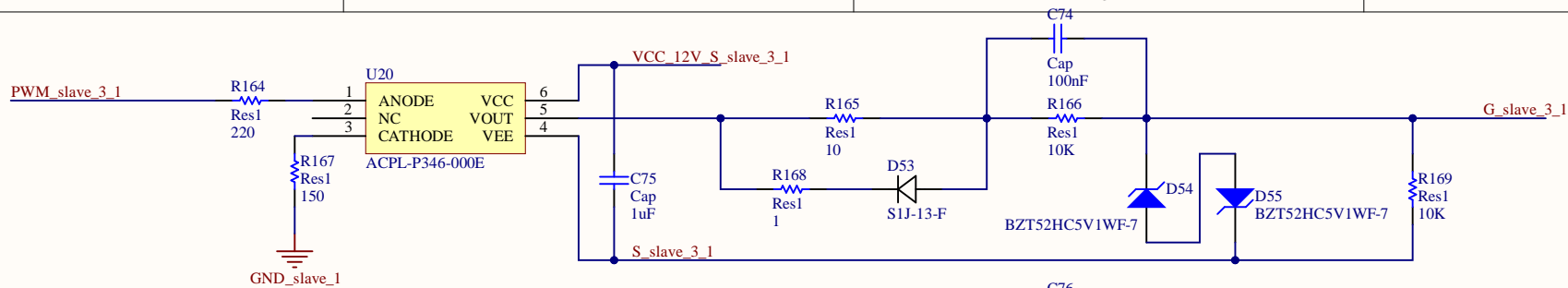
Title		
Aux Power Low Voltage Ports		
Size	Number	Revision
A4		
Date:	8/16/2022	Sheet of
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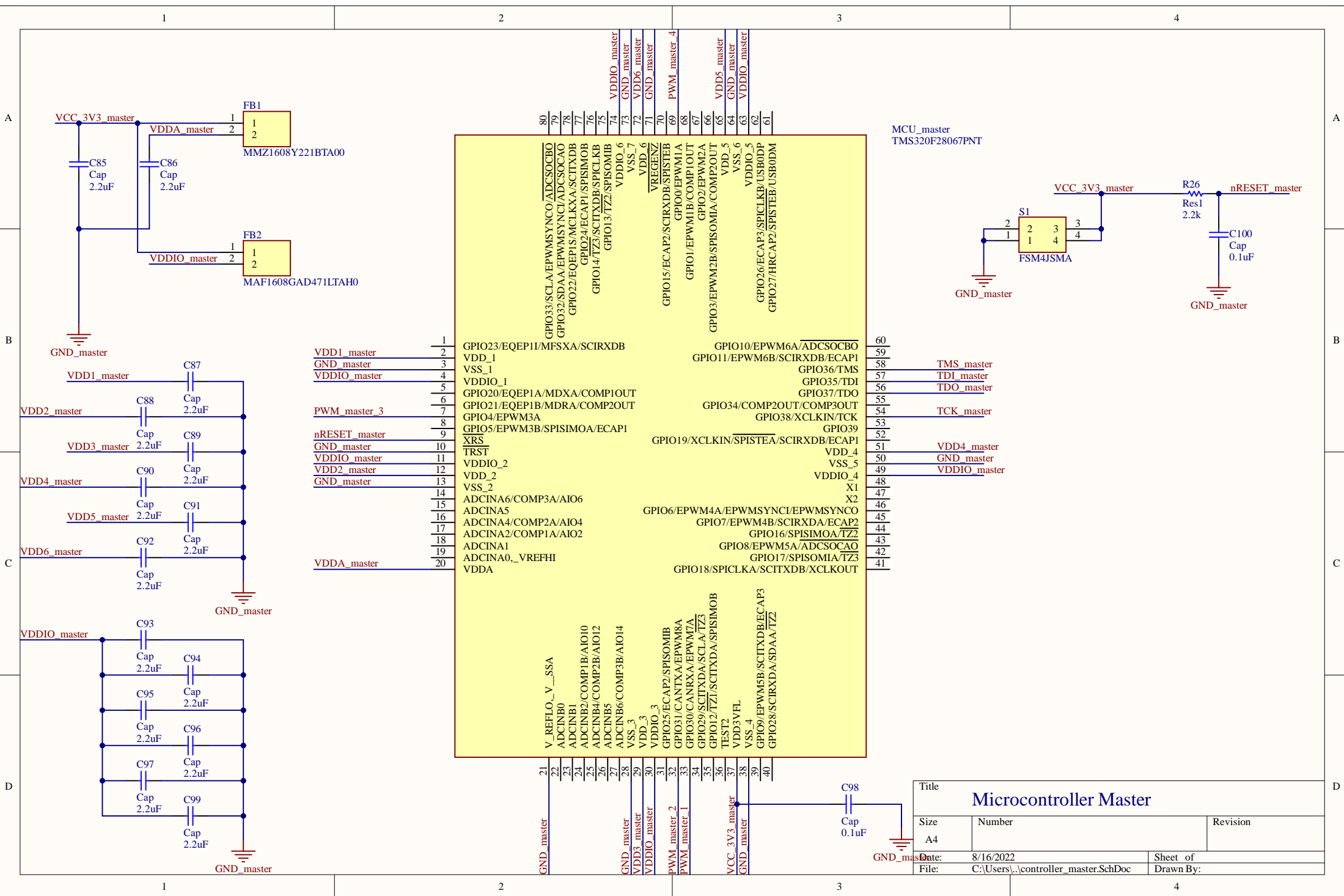


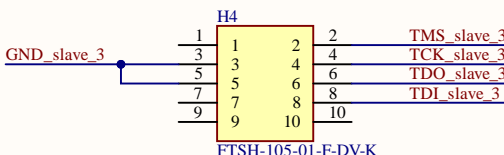
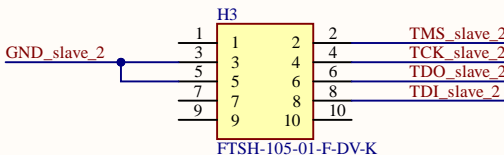
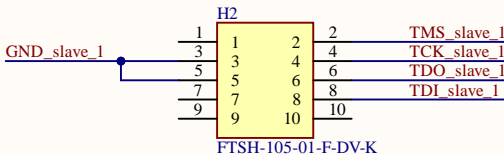
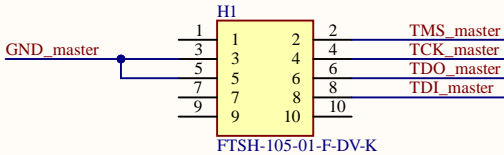


Title		
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Size	Number	Revision
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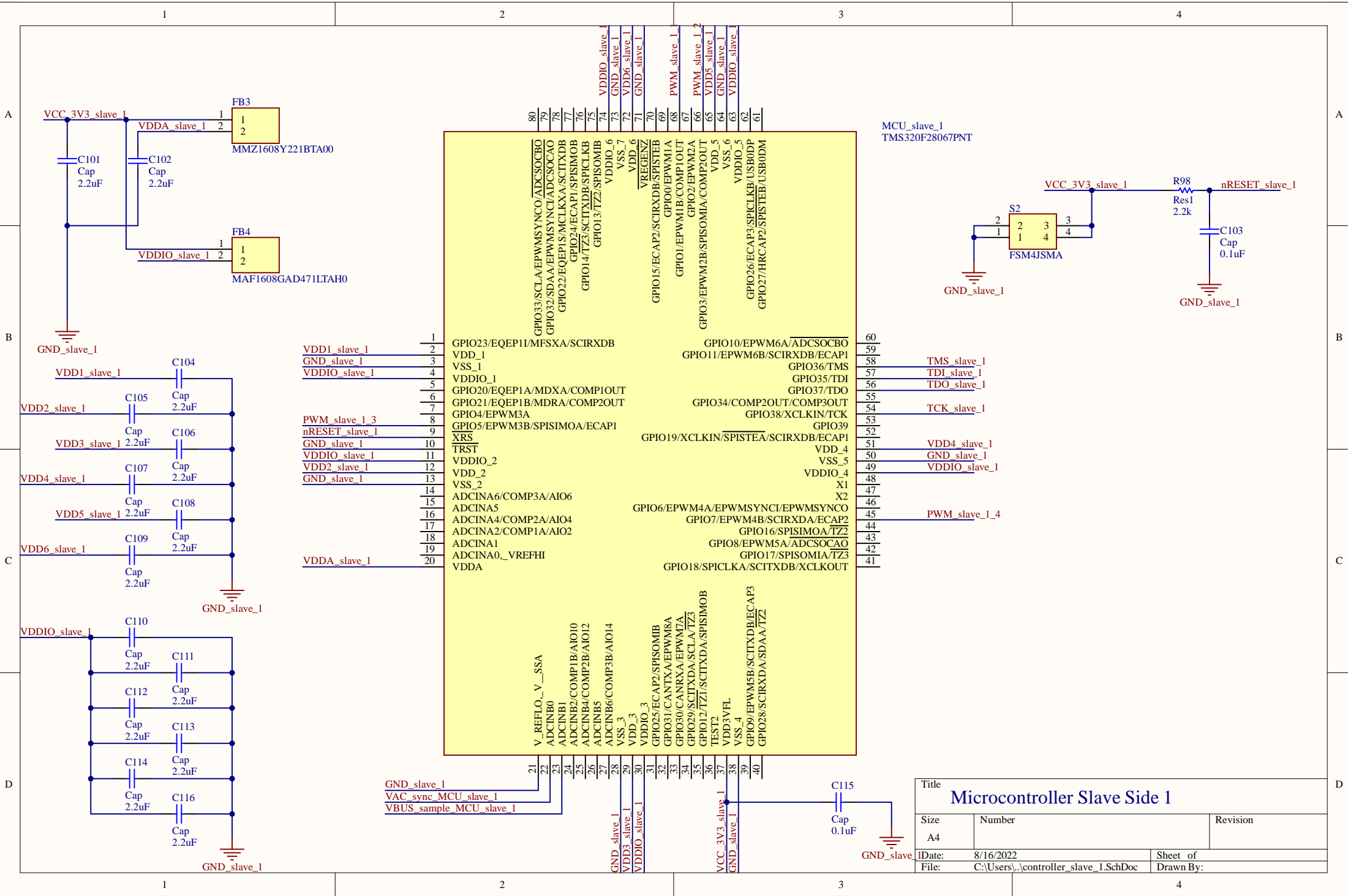


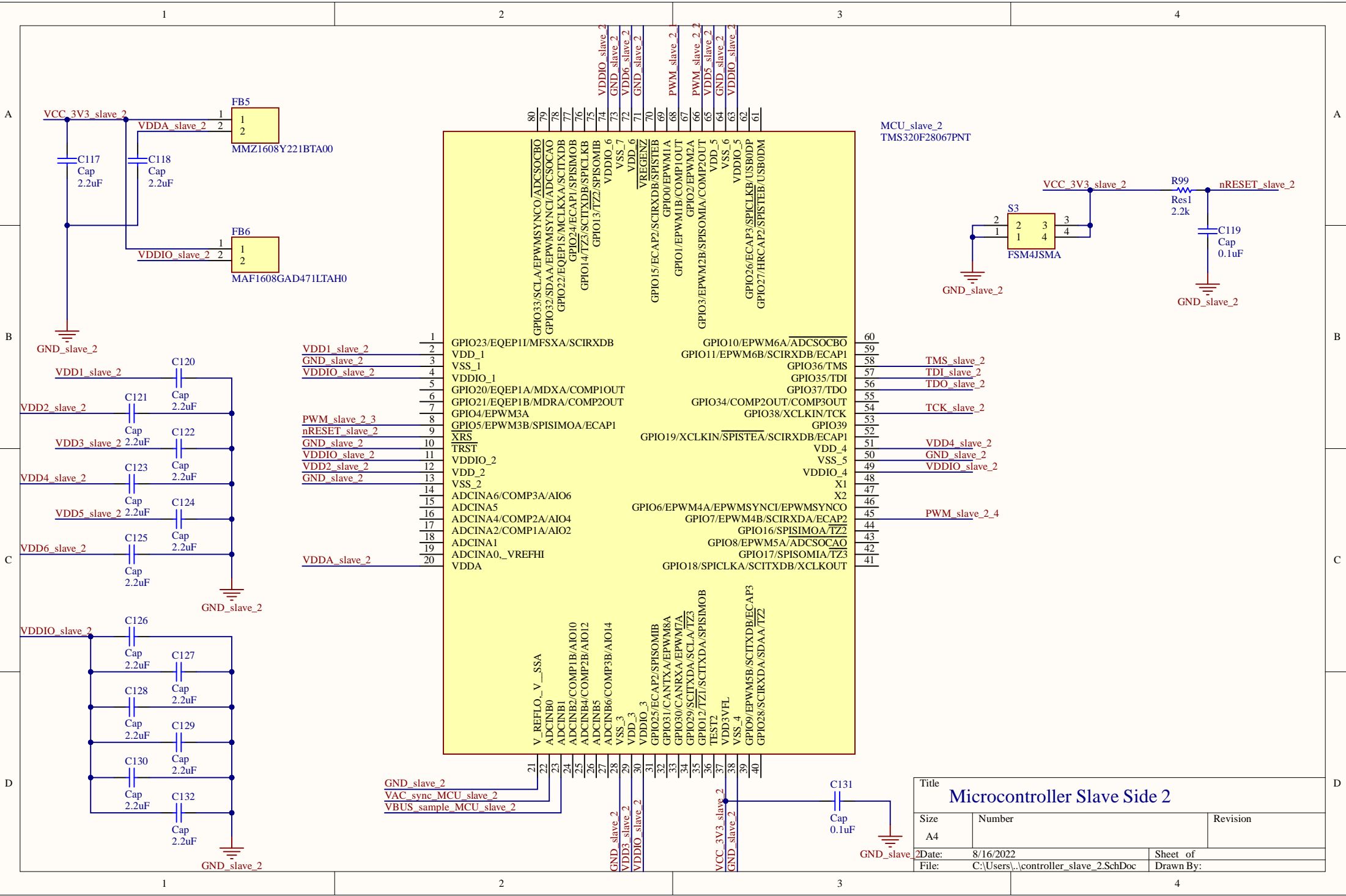
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Size	Number	Revision
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Date:	8/16/2022	Sheet of
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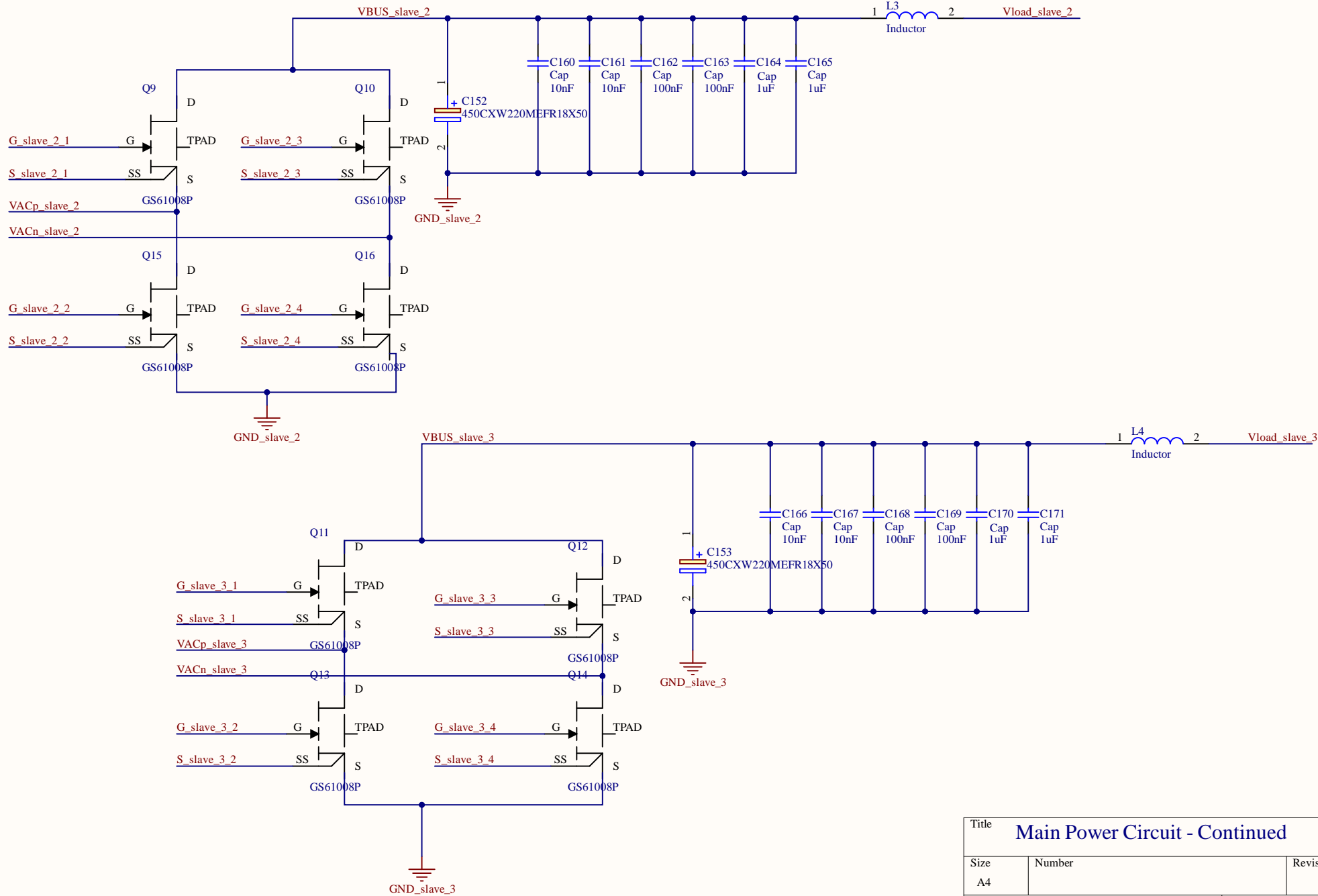


Title			JTAG Connectors
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Date:	8/16/2022	Sheet of	
File:	C:\Users\...\JTAG_connector.SchDoc	Drawn By:	





Title		
Microcontroller Slave Side 2		
Size	Number	Revision
A4		
2Date:	8/16/2022	Sheet of
File:	C:\Users\...\controller_slave_2.SchDoc	Drawn By:



Title		
Main Power Circuit - Continued		
Size	Number	Revision
A4		
Date:	8/16/2022	Sheet of
File:	C:\Users\main_power_circuit_continued	Drawn by:

