

## An Approach to Test the Open-Loop Parameters of Feedback Amplifiers

G. Giustolisi and G. Palumbo

**Abstract**—A novel method for determining open-loop parameters, such as unity-gain frequency and phase margin, in feedback amplifiers is given. It is based on relations between open-loop and closed-loop configurations and is very useful for evaluating the above parameters in a unity-gain amplifier from a simple measure of its closed-loop 3-dB cutoff frequency and the respective phase for a given feedback factor,  $\beta$ . The approach is validated by simulations of an ideal amplifier and of a real OTA. Moreover, measures carried out for a  $\mu\text{A-741}$  operational amplifier show a percentage error between real and calculated values lower than 2%.

**Index Terms**—Close-loop systems, feedback amplifiers, frequency measurement, open-loop systems, phase measurement.

### I. INTRODUCTION

The commonly used parameters, which describe the stability and the bandwidth of a feedback amplifier, are the phase margin and the unity-gain frequency in open-loop configuration [1]–[4].

Unfortunately, their measurement can present difficulties in many cases, especially when the amplifier under test is a low-voltage, high-gain amplifier. Indeed, due to the high gain, the input signal must be kept low in order to get the output signal level less than the power supply. At the same time, the input signal must be higher than the equivalent input noise level, which grows with the decreasing of bias currents. Obviously, if the noise level multiplied by the amplifier gain is greater than the power supply value, we cannot have a direct measure of the amplifier open-loop parameters easily.

Several methods have been proposed to evaluate the phase margin and the unity-gain frequency of an amplifier both in the frequency and in the time domain [5]. In particular, [6] suggests achieving the desired parameters measuring the overshoot in the time domain. In [7], [8] strategies to measure the open-loop gain of an amplifier directly in the frequency domain are given. The approach in [9] is based on the implementation of an oscillator as test stage. Finally, [10] proposes to measure the phase of the amplifier in unity gain configuration and does not require external components.

This paper presents a novel and accurate method for the determination of the phase margin and the unity-gain frequency of an amplifier. Considering the achieved accuracy, the method is simpler with respect to previous approaches and has also the advantage to allow an easy measurement of such low-voltage IC amplifiers which cannot be closed in unity-gain configuration [11], [12]. The approach is based on the measure of the 3-dB cutoff frequency and its respective phase value for a given feedback factor. Moreover, the same measure gives us information on the same parameter values of the amplifier in unity-gain feedback configuration.

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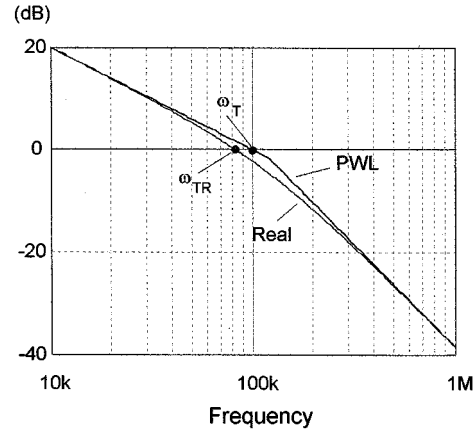


Fig. 1. Frequency gain magnitude: Real and PWL approximation.

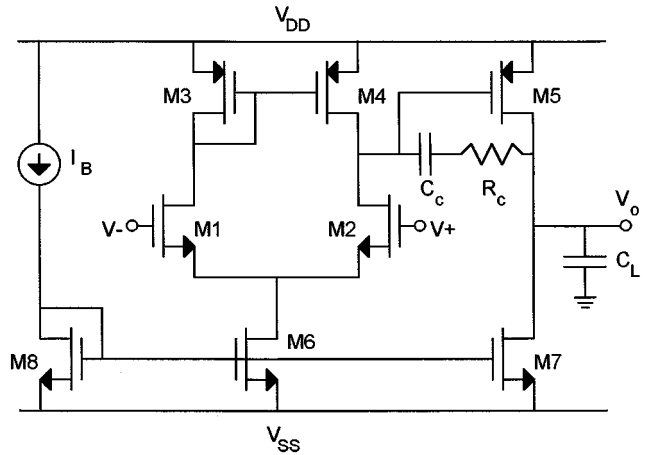


Fig. 2. Schematic of the OTA.

### II. PARAMETERS EXTRACTION

The frequency behavior of an open-loop amplifier is approximately defined by two real poles,<sup>1</sup>  $\omega_1$  and  $\omega_2$ , and a dc gain,  $a_0$

$$a(s) = \frac{a_0}{\left(1 + \frac{s}{\omega_1}\right)\left(1 + \frac{s}{\omega_2}\right)}. \quad (1)$$

Assuming a feedback factor,  $\beta$ , the closed-loop transfer function, which has usually complex conjugate poles, is

$$A_{CL}(s) = \frac{A_{CL}(0)}{1 + \frac{s}{\omega_0 Q} + \frac{s^2}{\omega_0^2}} \quad (2)$$

where  $\omega_0$  and  $Q$  which are the *pole frequency* and the *pole-Q factor*, respectively, are given by

$$\omega_0 = \sqrt{\omega_1 \omega_2 (1 + \beta a_0)} \quad (3)$$

$$Q = \frac{\omega_0}{\omega_1 + \omega_2} \quad (4)$$

and  $\beta a_0 \gg 1$ . Note that the term  $A_{CL}(0)$  is often reported to be equal to  $a_0/(1 + \beta a_0)$  and, in general, it is set by a ratio of passive elements (resistors or capacitors). Actually, although it depends not only on the

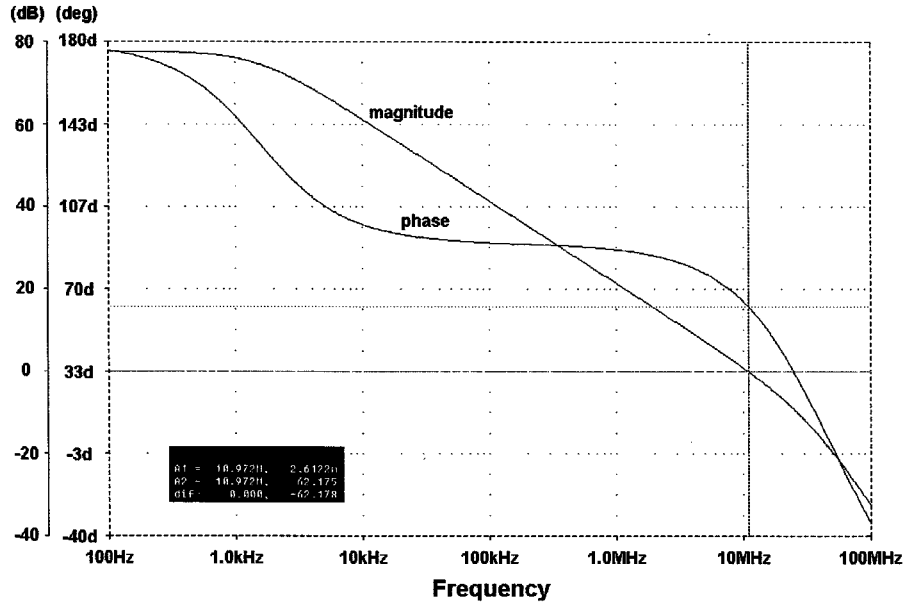
<sup>1</sup>Every amplifier with more than one pole or zeros close to the transition frequency can be well approximated in a form similar to that given in (1) since its behavior mainly depends on its phase margin and its transition frequency [14], [15].

TABLE I  
SIMULATED VALUES

$\omega_{GBW}=2\pi \cdot 1\text{MHz}$	$\omega_3/2\pi$	$\varphi_3$	$\omega_{TR}/2\pi$	$\varphi_{mR}$
$\varphi_m=60^\circ$	1.41 MHz	$-95.74^\circ$	890 kHz	$62.80^\circ$
$\varphi_m=50^\circ$	1.33 MHz	$-110.13^\circ$	824 kHz	$55.00^\circ$
$\varphi_m=45^\circ$	1.27 MHz	$-115.80^\circ$	787 kHz	$51.96^\circ$

TABLE II  
ESTIMATED VALUES

$\omega_{GBW}=2\pi \cdot 1\text{MHz}$	$\omega_T/2\pi$ eq. (14)	$\varphi_m$ eq. (15)	$\omega_{TR}/2\pi$ eq. (19)	$\varphi_{mR}$ eq. (21)
$\varphi_m=60^\circ$	1.00 MHz	$60.04^\circ$	892 kHz	$62.84^\circ$
$\varphi_m=50^\circ$	1.00 MHz	$49.86^\circ$	823 kHz	$55.28^\circ$
$\varphi_m=45^\circ$	997 kHz	$45.10^\circ$	785 kHz	$51.96^\circ$

Fig. 3. OTA open-loop magnitude and phase ( $\beta = 1$ ).

feedback network but also on the input terminal where the signal is applied (i.e., inverting or noninverting), its value has no influence in the following, as it will be simplified.

In stable feedback amplifiers the second pole,  $\omega_2$ , is at frequency much higher than that of the first pole,  $\omega_1$ , and higher than the unity-gain frequency,  $\omega_T$ , (i.e.,  $\omega_1 \ll \omega_2, \omega_2 \geq \omega_T$ ). Hence, the unity-gain frequency is almost equal to the gain-bandwidth product,  $\omega_{GBW} = \beta a_0 \omega_1$ , and defining the separation factor,  $K$ , as

$$K = \frac{\omega_2}{\omega_T} \cong \frac{\omega_2}{\omega_{GBW}} = \frac{\omega_2}{\beta a_0 \omega_1} = tg(\varphi_m) \quad (5)$$

relationship (2) can be expressed versus the phase margin,  $\varphi_m$ , and  $\omega_T$  [13]

$$A_{CL}(s) = \frac{A_{CL}(0)}{1 + \frac{s}{\omega_T} + \frac{s^2}{K\omega_T^2}}. \quad (6)$$

Let us define  $\omega_3$  the frequency where the gain of the closed-loop amplifier is reduced of 3 dB with respect to its dc value, and  $\varphi_3$  its corresponding phase. Thus

$$|A_{CL}(j\omega_3)| = \frac{A_{CL}(0)}{\sqrt{2}} \quad (7)$$

$$\tan(\varphi_3) = \frac{\text{Im}[A_{CL}(j\omega_3)]}{\text{Re}[A_{CL}(j\omega_3)]}. \quad (8)$$

Evaluating (6) for  $s = j\omega$  and substituting it in (7) and (8) we get

$$\left[1 - \frac{1}{K} \left(\frac{\omega_3}{\omega_T}\right)^2\right]^2 + \left(\frac{\omega_3}{\omega_T}\right)^2 = 2 \quad (9)$$

$$\tan(\varphi_3) = -\frac{\frac{\omega_3}{\omega_T}}{1 - \frac{1}{K} \left(\frac{\omega_3}{\omega_T}\right)^2}. \quad (10)$$

Solving (9) with respect  $1/K$

$$\frac{1}{K} = \frac{1 - \sqrt{2 - \left(\frac{\omega_3}{\omega_T}\right)^2}}{\left(\frac{\omega_3}{\omega_T}\right)^2} \quad (11)$$

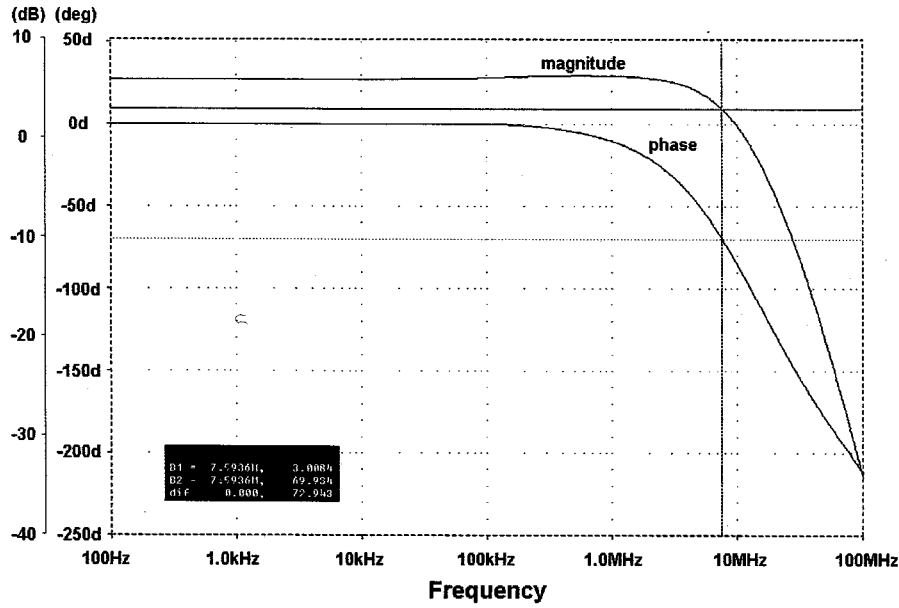
and substituting the value in (10), the 3-dB phase becomes

$$\tan(\varphi_3) = -\frac{\frac{\omega_3}{\omega_T}}{\sqrt{2 - \left(\frac{\omega_3}{\omega_T}\right)^2}}. \quad (12)$$

Solving relationship (12) for the ratio  $\omega_3/\omega_T$ , we get

$$\left(\frac{\omega_3}{\omega_T}\right)^2 = \frac{2tg^2(\varphi_3)}{1 + tg^2(\varphi_3)} = 2\sin^2(\varphi_3). \quad (13)$$



Fig. 5. OTA closed-loop magnitude and phase ( $\beta = 1/2$ ).TABLE III  
ERRORS BETWEEN SIMULATED AND ESTIMATED VALUES

$\omega_{3dB}=2\pi \cdot 1\text{MHz}$	$\omega_T/2\pi$ eq. (14)	$\varphi_m$ eq. (15)	$\omega_{TR}/2\pi$ eq. (19)	$\varphi_{mR}$ eq. (21)
$\varphi_m=60^\circ$	12.4 %	-4.4 %	0.22 %	0.06 %
$\varphi_m=50^\circ$	21.4 %	-9.3 %	-0.12 %	0.51 %
$\varphi_m=45^\circ$	27.1 %	-13.2 %	-0.25 %	0.00 %

TABLE IV  
COMPONENT OF THE OTA

Component	Value
M1, M2	100/2
M3, M4	50/2
M5	400/2
M6	50/2
M7	200/2
M8	50/2
$C_c$	5 pF
$R_c$	720 $\Omega$
$I_B$	50 $\mu\text{A}$
$V_{DD}=-V_{SS}$	2.5 V

A more realistic result can be found by means of (19) and (21) and, as the Appendix will show, this yield to a very precise result.

Finally, if a measure of open-loop parameters in unity-gain configuration is requested, (22) let us to determine them even if the closed-loop amplifier was not tested with a unitary feedback factor  $\beta$ .

#### IV. VALIDATION RESULTS

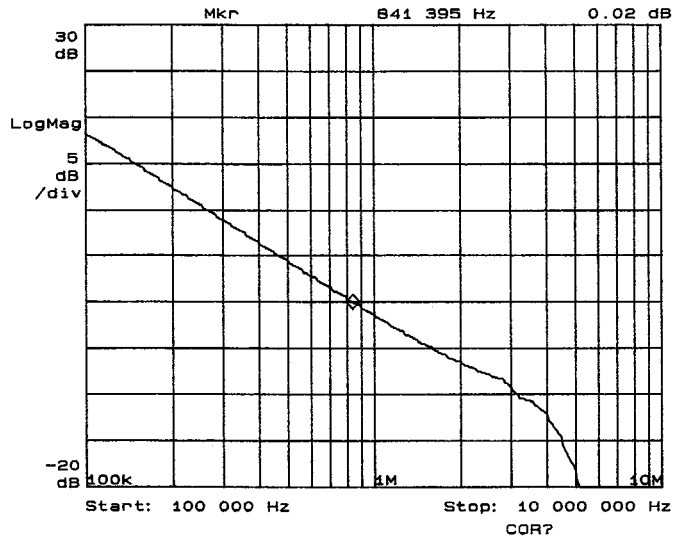
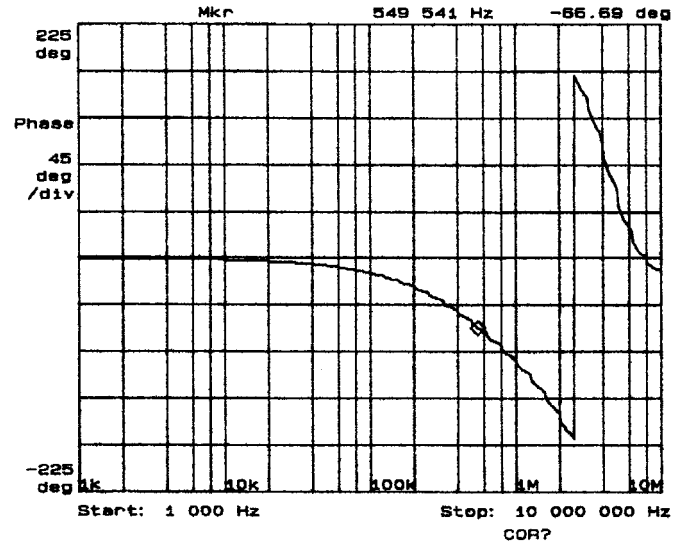
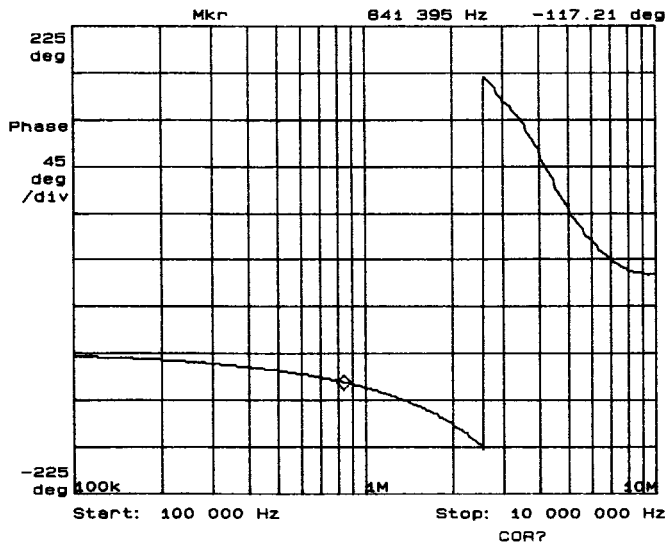
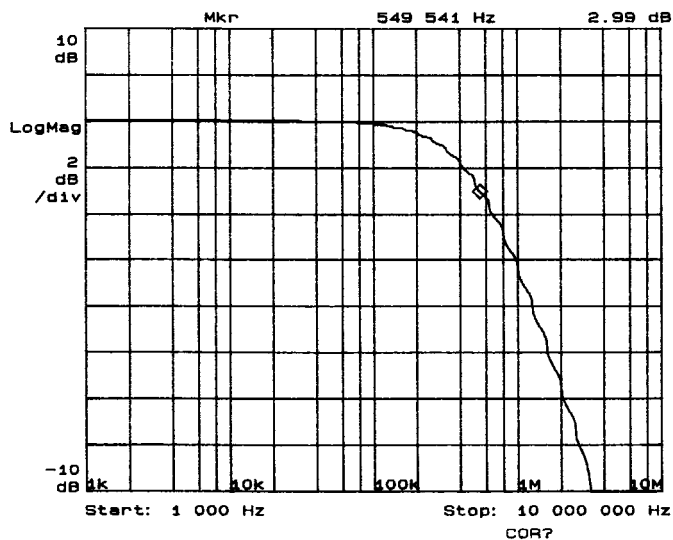
The proposed procedure has been extensively validated. In particular, in order to give a theoretical validation, the method is applied to the ideal case of a pure two poles amplifier and simulated results with estimated ones are compared, and the accuracy is analytically evaluated in the Appendix. It is again validated by means of simulations performed on an amplifier designed at transistor level. Finally, the approach is applied to a real  $\mu\text{A}-741$  and measured values are compared with estimated ones.

#### A. Simulation Results

An ideal two poles amplifier with a 90-dB open loop gain and a 1-MHz gain-bandwidth product was considered, and its second pole was set to give a phase margin equal to 60, 50, and 45 degrees. Spice simulation results, estimated values and related errors are summarized in Tables I–III, respectively. It is apparent that as expected the error given by (14) and (15) increases when the second pole is closer to the unity-gain frequency. However, relationship (19) and (21) give good accuracy regardless of the second pole position.

Then the approach has been applied to the two-stage CMOS OTA in Fig. 2, usually used in switched capacitor circuit [3], [4]. The amplifier is designed in a 1.2- $\mu\text{m}$  standard process with the transistor aspect ratios in Table IV. Setting a unitary feedback factor, open-loop and closed-loop gains are plotted in Figs. 3 and 4, respectively. In particular, when loaded with a 5-pF capacitor, the OTA has a unity-gain frequency and a phase margin equal to 10.97 MHz and  $62.17^\circ$ , respectively. Moreover, in unitary closed-loop configuration it has 3-dB frequency and a phase equal to 18.46 MHz and  $-104.75^\circ$ , respectively. By using this two values into relationship (14) and (15) we get estimated values of unity-gain frequency and a phase margin equal to 13.5 MHz and  $53.99^\circ$ , respectively, which means errors equal to 23.1% and  $-13.2\%$ , respectively. Of course a better agreement is found by using relationships (19) and (21) which give 11.48 MHz and  $58.27^\circ$  for  $\omega_{TR}/2\pi$  and  $\varphi_{mR}$ , respectively, which means errors equal to 4.6% and  $-6.3\%$ .

The same OTA has been analyzed in noninverting configuration with a 6-dB closed-loop gain ( $\beta = 1/2$ ). This gain, plotted in Fig. 5, shows a 3-dB cutoff frequency of 7.59 MHz and a phase  $\varphi_3$ , of  $-69.93^\circ$ . From relationships (14) and (15) we get for  $\omega_T/2\pi$  and  $\varphi_m$ , 5.72 MHz and  $73.74^\circ$ . The error with respect to the simulated values is of 4.3% and  $-3.9\%$ , respectively. On the other hand, by using (19) and (21) we get for  $\omega_{TR}/2\pi$  and  $\varphi_{mR}$ , 5.5 MHz and  $74.31^\circ$  which lead to errors

Fig. 6.  $\mu$ A-741 open loop magnitude ( $\beta = 1$ ).Fig. 9.  $\mu$ A-741 closed loop phase ( $\beta = 1/2$ ).Fig. 7.  $\mu$ A-741 open loop phase ( $\beta = 1$ ).Fig. 8.  $\mu$ A-741 closed loop magnitude ( $\beta = 1/2$ ).

with respect to the simulated values of 0.2% and  $-2.4\%$ , respectively. In both cases, all the equations give a highly accurate result since the second pole is less close to the real unity-gain frequency. Moreover, by using (22) the unity-gain frequency,  $\omega_{T1}/2\pi$ , and the phase margin,  $\varphi_{m1}$ , for the amplifier in unity-gain configuration, result 11.01 MHz and  $60.68^\circ$ , respectively, with an overall error lower than 2.5%.

### B. Experimental Results

The approach was also used in a real experimental test. In particular, a  $\mu$ A-741 operational amplifier has been tested. Direct measures of both its transition frequency and phase margin are shown in Figs. 6 and 7, respectively, yielding to a value of 841 kHz and  $62.79^\circ$ .

Indirect measure of these values is performed by characterizing the amplifier in closed loop configuration with a closed loop gain of 6-dB (i.e.,  $\beta = 1/2$ ). Specifically, the feedback network was implemented with two equal resistors and the input signal was applied to the noninverting terminal. Figs. 8 and 9 show measured values for  $\omega_3/2\pi$  and  $\varphi_3$  which results to be 549.5 kHz and  $-66.69^\circ$ , respectively. By applying relationships (19) and (21) we get for  $\omega_{TR}/2\pi$  and  $\varphi_{mR}$ , 410.2 kHz and  $75.8^\circ$ , respectively. Feeding them in (22) we get for the estimated unity-gain frequency and the phase margin the values of 824.2 kHz and of  $63.15^\circ$  with an error of  $-2\%$  and  $0.57\%$ , respectively.

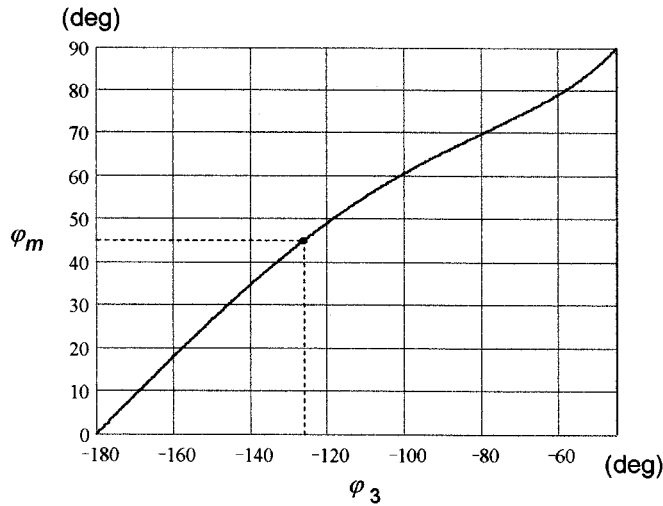
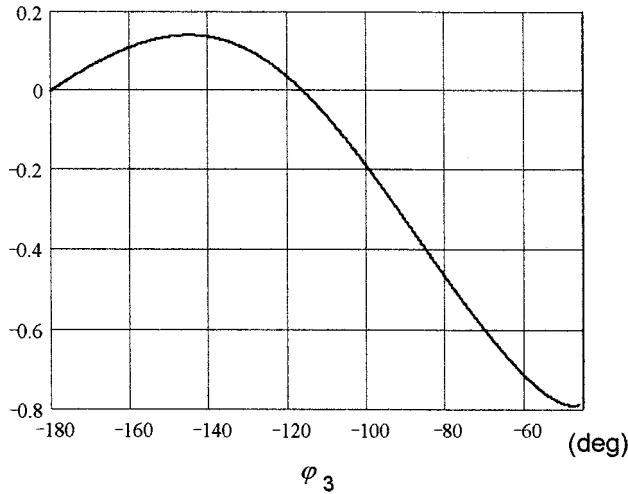
## V. CONCLUSION

A novel method to evaluate the main open-loop parameters of a two-poles feedback amplifier is given. In particular, it is useful to achieve the unity gain frequency and the phase margin of an open-loop amplifier measuring only its closed-loop 3-dB cutoff frequency and the corresponding phase.

The approach was validated using both simulation and experimental results and in any case a high accuracy was demonstrated.

## APPENDIX

In order to analytically evaluate the accuracy of equations (19) and (21) the sensitivity of both the equations with respect to  $\omega_3$  and  $\varphi_3$  is evaluated in the range of interest. In particular for a two-poles feedback amplifier, the phase  $\varphi_3$  must lie in the range between  $-180^\circ$  and  $-45^\circ$  (these values represent the case when the closed-loop system realize a double integrator or a single-pole system, respectively). A plot of  $\varphi_{mR}$  versus  $\varphi_3$ , obtained by substituting in (21) the value of  $K$  given in

Fig. 10. Phase margin versus cutoff phase  $\varphi_3$ .Fig. 11.  $\omega_{TR}$  sensitivity versus cutoff phase  $\varphi_3$ .

(15), is shown in Fig. 10. It is worth noting that this function grows monotonically and that, in practical cases (that is for well-compensated circuit with a phase margin greater than  $45^\circ$ ), the lower bound of  $\varphi_3$  is reduced to about  $-126.3^\circ$ .

The percentage variation of  $\omega_{TR}$  with respect to percentage variations of both  $\omega_3$  and  $\varphi_3$  can be expressed as

$$\frac{\Delta \omega_{TR}}{\omega_{TR}} = S_{\omega_3}^{\omega_{TR}} \cdot \frac{\Delta \omega_3}{\omega_3} + S_{\varphi_3}^{\omega_{TR}} \cdot \frac{\Delta \varphi_3}{\varphi_3}. \quad (\text{A.1})$$

By substituting (14) in (19), it can be simply shown that  $S_{\omega_3}^{\omega_{TR}} = 1$ . The sensitivity  $S_{\varphi_3}^{\omega_{TR}}$  versus  $\varphi_3$  is depicted in Fig. 11, and it can be shown that  $|S_{\varphi_3}^{\omega_{TR}}| \leq \pi/4$ . As a consequence, errors in the measurement of both  $\omega_3$  and  $\varphi_3$  do not affect the estimation of the transition frequency appreciably.

The phase margin only depends on  $\varphi_3$  and its percentage error can be expressed as

$$\frac{\Delta \varphi_m}{\varphi_m} = S_{\varphi_3}^{\varphi_m} \cdot \frac{\Delta \varphi_3}{\varphi_3}. \quad (\text{A.2})$$

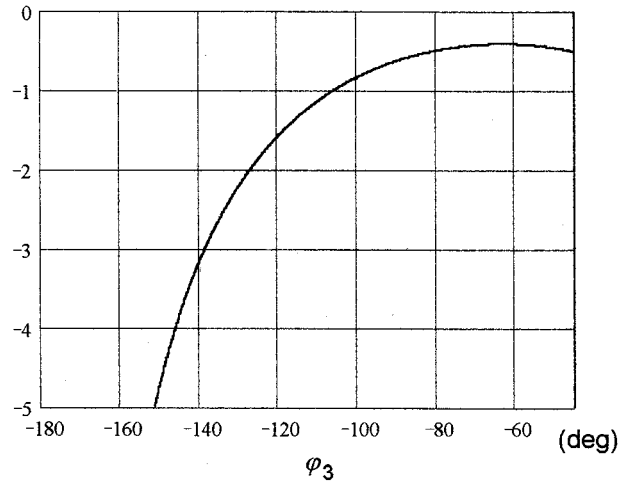
Fig. 12.  $\varphi_{mR}$  sensitivity versus cutoff phase  $\varphi_3$ .

Fig. 12 depicts the sensitivity  $S_{\varphi_3}^{\varphi_{mR}}$  versus  $\varphi_3$ . This function decreases drastically when  $\varphi_3$  approaches  $-180^\circ$  thus yielding a high sensitivity to measurement errors. However, in the practical case of a well-compensated circuit, the worst sensitivity is close to  $-2$  thus giving a sufficient accuracy.

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