

COMPUTER ENGINEERING

DLCA ODD SEM 2021-22/EXPERIMENT 4

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	Aim: - Study a filp flop IC Theory: - A flip flop is an electronic circuit with two stable states that can be used to stone leinary data. The stored data can be changed by applying varying inputs. Flip flop and latches are fundamental building blocks of digital electronic system used in compulses, communication and many other types of stems. Both are used as data Storage elements. It is the basic storage element in logic unit.					
I. SR flip flop: - -> This simple flip flop circuit has a set input (s) and reset input (R). In this system when you set 'S' as active, the output 'B' would lie high and 'Q' will be low. -> Once the inputs are established, the wiving of the circuit is maintained until "S" or "R" go high or power is turned off.						
		0 0 0 1	R 0 1 1 1 1	0 0 1 ∞	1 1 0 0	





