

Instructions to Candidate

1. All questions are compulsory.
2. Neat diagrams must be drawn wherever necessary.
3. Figures to the right indicate full marks.

Q.N.	CO No	BT No*		Max marks
Q. 1	CO1	2 3	A. Attempt any one Any One Computer Arithmetic , Booths algo B. Solve Any one of the following Division , Floating point number	10
Q. 2	CO 2	4 2	A. Attempt any two of the following RISC CISC , Advances in Pipelining super architectures , Address bus, data bus B. instruction cycle / pipelining	10
Q. 3	CO3	3 3,4	A. complete instruction execution B. Solve any ONE of the following Control Units	10
Q. 4.	CO4	1,4	A. Solve any TWO of the following (3 Marks each) DMA/IO mapping techniques / Types of memories B. Mapping techniques / Virtual memory	12
Q. 5.	CO 5	4 2	A. Multicore Architecture B. Flynn's Classification	10
Q. 6.	CO 1-5	1-5	Solve any FOUR of the following (TWO Mark each) Number conversion ,MAR and MDR , Flynn's Classification , Cache coherence , locality of reference interrupt processing etc	8

CO Statements:

- CO1: To Demonstrate Computer Architecture concepts along with Computer arithmetic and various related algorithms
- CO2: Compare design of modern processors with reference to Instruction pipeline .
- CO3: Illustrate the micro-operations sequencing in control unit and describe I/O organization
- CO4: Describe concepts related to memory Hierarchy & IO organization
- CO5 : Classify and state need and design of modern processor architecture.

***Blooms Taxonomy (BT) Level No:**

1. Remembering; 2. Understanding; 3. Applying; 4. Analyzing; 5. Evaluating; 6. Creating