

Example:

consider a machine with byte addressable main memory of size 4GB, block size of 4KB and Associative mapped cache having 1MB size.

- find (1) P.A. Split  
(2) Tag directory size.

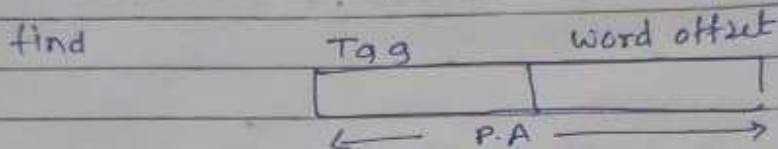
Soln:

Given

Size of M.M = 4GB

Size of block = 4KB

Size of C.M = 1MB.



Step 1: Number of bits in Physical Address

$$P.A = \log_2 (\text{Size of M.M})$$

$$= \log_2 (4GB)$$

$$= \log_2 (2^2 \times 2^{30})$$

$$= \log_2 (2^{32})$$

$$\boxed{P.A = 32 \text{ bit}}$$

Step 2:

$$\text{word offset} = \log_2 (\text{block size})$$

$$= \log_2 (4KB)$$

$$= \log_2 (2^2 \times 2^{10})$$

$$= \log_2 (2^{12})$$

$$\boxed{\text{word offset} = 12 \text{ bit}}$$

Step 3:

$$\begin{aligned}\text{tag bit} &= \text{PA bit} - \text{word offset bit} \\ &= 32 - 12\end{aligned}$$

$$\boxed{\text{tag} = 20}$$

OR

$$\text{No. of block in M.M} = \frac{\text{Size of M.M}}{\text{Size of block}}$$

$$\begin{aligned}&= \frac{4GB}{4KB} \\ &= \frac{2^9 \times 2^{30}}{2^2 \times 2^{10}} \\ &= \frac{2^{32}}{2^{12}} \\ &= 2^{20}\end{aligned}$$

$$\text{Tag bit} = \log_2 (\text{No. of Block in M.M}).$$

$$= \log_2 (2^{20})$$

$$\boxed{\text{tag} = 20}$$

$$\boxed{20 \mid 12}$$

← 32 →

Step 4: Tag directory size = No. of lines in cache  
\* No. of bits.  
intag

$$\text{No. of cache lines} = \frac{\text{Size of C.M}}{\text{Size of block}}$$

$$= \frac{1MB}{4KB}$$

$$= \frac{2^{20}}{2^2 \times 2^{10}}$$

$$= \frac{2^{20}}{2^{12}}$$

$$\boxed{\text{No. of lines} = 2^8}$$

$$\text{Tag directory size} = 2^8 \times 20$$

$$= 5120 \text{ bits}$$