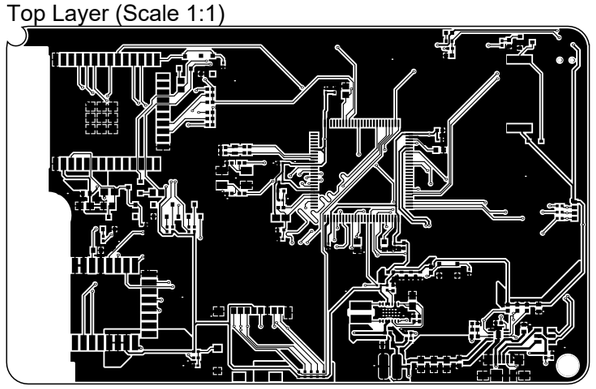
REV STATUS OF SHEETS | REV | SHEET

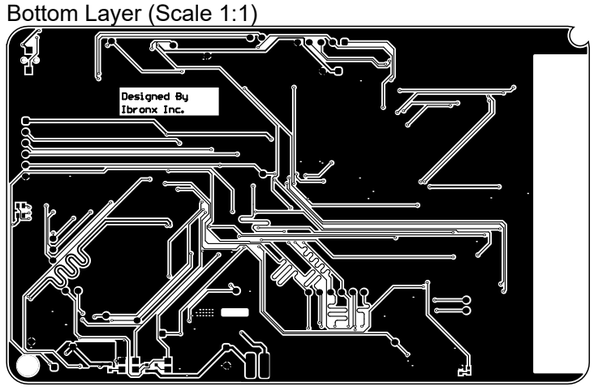
REVISIONS

| ZONE | REV | DESCRIPTION | DATE | APPROVED |
|---|---|---|---|---|
| | | | | |
| | | | | |
| | | | | |
| | | | | |

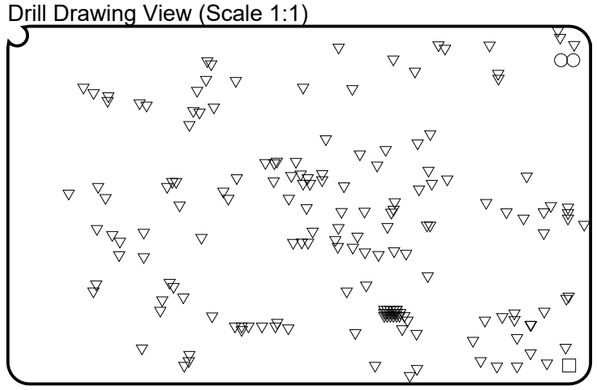
Top Layer (Scale 1:1)



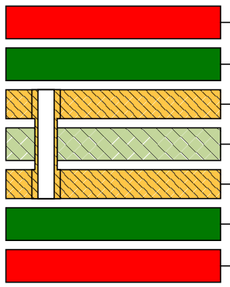
Bottom Layer (Scale 1:1)




Drill Drawing View (Scale 1:1)



Layer Stack Legend



| Material | Layer | Thickness | Dielectric Material | Type | Gerber |
|---|---|---|---|---|---|
| | Top Overlay | | | Legend | GTO |
| Surface Material | Top Solder | 0.01mm | Solder Resist | Solder Mask | GTS |
| Copper | Top Layer | 0.04mm | | Signal | GTL |
| | | *0.32mm* | *FR-4* | *Dielectric* | |
| Copper | Bottom Layer | 0.04mm | | Signal | GBL |
| Surface Material | Bottom Solder | 0.01mm | Solder Resist | Solder Mask | GBS |
| | Bottom Overlay | | | Legend | GBO |

Total thickness: 0.41mm

Drill Table

| Symbol | Count | Hole Size | Plated | Hole Tolerance |
|---|---|---|---|---|
| ▽ | 168 | 0.20mm | Plated | |
| ○ | 2 | 0.70mm | Non-Plated | |
| □ | 1 | 3.40mm | Plated | |
| | 171 Total | | | |

Transmission Line Structure Table

| Impedance Id | Transmission Line | Target Impedance | Calculated Impedance | Trace layer | Wide Trace Width | Narrow Trace Width | Gap | Reference layers | Substack |
|---|---|---|---|---|---|---|---|---|---|
| 1 | Edge-Coupled Coated Microstrip | 90 | 89.99 | Top Layer | 0.28mm | 0.28mm | 0.13mm | Bottom Layer | Board Layer Stack |
| 2 | Edge-Coupled Coated Microstrip | 90 | 89.99 | Bottom Layer | 0.28mm | 0.28mm | 0.13mm | Top Layer | Board Layer Stack |

Notes:

1. FINISH: ENIG PER IPC-4552
2. ALL HOLES FINISHED SIZE AS SHOWN
3. 

PART NO: =PCB_PART_NUMBER

| APPROVALS | DATE |
|---|---|
| ENGINEER: =PCB_ENGINEER | =PCB_ENGINEE |
| DESIGNER: =PCB_DESIGNER | =PCB_DESIGNE |
| CHECKER: =PCB_CHECKER | =PCB_CHECKE |

Reference Documents

BOM DOC: =DOC_NO_BOM
ASSY DOC: =DOC_NO_FAB_DWG
SCH DOC: =DOC_NO_SCH_DWG
PCB DOC: =PCB_DWG_NO

NEXT ASSY | USED ON
APPLICATION

THIRD ANGLE PROJECTION

Altium™

=Address1
=Address2
=Address3
=Address4

DESIGN ITEM: .Item
DESIGN ITEM REVISION: .ItemRevision
TITLE: =PCB_TITLE_1 =PCB_TITLE_2
SIZE: A3 | CAGE CODE: =CAGE_CO | DWG NO: | REV:
SCALE: | FILE NAME: Fabrication.PCBDwf | SHEET: 1 OF 1

.lt

DWG NO: =DOC_NO_ASSY_DWG REV: .lte